

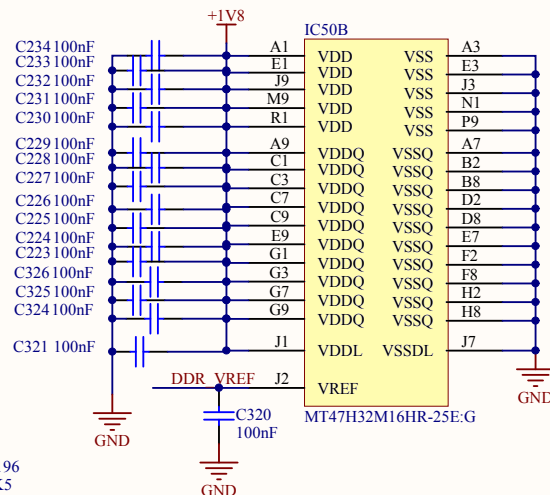
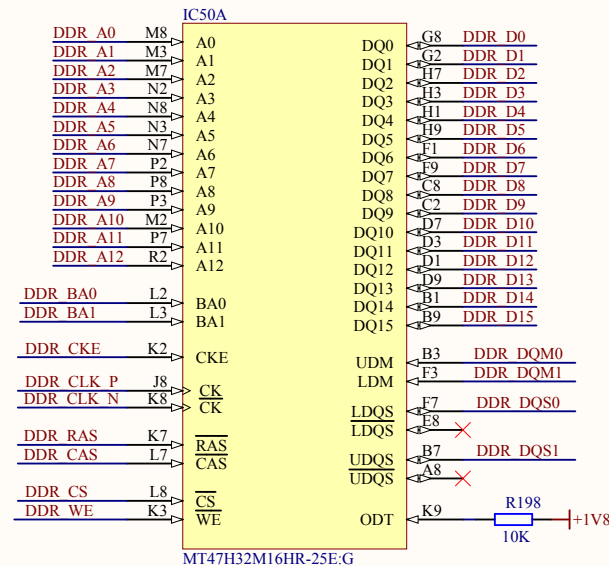
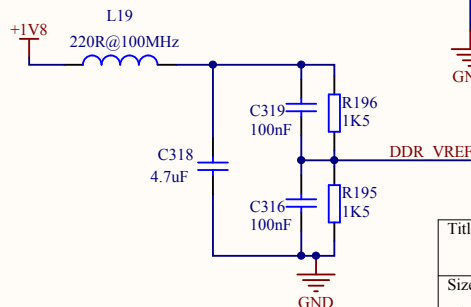
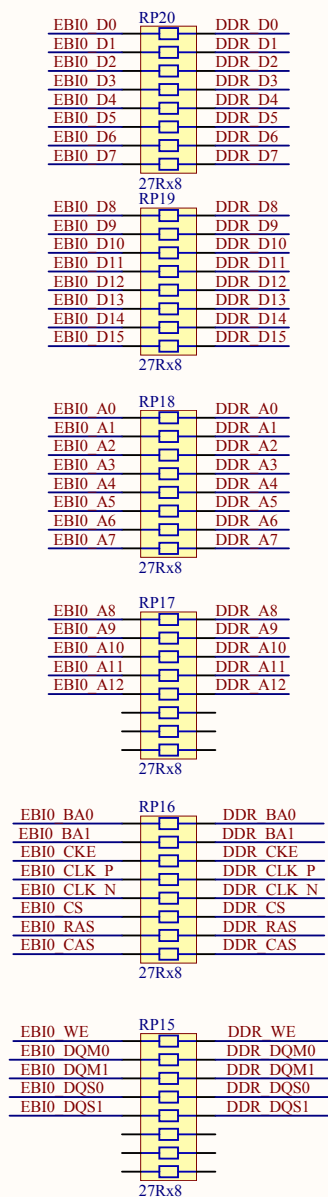
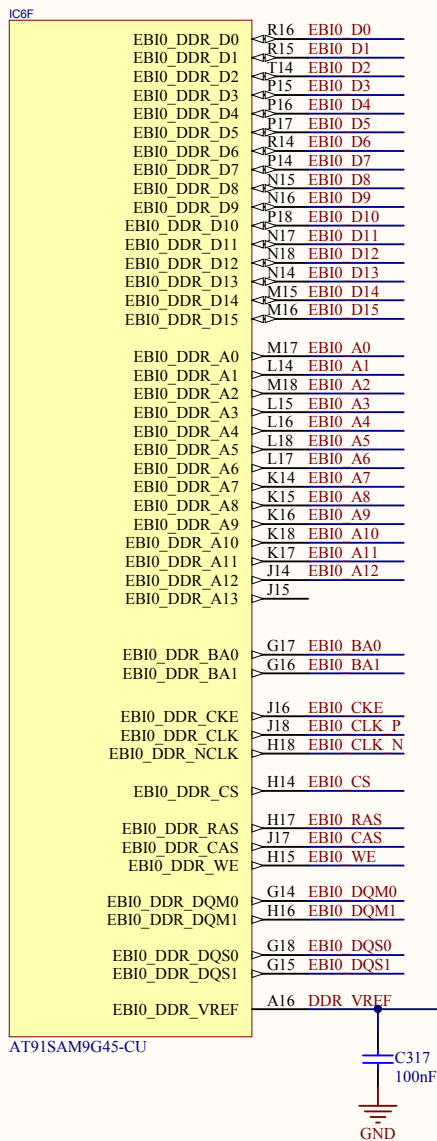
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<b>Sheet:</b> External 100Mbps Ethernet PHY & magnetics	
<b>Version:</b> 1.1.0	<b>Date:</b> 2009/11/12
<b>Author:</b> Tomasz Wlowski	<b>License:</b> Open Hardware License (OHL)
<b>Company:</b> CERN BE-CO-HT	

1

2

3

4



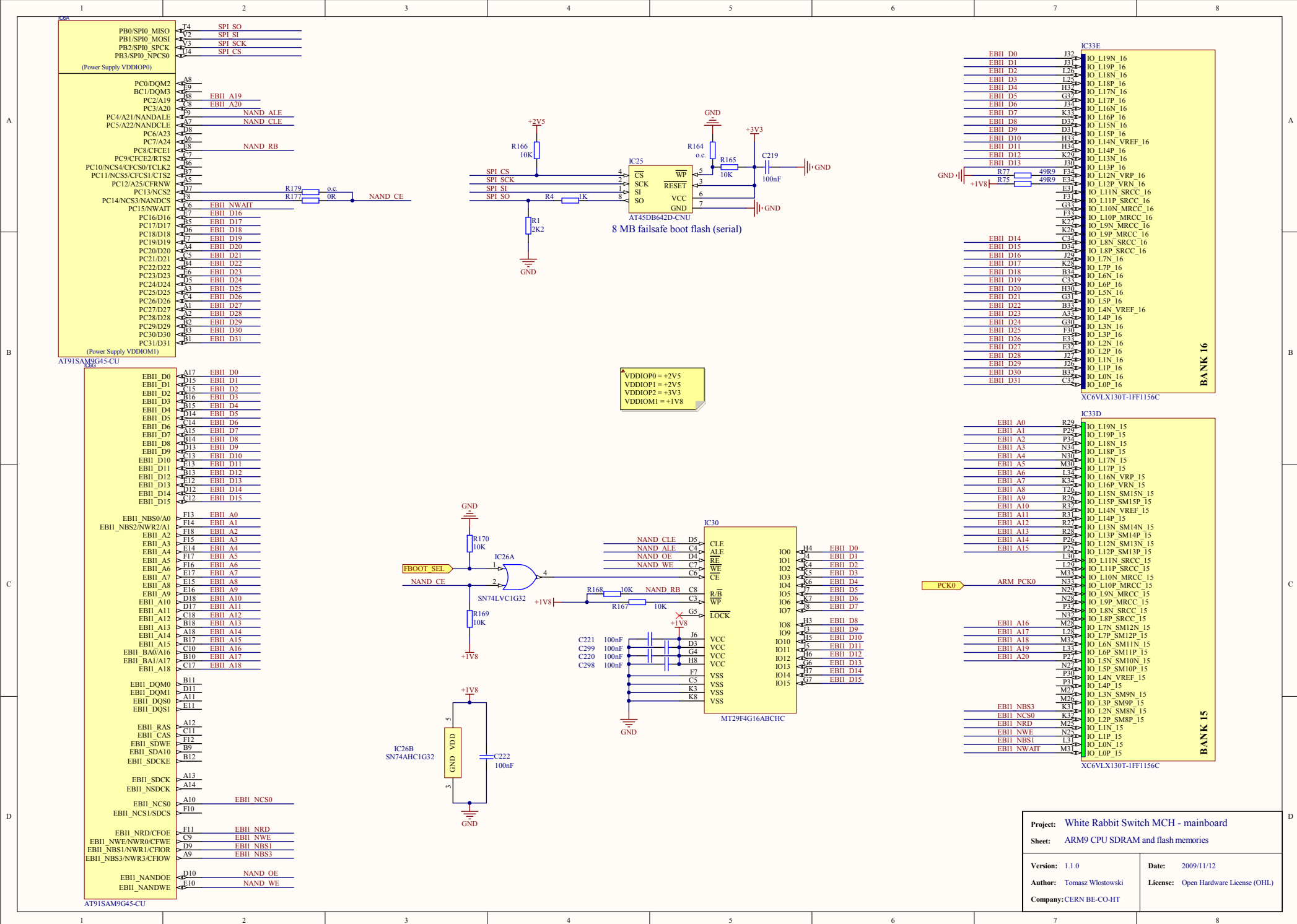
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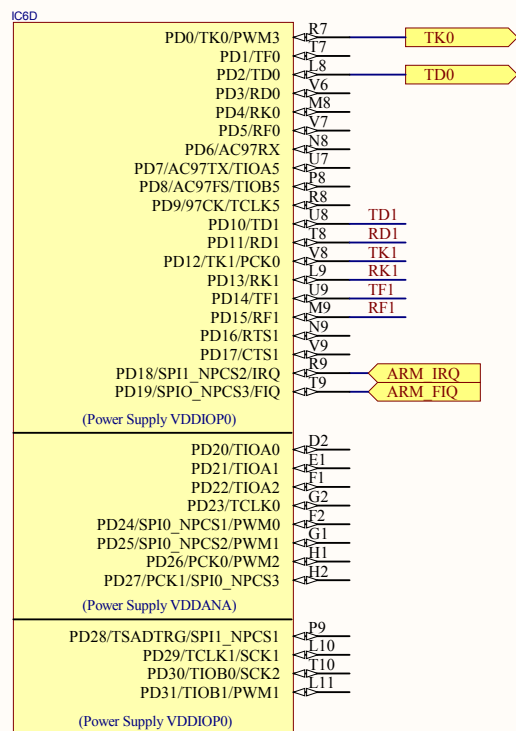
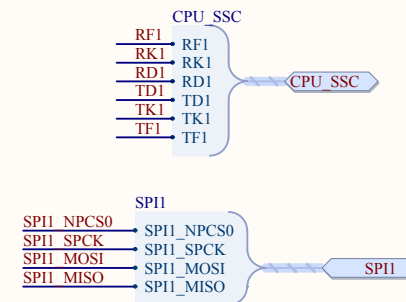
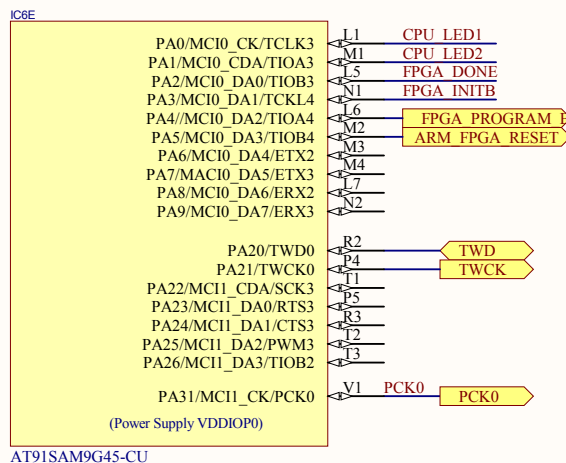
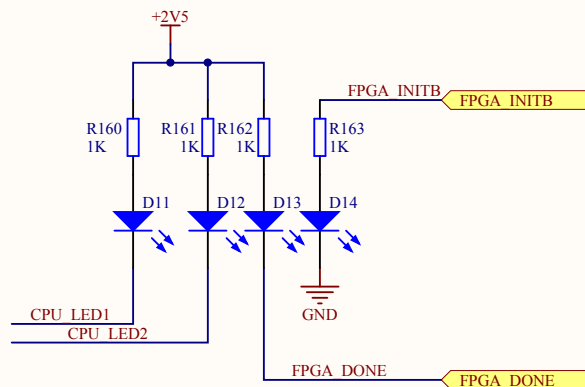
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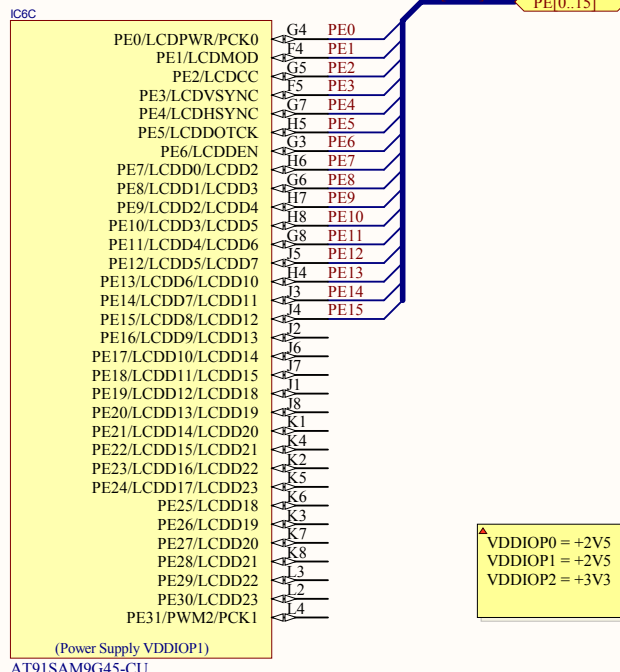
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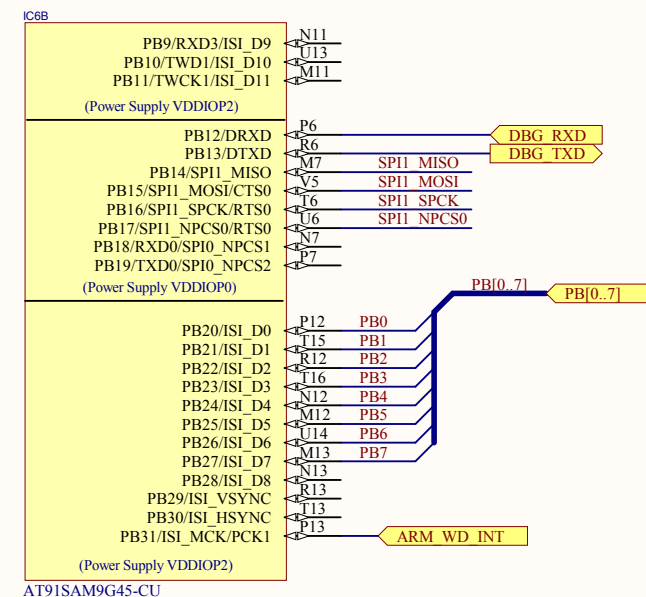


AT91SAM9G45-CU



AT91SAM9G45-CU

VDDIOP0 = +2V5  
VDDIOP1 = +2V5  
VDDIOP2 = +3V3



AT91SAM9G45-CU

## White Rabbit Switch MCH - mainboard

### ARM9 CPU I/O ports, busses and power

#### Project:

#### Sheet:

Tomasz Wlostowski

#### Version:

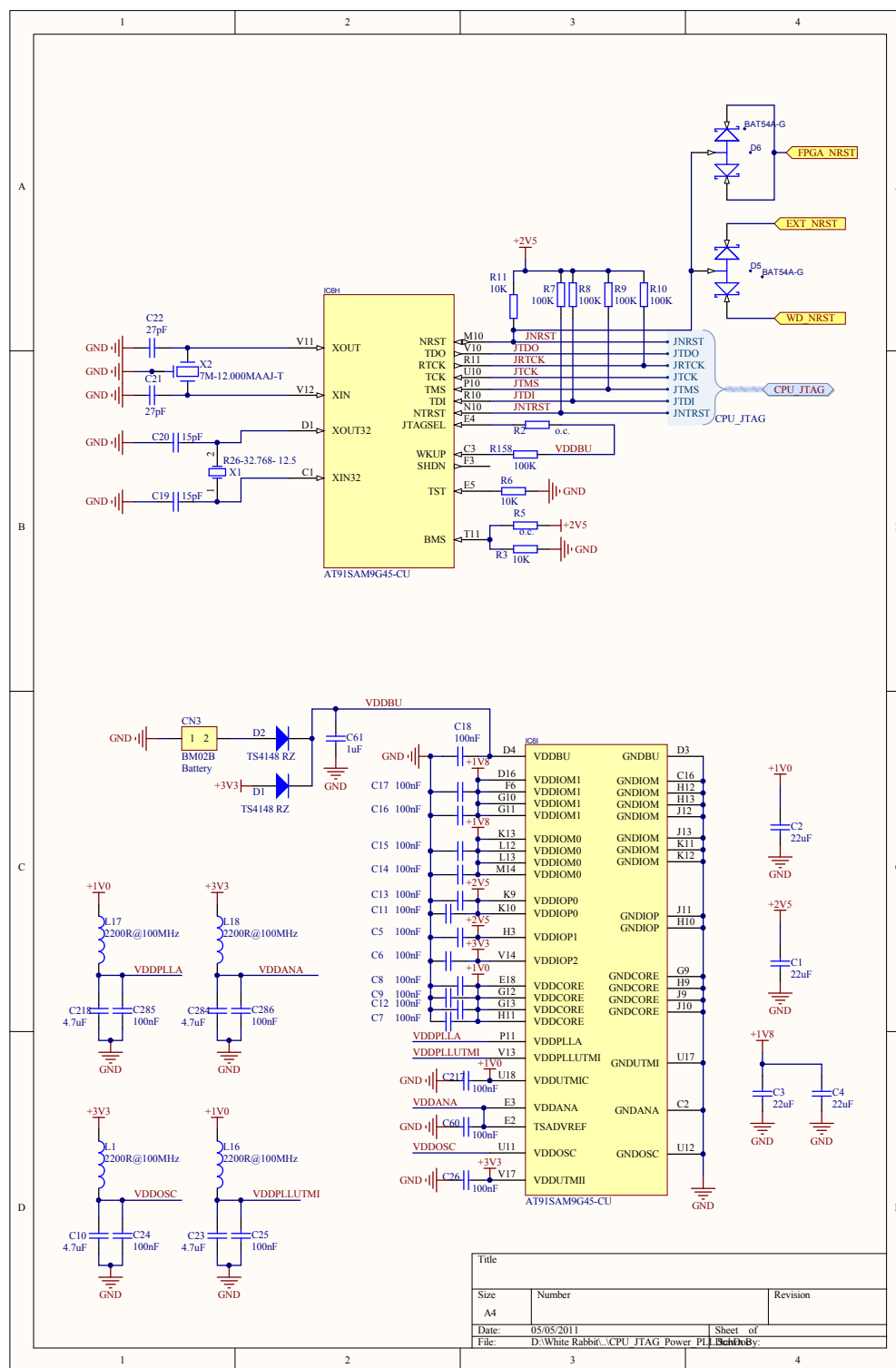
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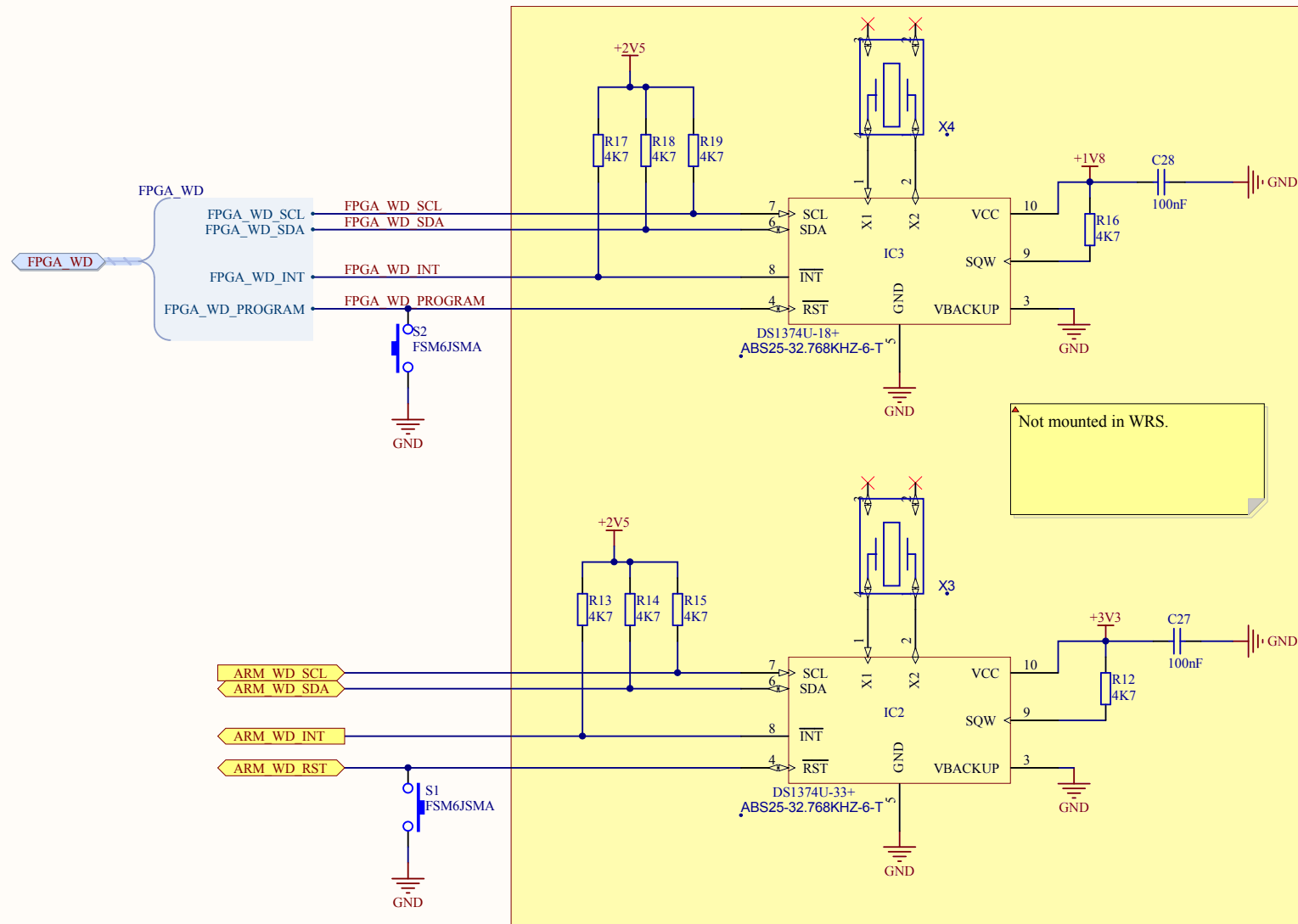
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Date: 2009/11/12

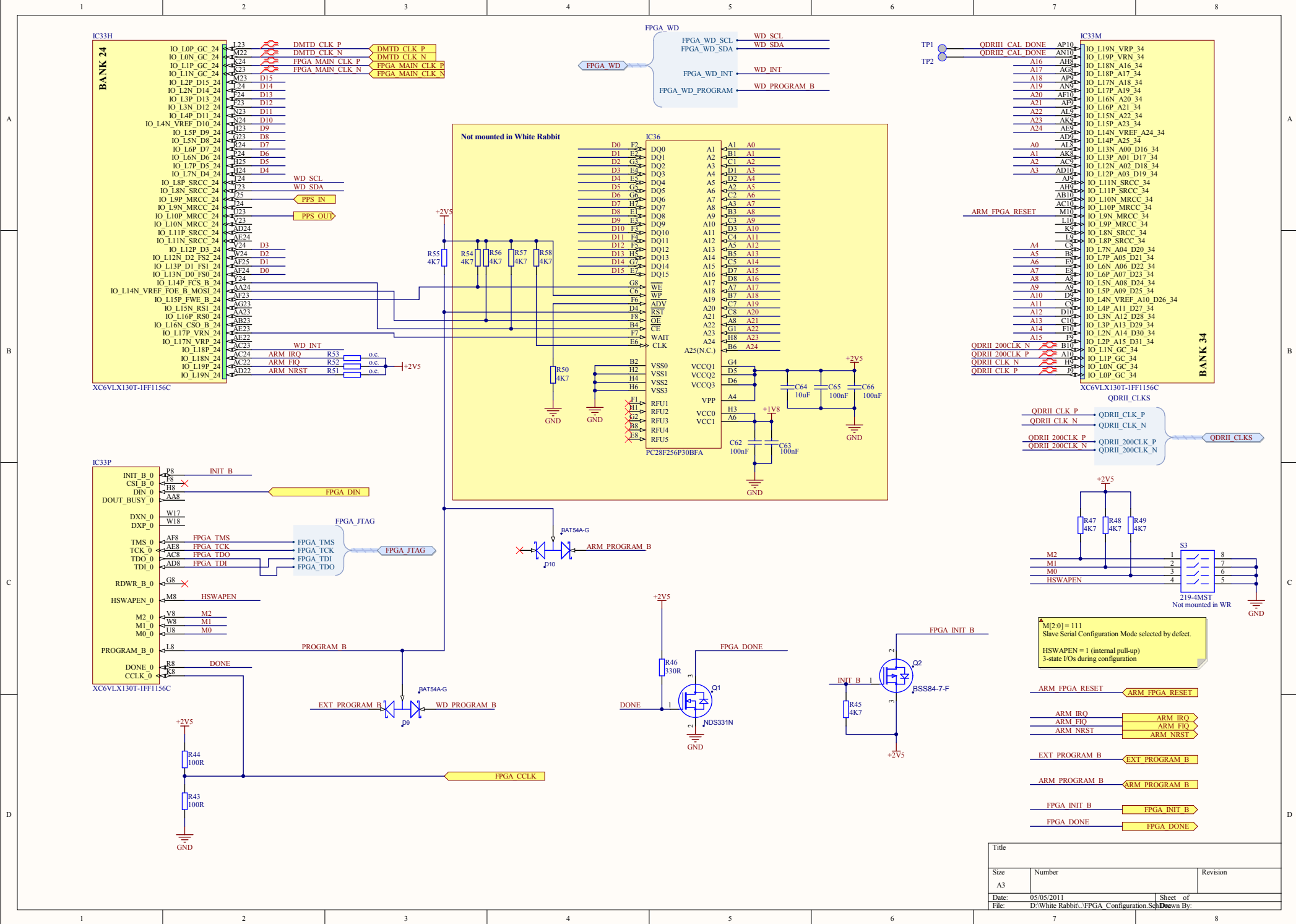
License: Open Hardware License (OHL)

#### Company:





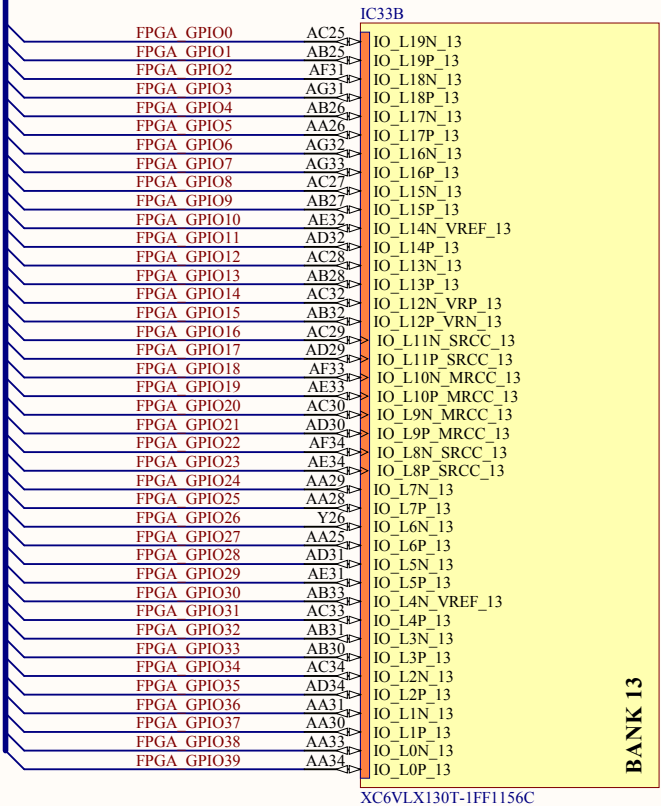
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FPGA\_GPIO[0..39]

FPGA\_GPIO[0..39]

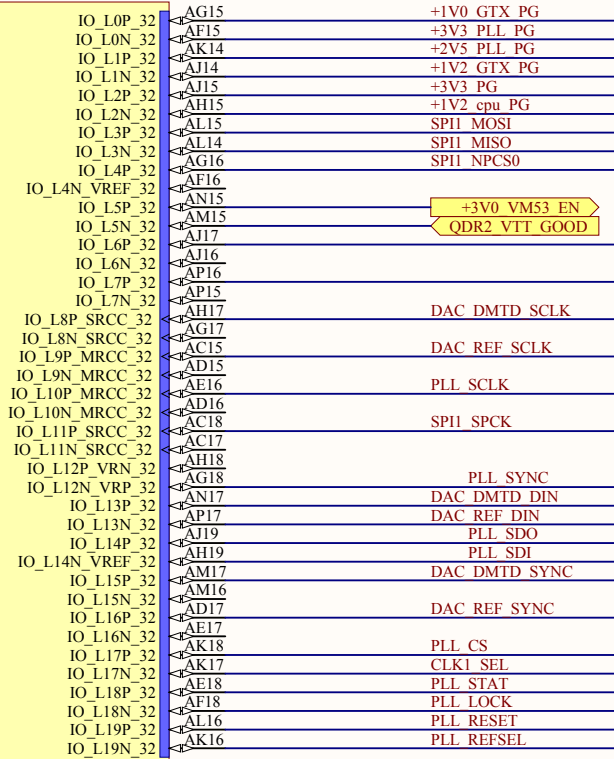


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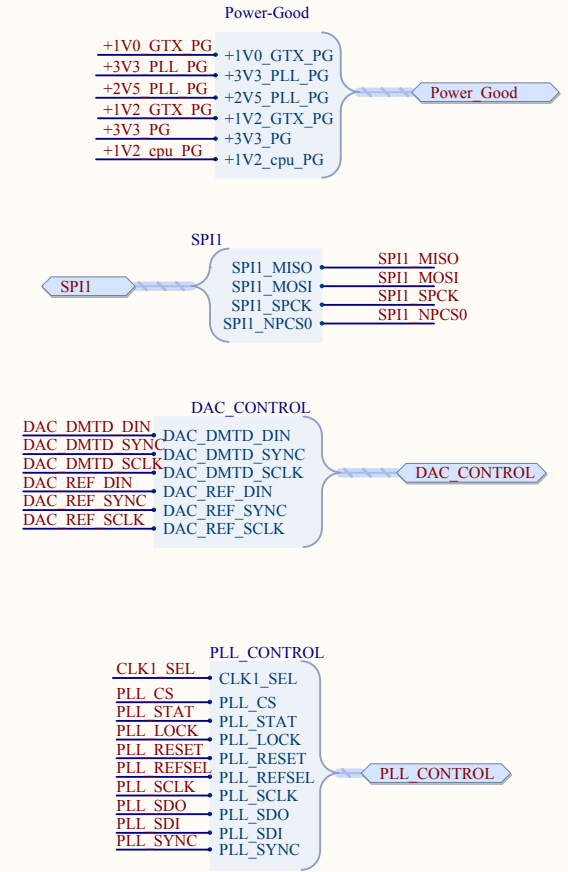
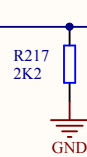


IC33K

BANK 32



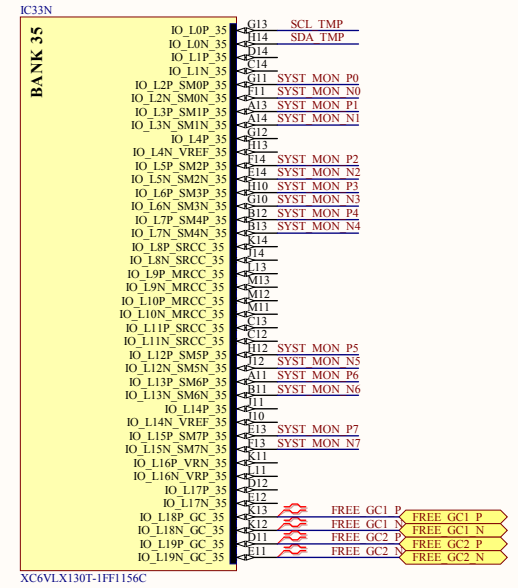
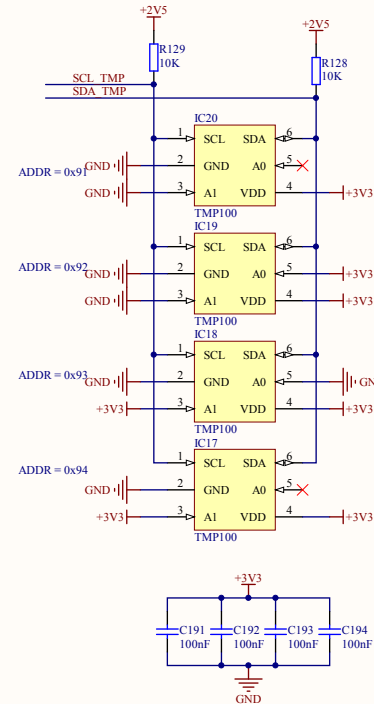
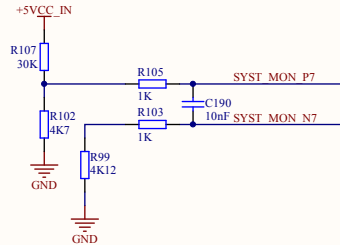
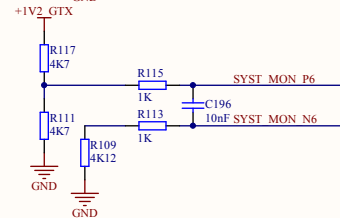
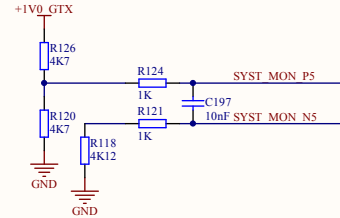
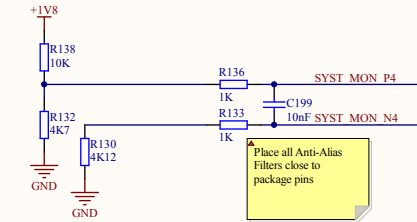
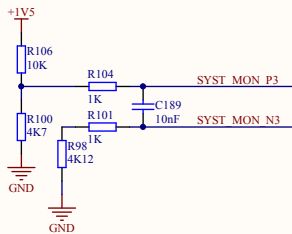
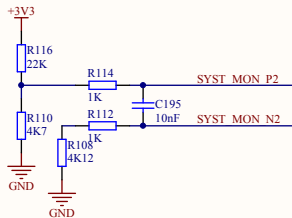
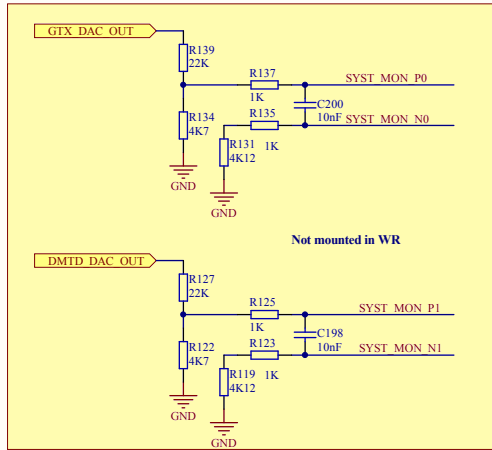
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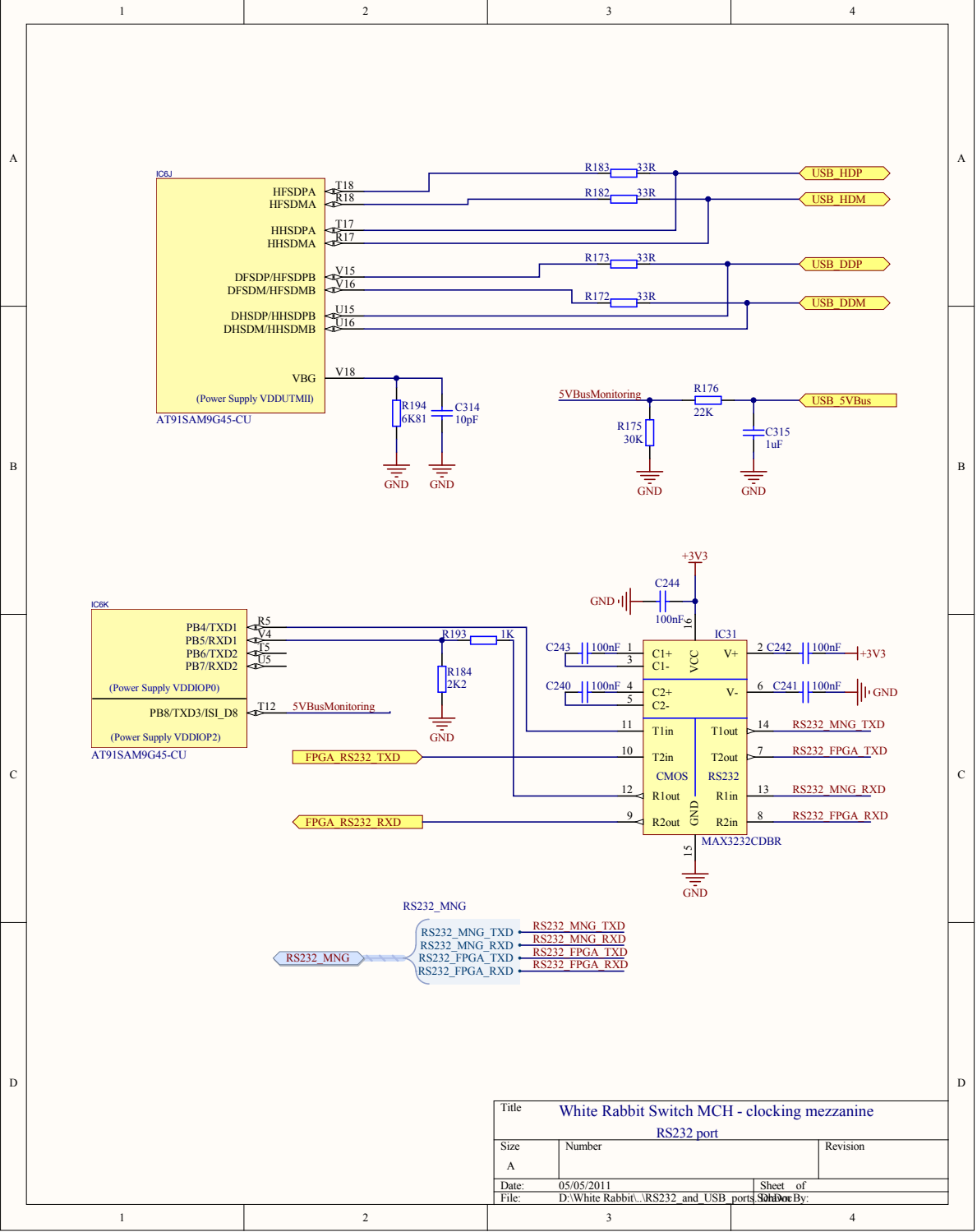
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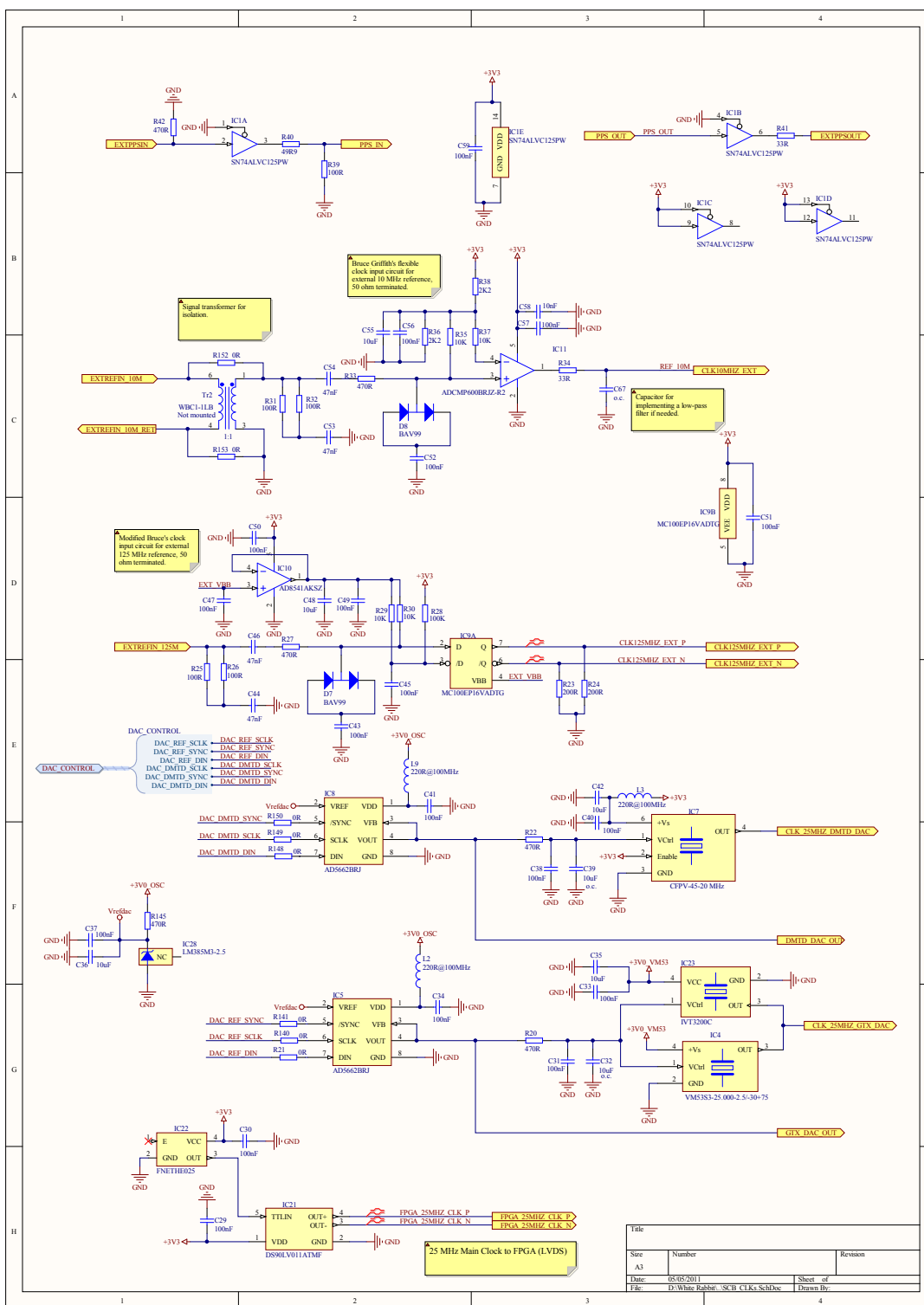


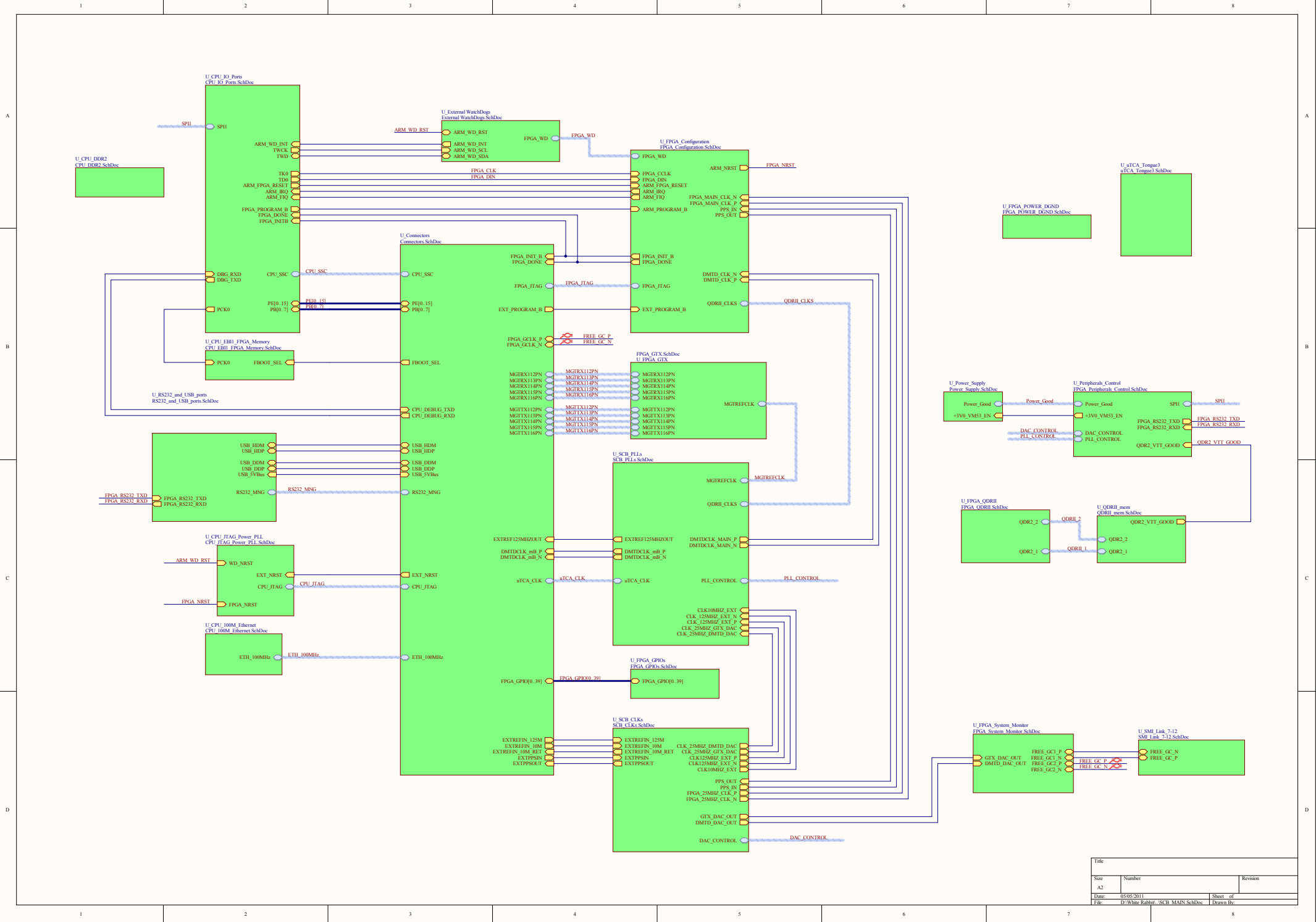






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IC33A

BANK 12

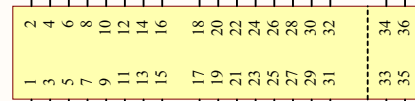
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IO\_L3N\_12  
IO\_L4P\_12  
IO\_L4N\_VREF\_12  
IO\_L5P\_12  
IO\_L5N\_12  
IO\_L6P\_12  
IO\_L6N\_12  
IO\_L7P\_12  
IO\_L7N\_12  
IO\_L8P\_SRCC\_12  
IO\_L8N\_SRCC\_12  
IO\_L9P\_MRCC\_12  
IO\_L9N\_MRCC\_12  
IO\_L10P\_MRCC\_12  
IO\_L10N\_MRCC\_12  
IO\_L11P\_SRCC\_12  
IO\_L11N\_SRCC\_12  
IO\_L12P\_VRN\_12  
IO\_L12N\_VRP\_12  
IO\_L13P\_12  
IO\_L13N\_12  
IO\_L14P\_12  
IO\_L14N\_VREF\_12  
IO\_L15P\_12  
IO\_L15N\_12  
IO\_L16P\_12  
IO\_L16N\_12  
IO\_L17P\_12  
IO\_L17N\_12  
IO\_L18P\_12  
IO\_L18N\_12  
IO\_L19P\_12  
IO\_L19N\_12

AD25  
AD26  
AE27  
AD27  
AH33  
AH32  
AE28  
AE29  
AJ34  
AH34  
AF28  
AF29  
AJ34  
AK34  
AH29  
AH30  
AN33  
AN34  
AG27  
AG28  
AF30  
AG30  
AF26  
AE26  
AJ31  
AJ32  
AJ29  
AJ30  
AK33  
AK32  
AL31  
AK31  
AM33  
AL33  
AN32  
AM32  
AP32  
AP33  
AL30  
AM31

TxFD12 P  
TxFD12 N  
TxFD11 P  
TxFD11 N  
TxFD10 P  
TxFD10 N  
TxFD9 P  
TxFD9 N  
TxFD8 P  
TxFD8 N  
TxFD7 P  
TxFD7 N  
  
FREE SRCC P  
FREE SRCC N  
  
FREE MRCC P  
FREE MRCC N  
  
RxFD12 P  
RxFD12 N  
RxFD11 P  
RxFD11 N  
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RxFD7 P  
RxFD7 N

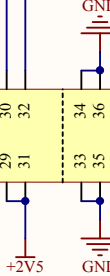
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FREE MRCC P  
FREE MRCC N



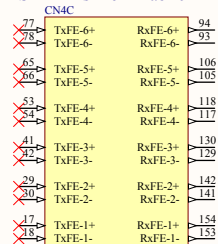
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QSS-016-01-L-D-DP

FREE GC P  
FREE GC N  
FREE GC P  
FREE GC N

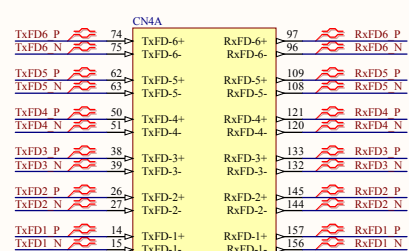


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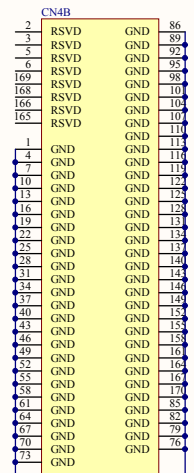
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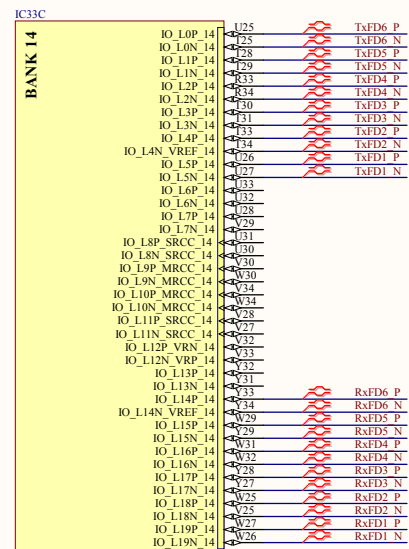
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UTCA\_MCH\_TONGUE3



UTCA\_MCH\_TONGUE3



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