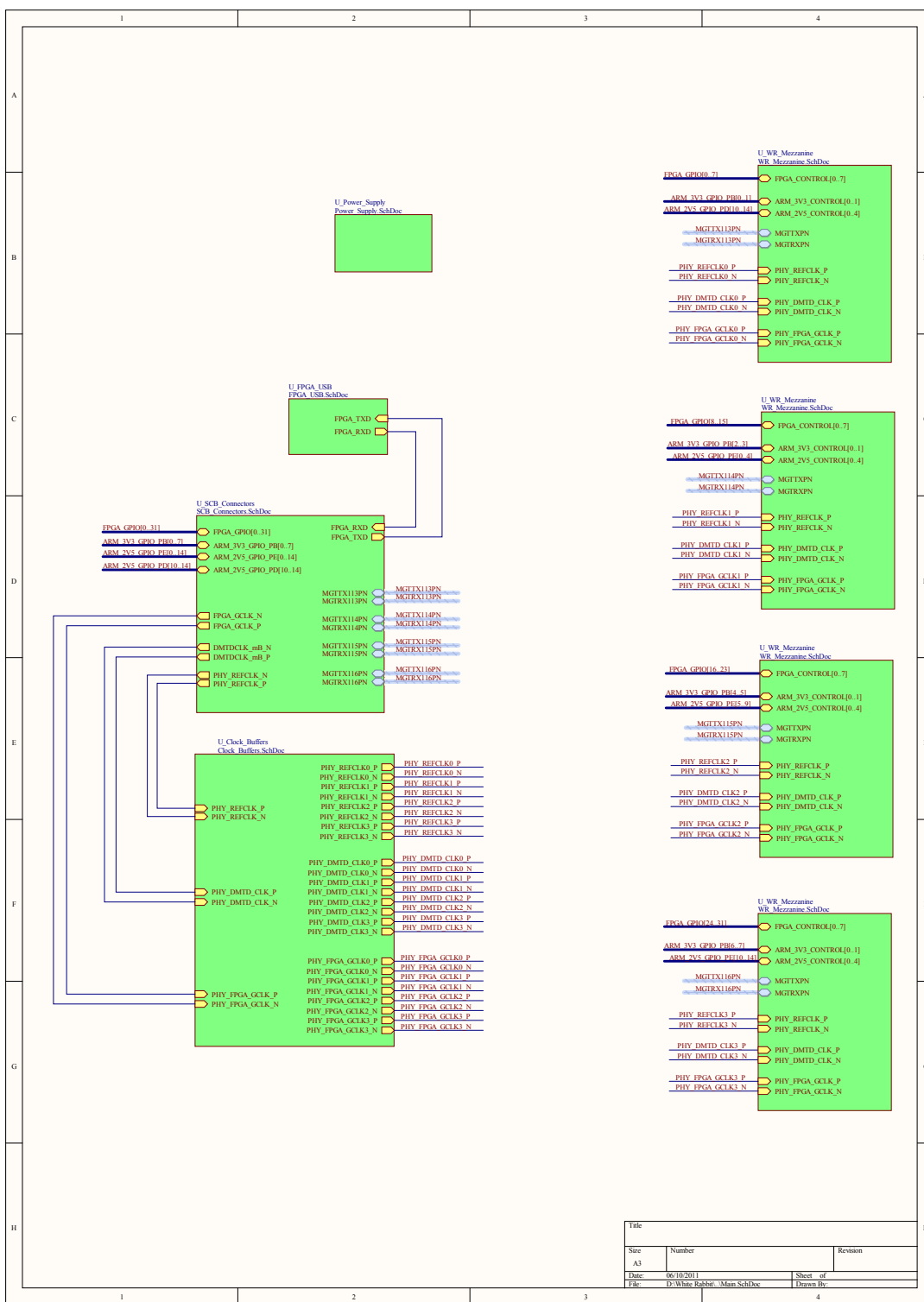
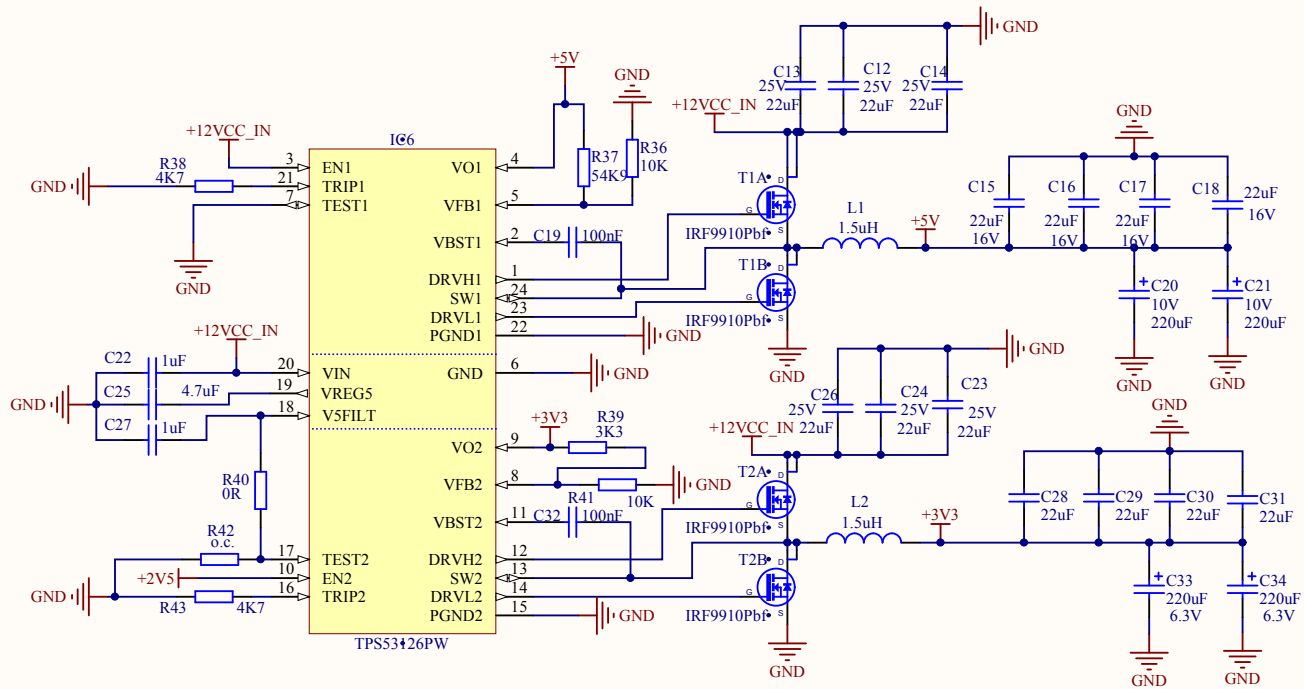


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Title		
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Date:	06/10/2011	Sheet of
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Title		
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A

A

B

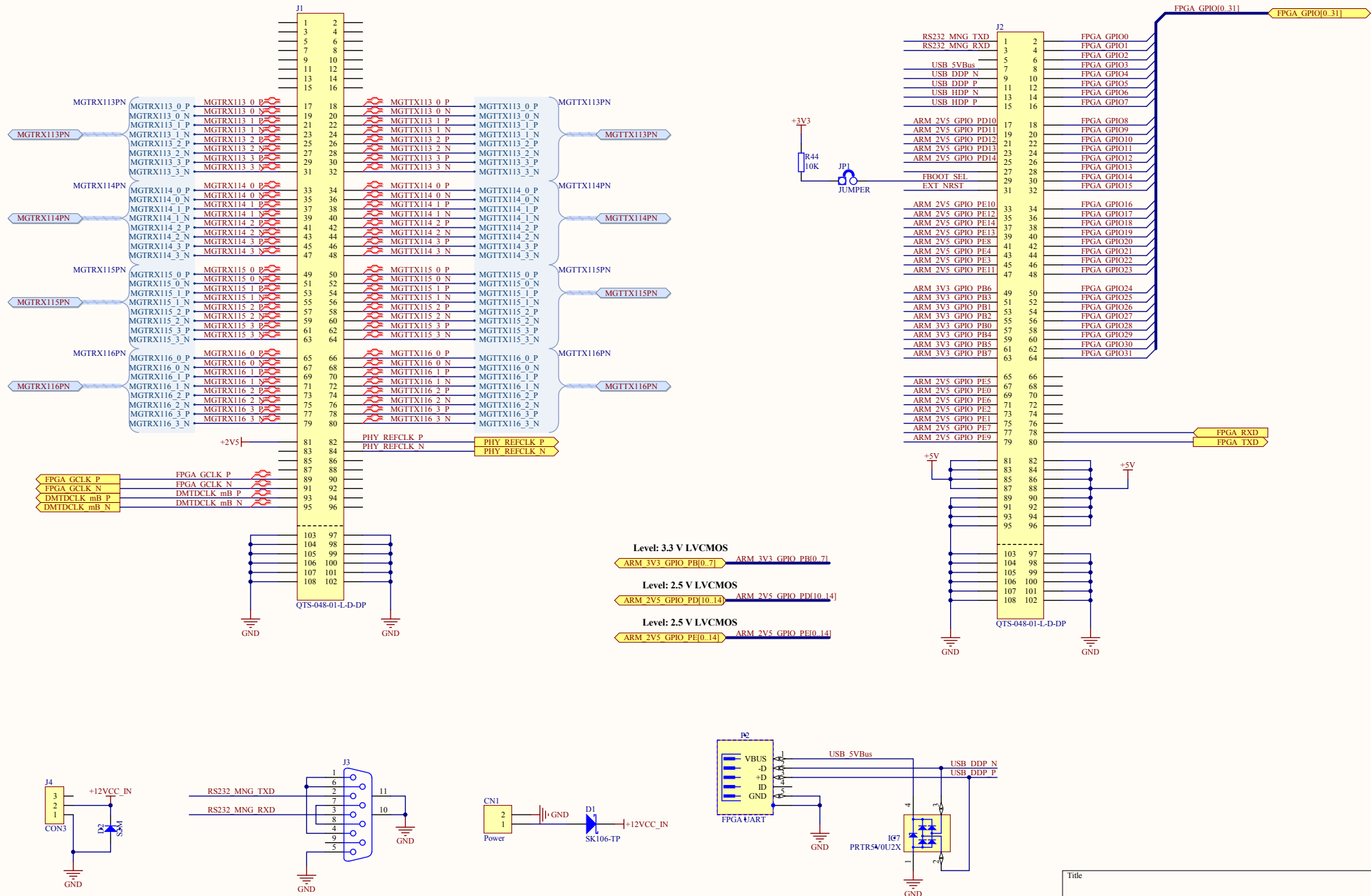
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C

C

D

D



Level: 3.3 V LVCMOS  
ARM 3V3 GPIO PB[0..7] ARM 3V3 GPIO PB[0..7]

Level: 2.5 V LVCMOS  
ARM 2V5 GPIO PD[10..14] ARM 2V5 GPIO PD[10..14]

Level: 2.5 V LVCMOS  
ARM 2V5 GPIO PE[0..14] ARM 2V5 GPIO PE[0..14]

Title		
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