

-dirty [q=quit,r=refresh,f=freeze,t=toggle servo,c=extra ppsl params]

WR Switch Sync Monitor v7.0-

WR time (TAI) : 2025-01-19 12:18:56.003770 Leap seconds: 37  
Switch time (UTC): 2025-01-19 12:18:19.003312 TAI-UTC : +37.000458  
PLL mode: BC PLL locking state: LOCKED PPS: On BMCA: Standard Domain: 0  
Local ClockId: : : ff:fe:02:61:01 clockClass: 248(BC) clockAccuracy:0xFE(unkn) logVar: 65535  
prio1: 128 prio2: 128

Grand Master Id		stepsRemoved	clockClass	timeSource	accuracy	logVar
:	:	1st hop	193(FR)	0xA0(int0scillator)	0x20(25ns)	50973

HAL				PPST											VLANs
Iface	Freq	Inst	Name	Config	MAC of peer port	PTP/EXT/PDETECT	States	PrConf	annI	syncI	delReqI	pdelReqI			VLANs
- wr11	Lock	0	wri1-1	auto	: : :03:71:02	SLAVE	/WR:IDLE	/EXT_ON	V-wH/W	1	0	0	0	0	10
- wr12		1	wri2-1	auto	: : :09:08:9e	MASTER	/WR:IDLE	/EXT_ON	V-wH/W	1	0	0	0	0	10
- *wri3	5	2	wri3-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	10
- *wri4		3	wri4-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	V-wH/W	0	0	0	0	0	
- *wri5		4	wri5-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	1	0	0	0	0	
- *wri6		5	wri6-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	
- *wri7		6	wri7-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	
- *wri8		7	wri8-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	
- *wri9		8	wri9-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	
- *wri10		9	wri10-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	
- *wri11		10	wri11-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	V-wH/W	0	0	0	0	0	
- *wri12		11	wri12-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	
- *wri13		12	wri13-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	400
- *wri14		13	wri14-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	
- *wri15		14	wri15-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	
- *wri16		15	wri16-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	
- *wri17		16	wri17-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	
- *wri18		17	wri18-1	auto	00:00:00:00:00:00	DISABLED	/WR:IDLE	/NONE	R-wH/W	0	0	0	0	0	

Iface: +/- SFP in DB; PrConf-Protocol config: V-Eth over VLAN, U-UDP, R-Ethernet; a-ext autoneg; Profile: W-WR, L-HA  
annI-LogAnnounceInterval;syncI-LogSyncInterval;delReqI-LogMinDelayReqInterval;pdelReqI-LogMinPdelayReqInterval;

----- Synchronization status -----  
Servo state: wri1:White-Rabbit: TRACK\_PHASE

+-- Timing parameters -----  
| meanDelay : 55.112 nsec err state: 2  
| delayMS : 55.112 nsec err offset: 2  
| delayMM : 1143.787 nsec err delta: 3  
| delayAsymmetry : 0.000 nsec  
| delayCoefficient : +0.0000000000000000 fpa : 0  
| ingressLatency : 275.901 nsec  
| egressLatency : 238.903 nsec  
| semistaticLatency: 0.000 nsec  
| offsetFromMaster : 0.003 nsec  
| Phase setpoint : -7.460 nsec  
| Skew : -0.001 nsec  
| Estimated link len: 11.249 meters  
| Update counter : 147763 times  
| Master PHY delays TX: 239.923 nsec RX: 278.835 nsec  
| Slave PHY delays TX: 238.903 nsec RX: 275.901 nsec

----- Temperatures -----  
FPGA: 54.25 PLL: 53.00 PSL: 50.75 PSR: 53.31