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|---|---|-----------------|
| Title | | |
| White Rabbit Switch MCH - clocking mezzanine Analog phase detector & ADC | | |
| Size | Number | Revision |
| A | | 0.0.5 |
| Date: | 2/27/2009 | Sheet of |
| File: | E:\WhiteRabbit-svn\...\CLKB_analog_phase_detector.doc | Drawn by SchDoc |

A

B

C

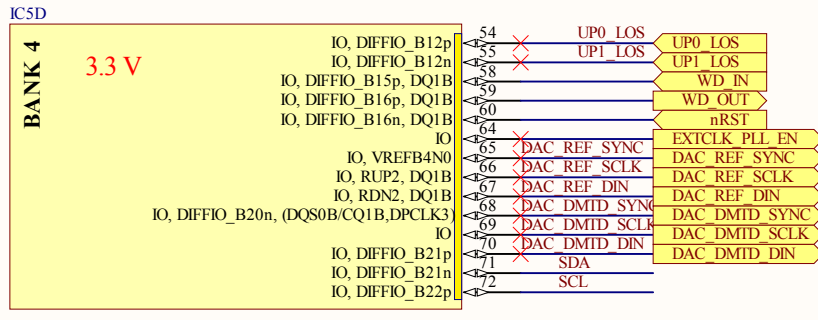
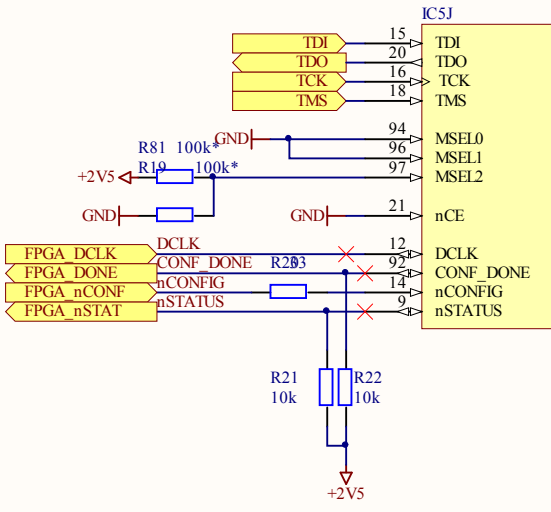
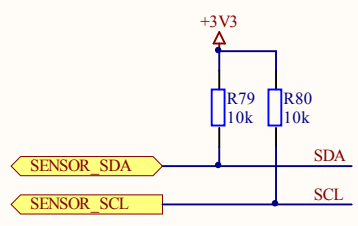
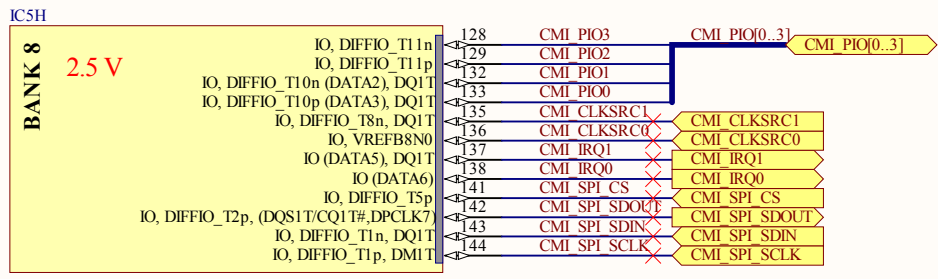
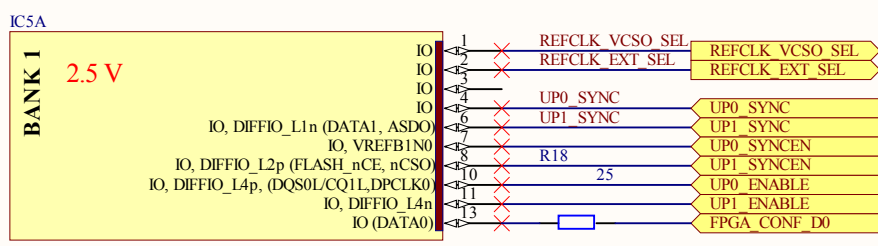
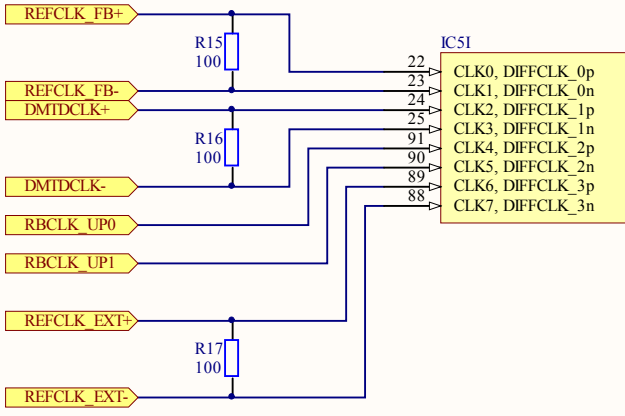
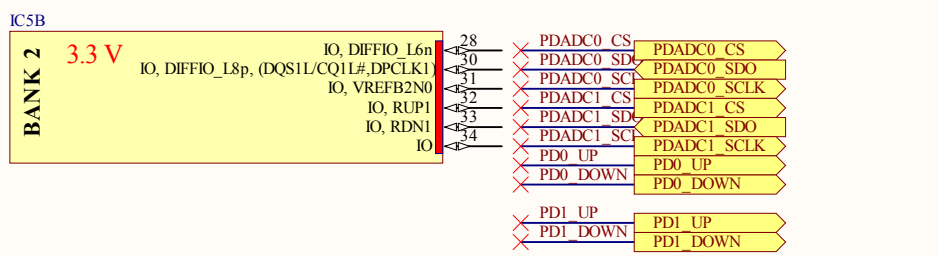
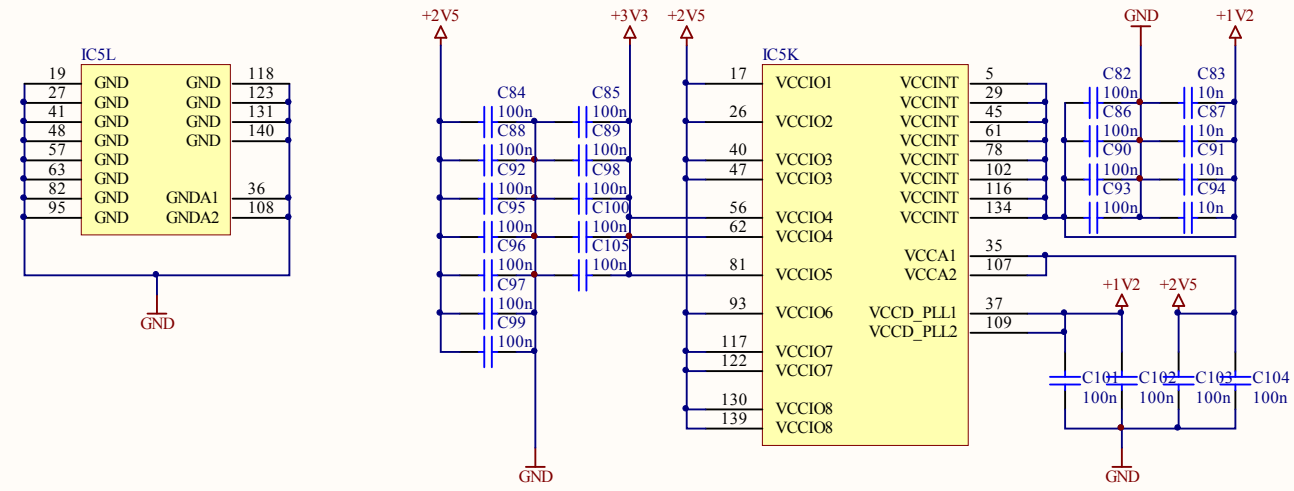
D

A

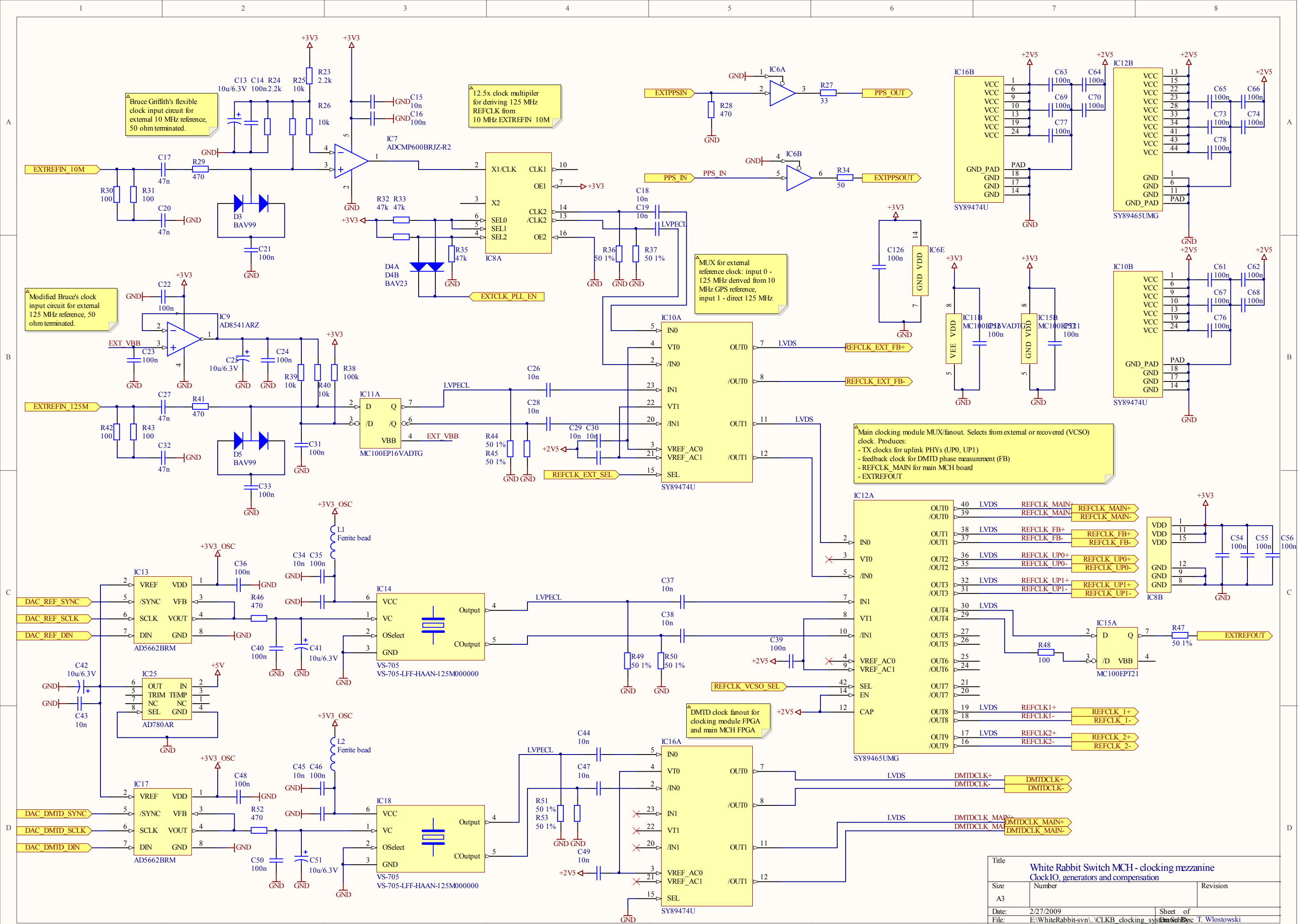
B

C

D

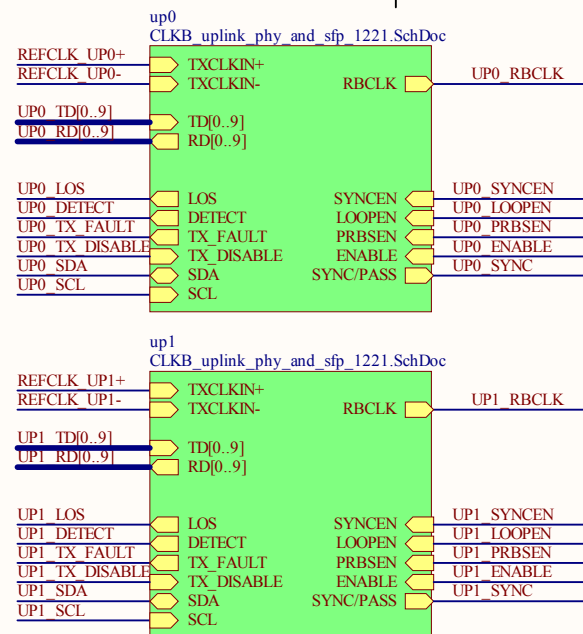


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| Title | | | White Rabbit Switch MCH - clocking mezzanine | |
| Size | | | Clocking FPGA | |
| A3 | | | Revision | |
| Date: | | | 2/27/2009 | |
| File: | | | E:\WhiteRabbit-svn\...\CLKB_clocking_fpga.dwg | |
| Sheet of | | | 1 of 1 | |
| Drawn By: | | | dsw | |

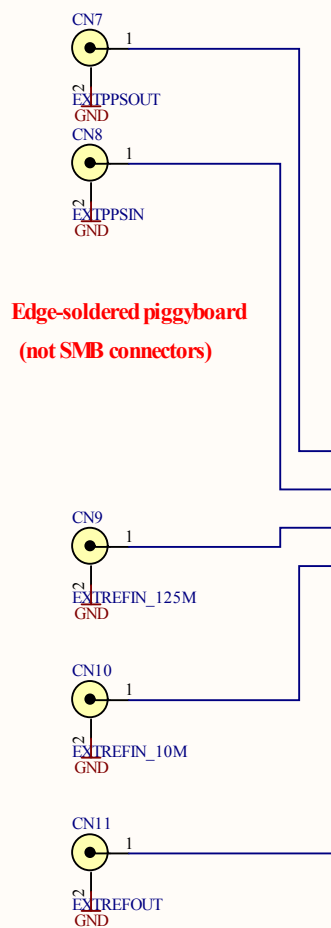


Front panel

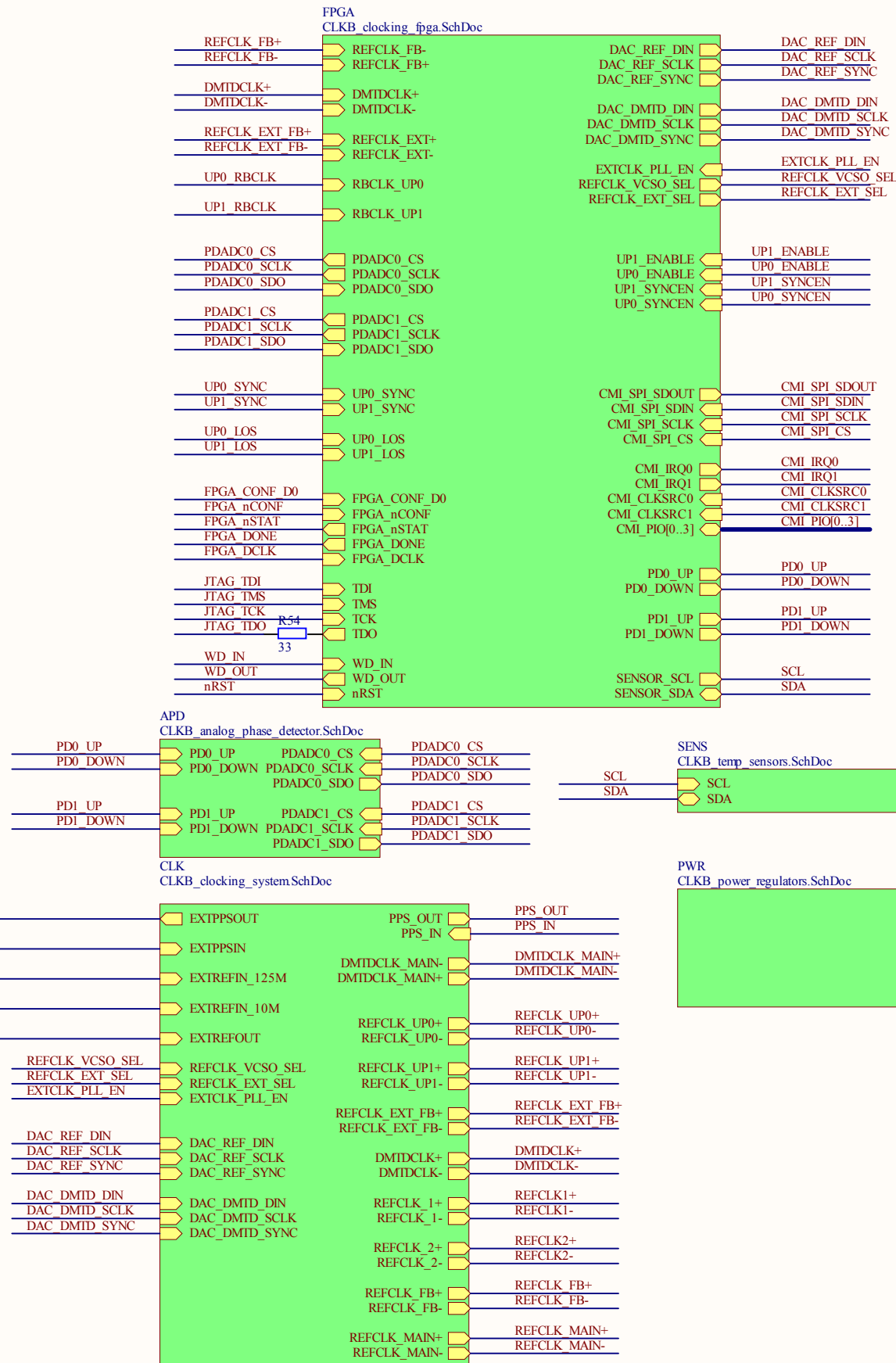
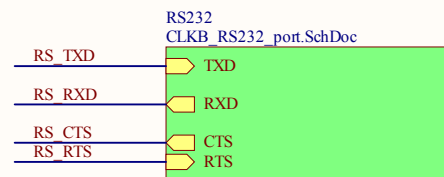
Uplink ports (PHY + SFP)



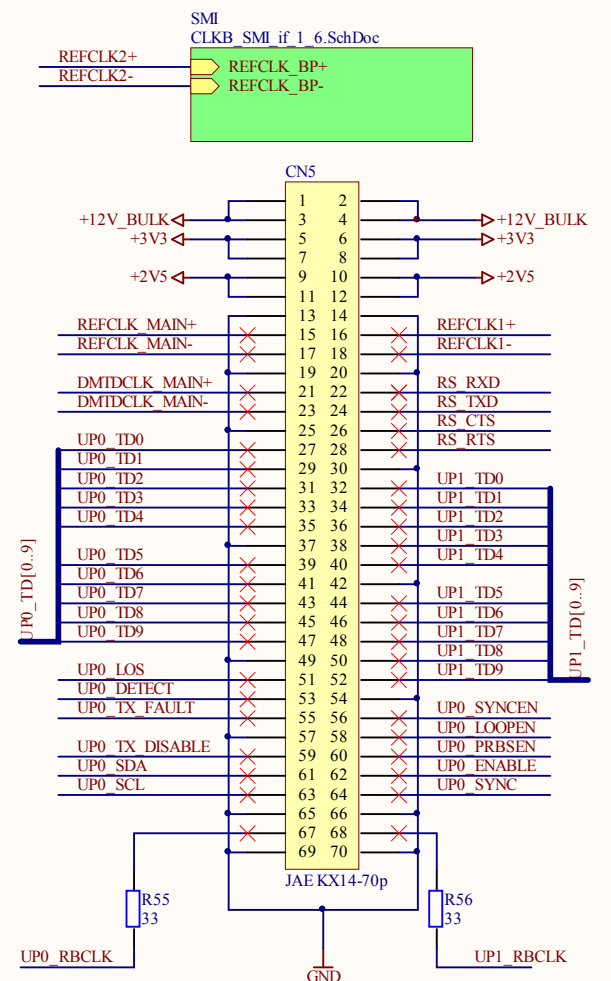
Clocks and PPS



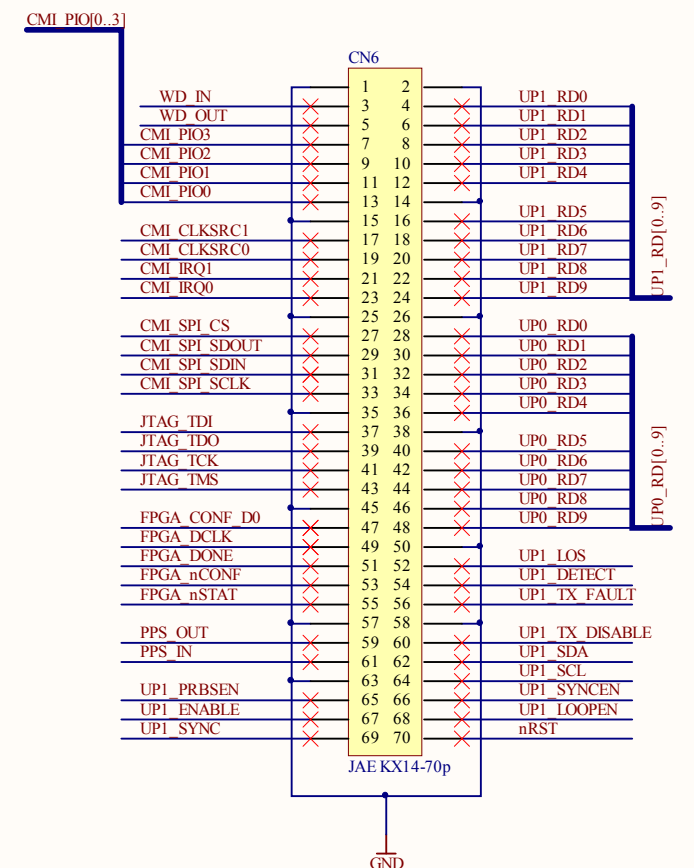
Edge-soldered piggyboard
(not SMB connectors)



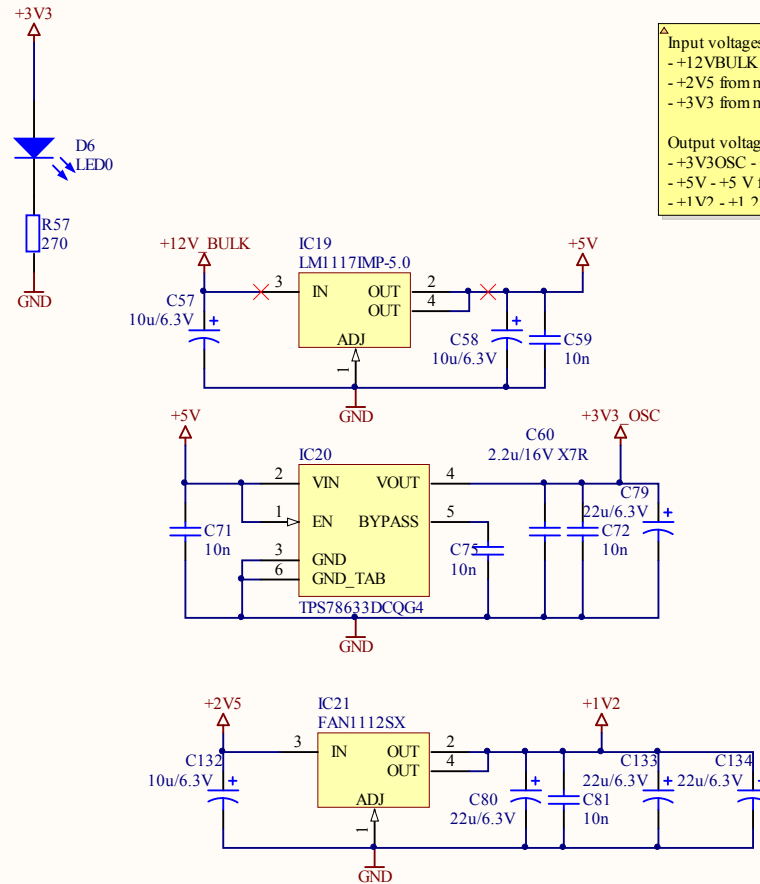
SMI backplane I/F



Mainboard interface



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|--|-------------------------------------|-----------|
| Title | | |
| White Rabbit Switch MCH - clocking mezzanine | | |
| System diagram & mainboard connection | | |
| Size | Number | Revision |
| A3 | | |
| Date: | 2/27/2009 | Sheet of |
| File: | E:\WhiteRabbit-svn\CLKB_main.SchDoc | Drawn By: |



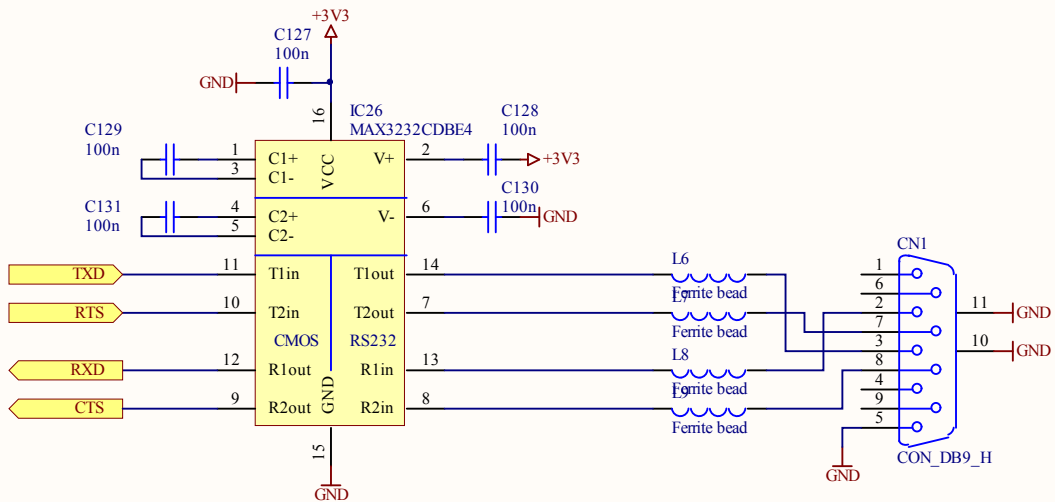
Input voltages (from main MCH board):

- +12VBULK backplane bulk power (+12 V)
- +2V5 from main regulator (+2.5 V, several amps)
- +3V3 from main regulator (+3.3 V, several amps)

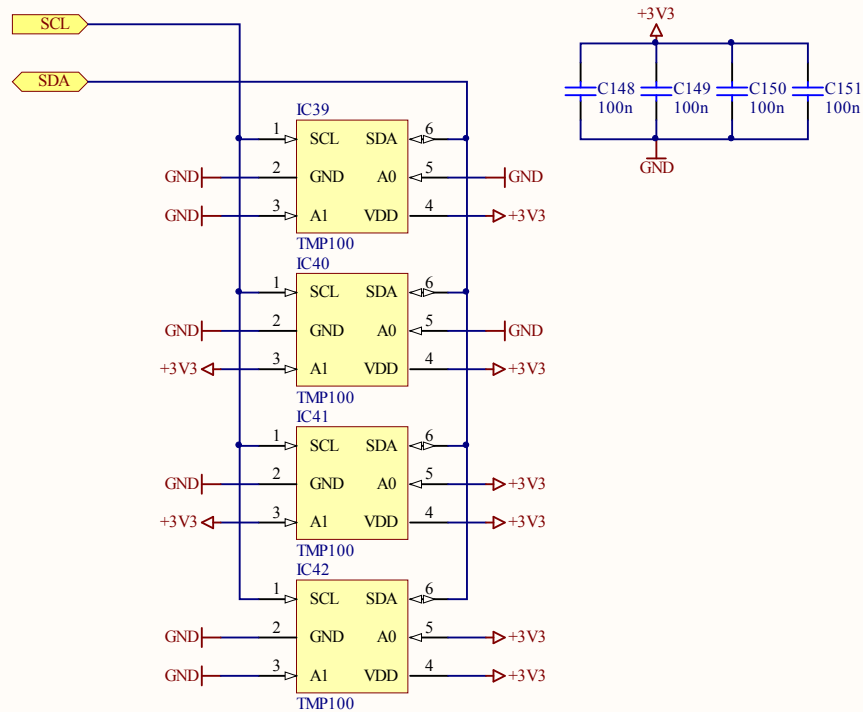
Output voltages:

- +3V3OSC - +3.3 V @200 mA ultralow-noise supply for VCSOs and DACs
- +5V - +5 V for DAC reference and +3V3OSC LDO supply
- +1V2 - +1.2 V @ 1 A for FPGA V_{core}

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|---|---|----------|
| Title | | |
| White Rabbit Switch MCH - clocking mezzanine Power supply regulators | | |
| Size | Number | Revision |
| A4 | | |
| Date: | 2/27/2009 | Sheet of |
| File: | E:\WhiteRabbit-svn\...\CLKB_power_regulator.sch | 4 |



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|-------|--|----------|--|
| Title | | | White Rabbit Switch MCH - clocking mezzanine RS232 port |
| Size | Number | Revision | |
| A | | | |
| Date: | 2/27/2009 | Sheet of | 3 of 4 |
| File: | E:\WhiteRabbit-svn\CLKB_RS232_port\WhiteRabbit.doc | | |



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|---|--|----------|
| Title | | |
| White Rabbit Switch MCH - clocking mezzanine Temperature sensors | | |
| Size | Number | Revision |
| A | | |
| Date: | 2/27/2009 | Sheet of |
| File: | E:\WhiteRabbit-svn\.\CLKB_temp_sensors | By: |

