Minimum voltage: 5V
Maximum voltage: 14V
Power consumption: 0.3A @ 12V
Connect directly to the WRS power supply.

Clock input for external 10MHz input
Signal type: AC or DC coupled
Allowed power: from -10dBm to 10dBm
Waveform: sine wave or square

Clock output to be connected to the WRS PCB board
Signal type: Buffered LVCMOS DC coupled

Differential probe points for debug
Signal type: LVPECL AC

Minimum voltage: 8V
Maximum voltage: 14V
Power consumption: 0.3A @ 12V
Connect directly to the WRS power supply.

Clock input for external 10MHz input
Signal type: AC or DC coupled
Allowed power: from -10dBm to 10dBm
Waveform: sine wave or square

Clock output to be connected to the WRS PCB board
Signal type: Buffered LVCMOS DC coupled

Differential probe points for debug
Signal type: LVPECL AC

Minimum voltage: 8V
Maximum voltage: 14V
Power consumption: 0.3A @ 12V
Connect directly to the WRS power supply.

Clock input for external 10MHz input
Signal type: AC or DC coupled
Allowed power: from -10dBm to 10dBm
Waveform: sine wave or square

Clock output to be connected to the WRS PCB board
Signal type: Buffered LVCMOS DC coupled

Differential probe points for debug
Signal type: LVPECL AC
Formula to calculate the output voltage:

\[ V_O = 0.8 \, \text{V} \times \left(1 + \frac{R_2}{R_3}\right) \]
Differential probe points for debug
Place near the J3 connector

Board detection based on digital I/O loopback

Settings for OSC frequency:

<table>
<thead>
<tr>
<th>ID0</th>
<th>ID1</th>
<th>ID2</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 0 0</td>
<td>10M</td>
<td></td>
</tr>
<tr>
<td>0 0 1</td>
<td>20M</td>
<td></td>
</tr>
<tr>
<td>1 0 0</td>
<td>25M</td>
<td></td>
</tr>
<tr>
<td>0 1 1</td>
<td>50M</td>
<td></td>
</tr>
<tr>
<td>1 0 0</td>
<td>100M</td>
<td></td>
</tr>
</tbody>
</table>

Default setting: 20M

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Default settings:
FILTA = LOW
FILB = HIGH
Dont place jumpers for default settings

Default settings:
SD1 = LOW
SD2 = LOW
Dont place jumpers for default settings