Items for discussion (afternoon)

Next meeting (workshop?) date and place
Darmstadt end of March.

One quick comment on working method
Dedicated frequent jabber sessions on given topics. Always feed back to the list (wr-dev). Monday 15:00 default time.

Deliverables for next meeting
- Updated WR spec (Javier, Peter, Henk). Includes discussion on calibration schemes.
- WR node specs and some kind of prototype/demonstrator (Mathias). This would include the Etherbone core (Mathias) and a study on embedded CPUs (Wesley).
- WR robustness document (Cesar/Maciej).
- FEC/Erasure core (Maciej + Cesar).
- Clean up/complete switch gateware (Tom/Seven Solutions). Clean up SVN repo, simulation models, collection of SV common testing modules, end point improvements (GMII interface for copper PHYs), WR fabric interface spec clean-up, fabric emulator in SV.
- Clean up/complete switch software (Alessandro/Integrasys). Clean-up software package, build documentation, small ARM7 CPU software. PTP daemon clean-up/forking (Alessandro with Tomasz). RTU support software.
- Switch management specification (Integrasys and Alessandro).
- V3 schematics and layout (Tom/Seven Solutions).
- Virtex 6 GTX simulation (Nikhef half January).
- WR core (Nikhef and Tomasz).
- Switch testing demo (Alessandro/Davide + Cesar + Maciej + Integrasys).
- Calibration write-up (Tony and Massimo).
- Distributed DDS using PFC board? (GSI will ask around so we can decide if it's done now or later).
- Reference WR endpoint design? Later. This could be a NIC!
- Anything from Elproma and Cosylab?

Optional discussion items
- Calibration schemes.
• WR standardization.
• WR node as a NIC?