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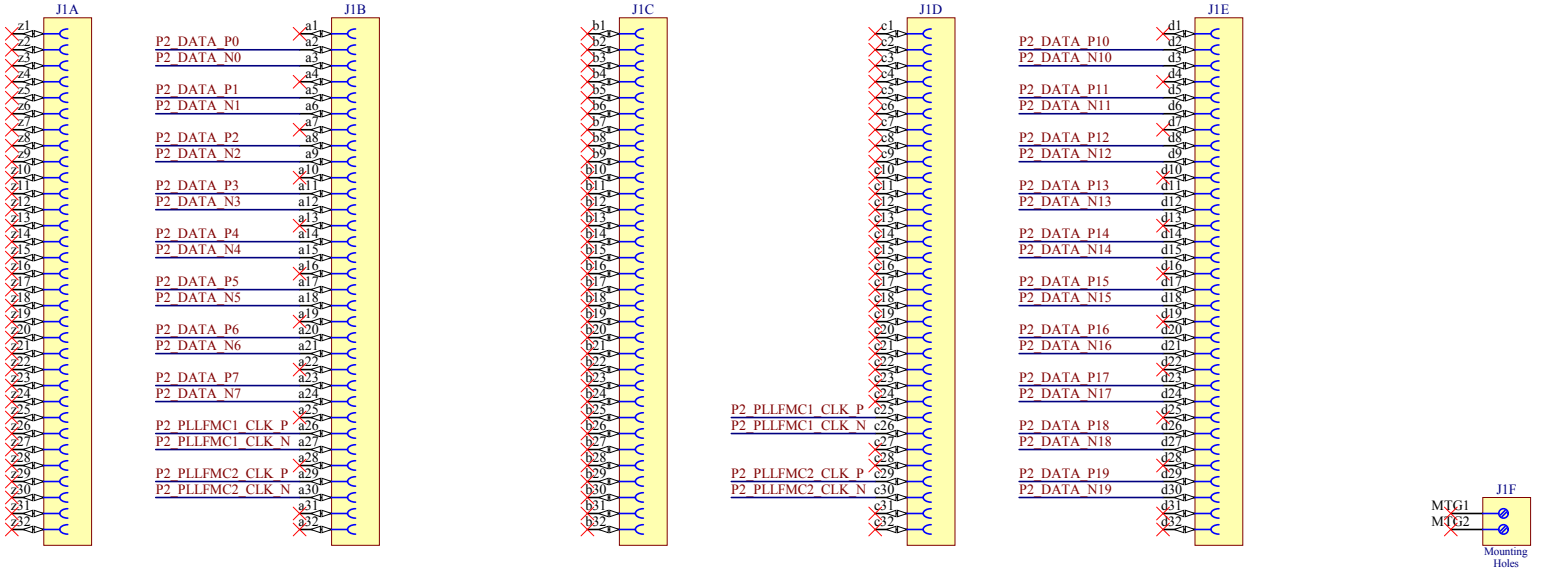

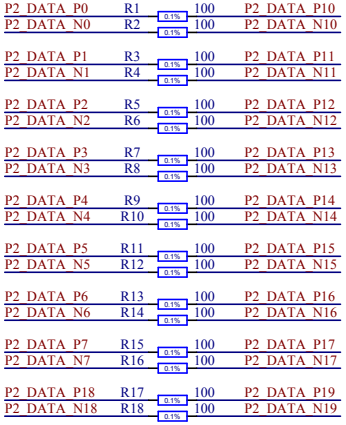

Project/Equipment: SVEC

Document: **VME P2 tester**

BE/CO

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