



<https://ohwr.org/project/sis1160-pci-io>
 Front-end PCI board with LEMO connectors
 to interface with GPIO interconnect pins of the SIS1160 FMC carrier (Struck).
 Funded by Generalitat Valenciana under grant number CDEIGENT/2019/11.
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Sheet:
 File: BoardIO.kicad_pcb

Title: SIS1160 PCI-L I/O add on

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