Always wear an antistatic wrist band when doing this procedure

1	Place the barcode sticker of the SPEC board under test. The sticker should be placed with the barcode next to the edge.
2	Place the mezzanine tester board on the FMC connector of the SPEC board under test. Fix the mezzanine to the SPEC board using the provided screws.
3	On the SPEC board under test, place the provided jumper on SW1.
4	On the SPEC board under test, connect a SATA cable between SATA 0 (GTP Link L1) and SATA 1 (GTP Link L2).
5	Connect the SFP loopback in the SFP connector of the SPEC board under test, you need to hear 'click'.
6	Connect the USB cable between the USB UART connector of the SPEC board under test and any USB slot in the computer.
7	Plug the SPEC board under test in the corresponding connector of the PCI Extender.
8	Switch on the computer and verify that the Power LED in the SPEC board under test as well as the three Power LEDs of the mezzanine tester board are on.
9	After the computer has finished with the booting procedure, a terminal running the testing program appears automatically in the middle of the screen. If that is not the case, follow these instructions: • Double click on the black icon present in the middle of the screen, or in the upper panel. • At the top of terminal's windows, you should see: user@[name of the pc]:~$ After the $ execute the following: ./run_pts.sh
10	Type the password, if needed. Password: **baraka**
11	The program asks for the serial number of the board and the MAC address for the SFP port. Use the bar code reader to read the code on the sticker. If needed, type the second serial number.
12	The software will automatically start executing tests 0 to 12. Remove the jumper when the program indicates and click on the buttons on test03.
13	Wait for the tests to finish and finally check the results.
14	Switch off the machine. Click on the power button placed in the upper right corner of the desktop and select **Shut Down**.

Once the testing has finished all the errors that may have appeared will be listed on the screen. The log files will be saved in **/home/user/pts/log**.

Log files with detailed descriptions of the tests will have been automatically generated and archived in a .zip file called: *zip_run_<run id>_<timestamp>_SPEC_<serial number>.zip.*

In case of error, you can repeat the tests one time more for the same board. If you need to repeat them more times, please report to the responsible of tests at CERN.