To Do: a trimmer-cap of 10 pF on pin1-GND allows tuning output-slew between channels.

Termination of unused outputs not really needed (?)

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A DC-supply is connected to the circuit, which includes a 95615065 D0-214 AC component. The diagram also shows a TDK ZJ/51S1P5-2PT-01 choke, common mode, 2x2002. The LT1963A LTZT1963A is highlighted in the circuit. A description of the circuit operation includes:

Vout = 1.21 V (1 + R2/R1) + I_ADJ • R2
I_ADJ = 50 mA at 25°C

The calculation R2/R1 = 5Ω / 2kΩ gives 3.31V

The diagram also notes:

FRONT PANEL PWR LED
1.85V 20mA => R = 73 Ohms

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DC input, filtering, voltage regulator
Anders Wallin (anders.e.e.wallin "at" gmell.com)
Sheet: PDL PSU
File: PDL PSU 2017.04.sch
Title: Anders’ Pulse Distribution Amplifier 2017.04
Size: 44
Date: 2017-01-27
Rev: 2017.04
KICad E.D.A. Head 4.0.7-c2-637859ubuntu16.04.3
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