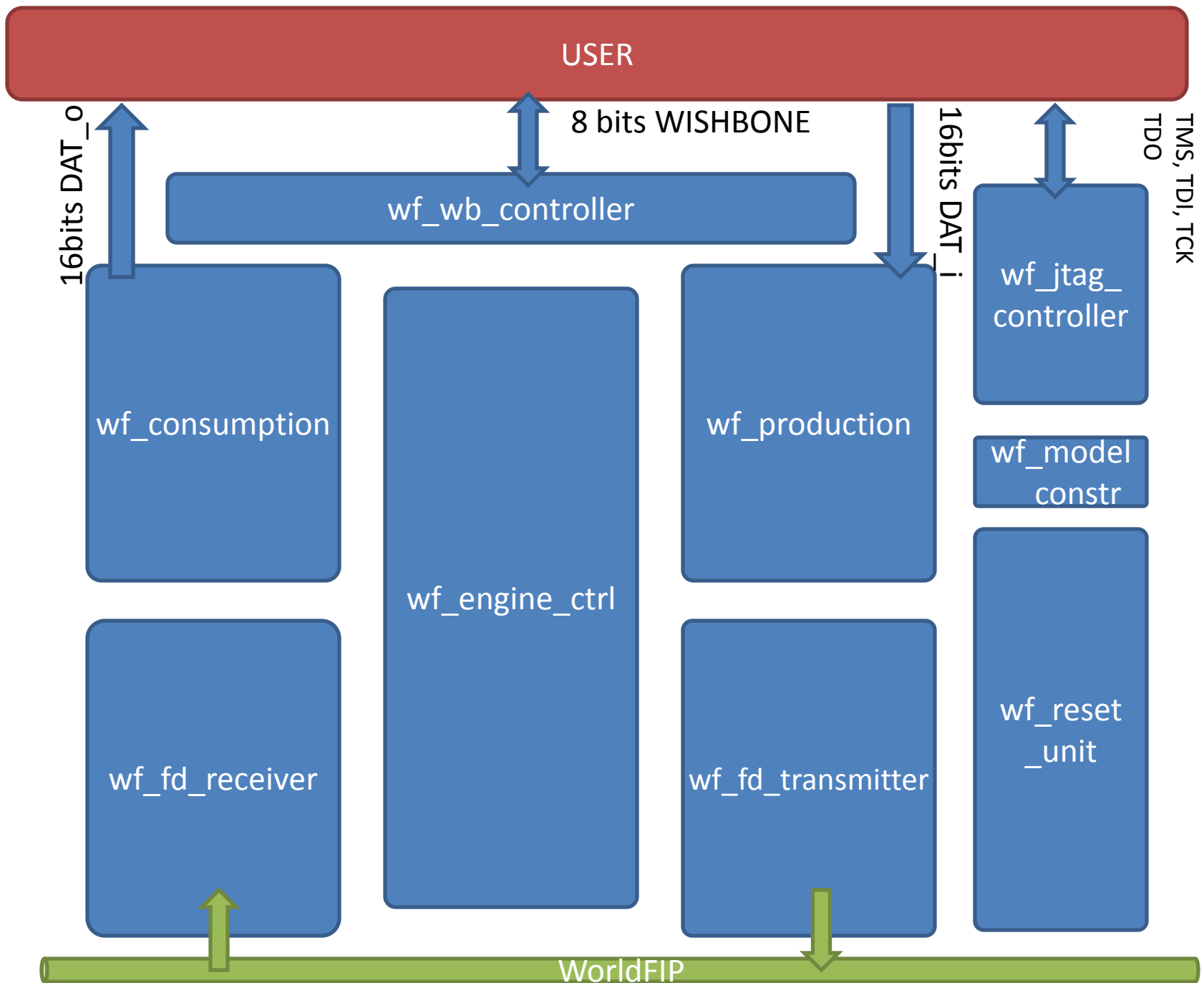
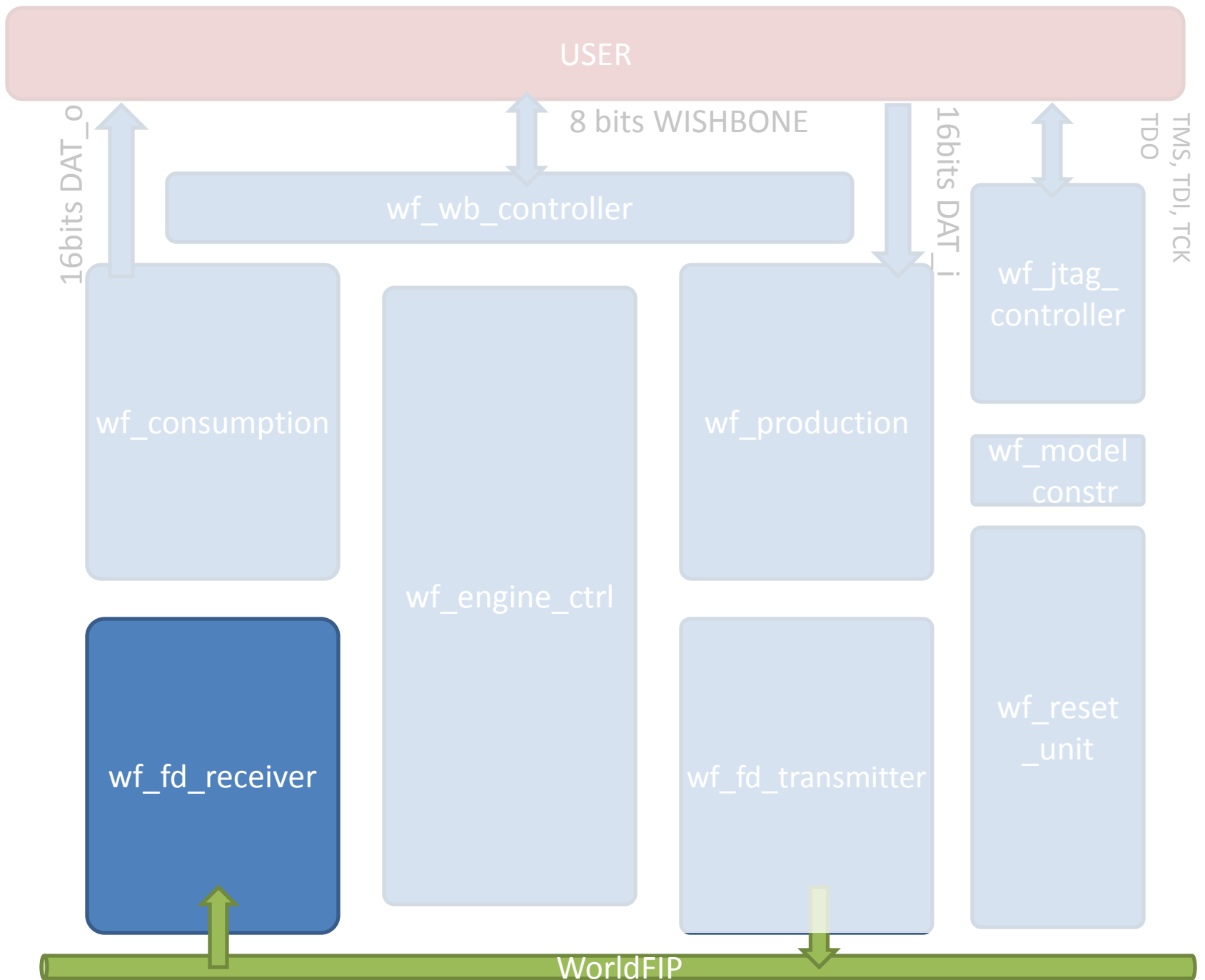
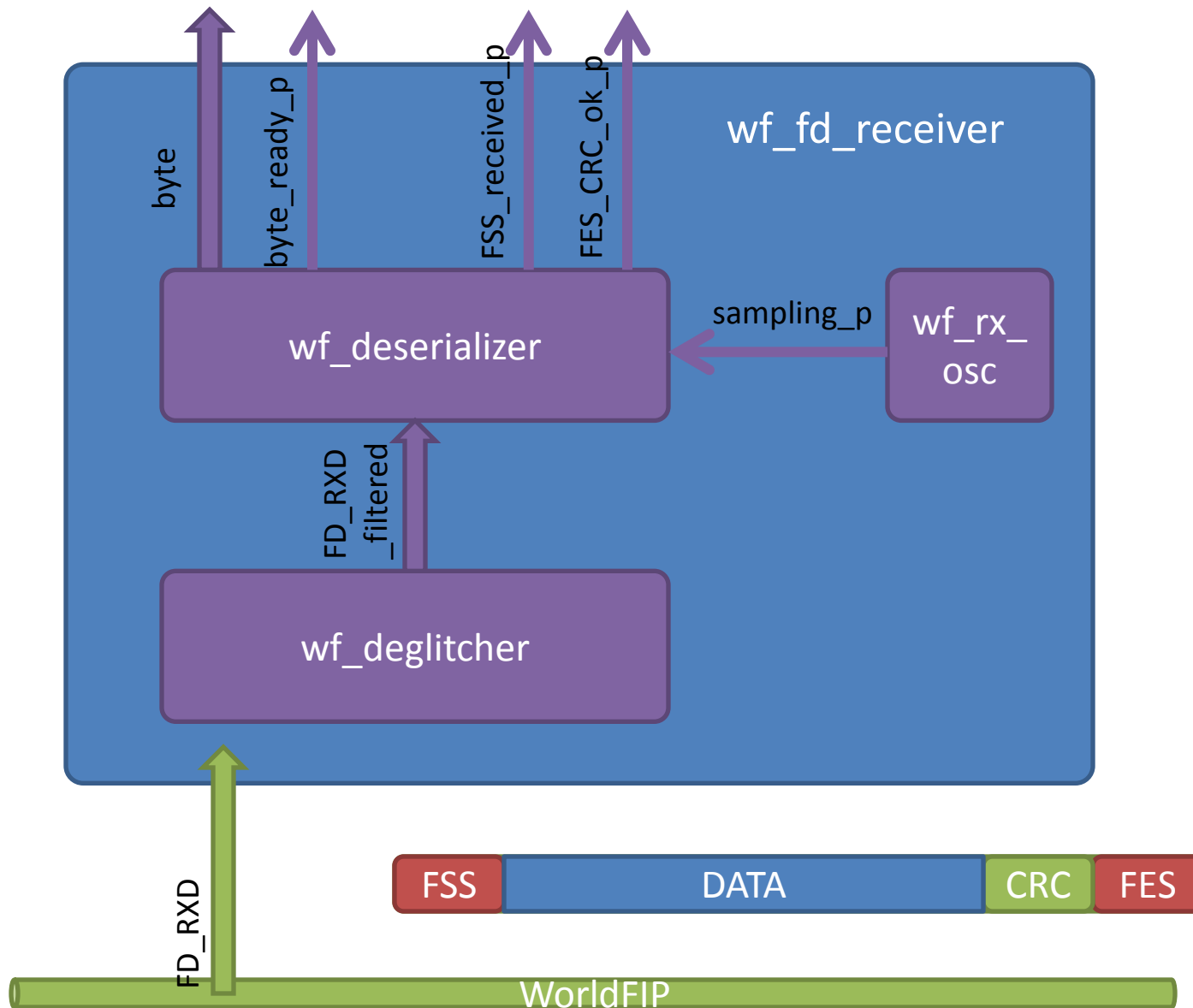
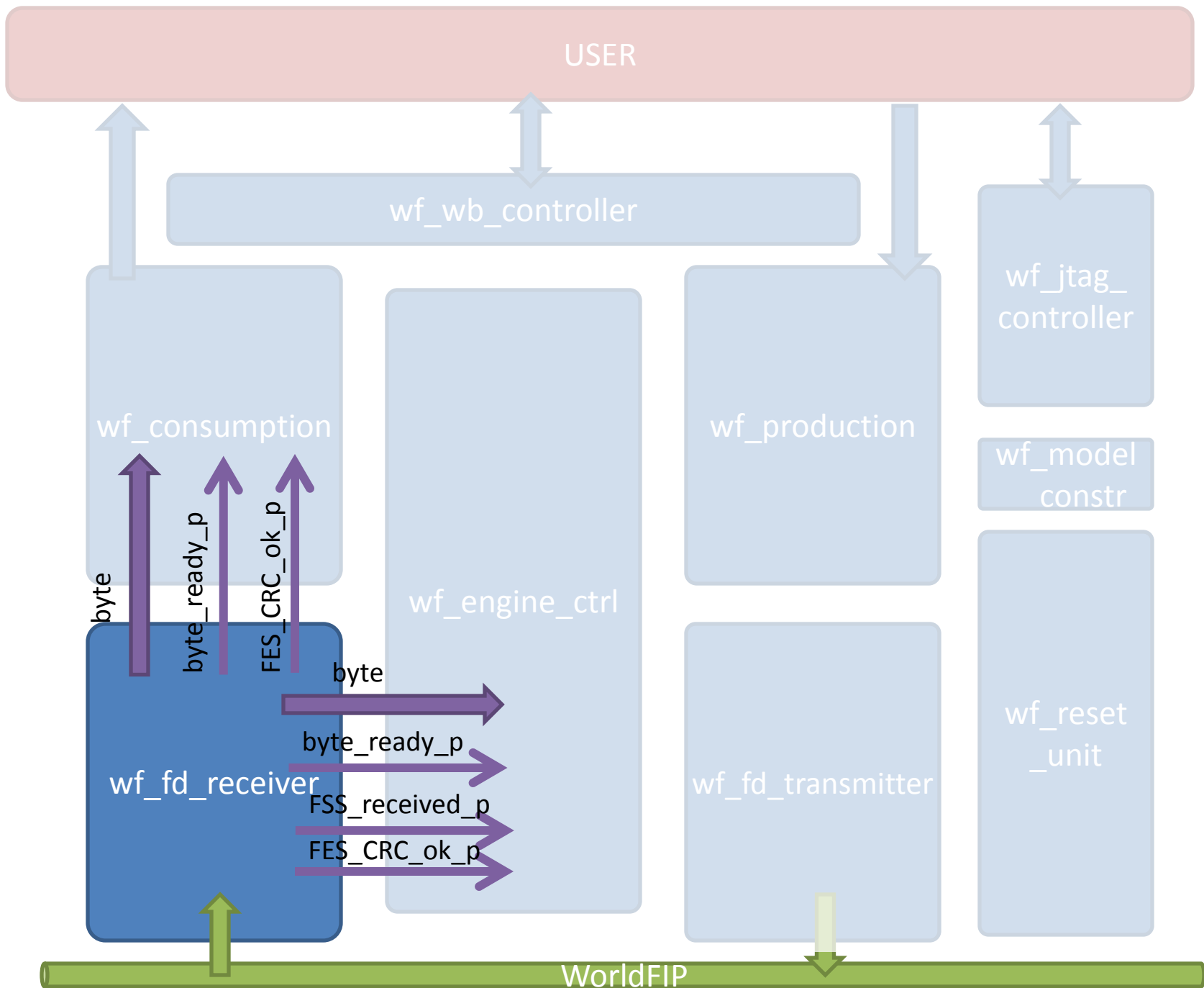


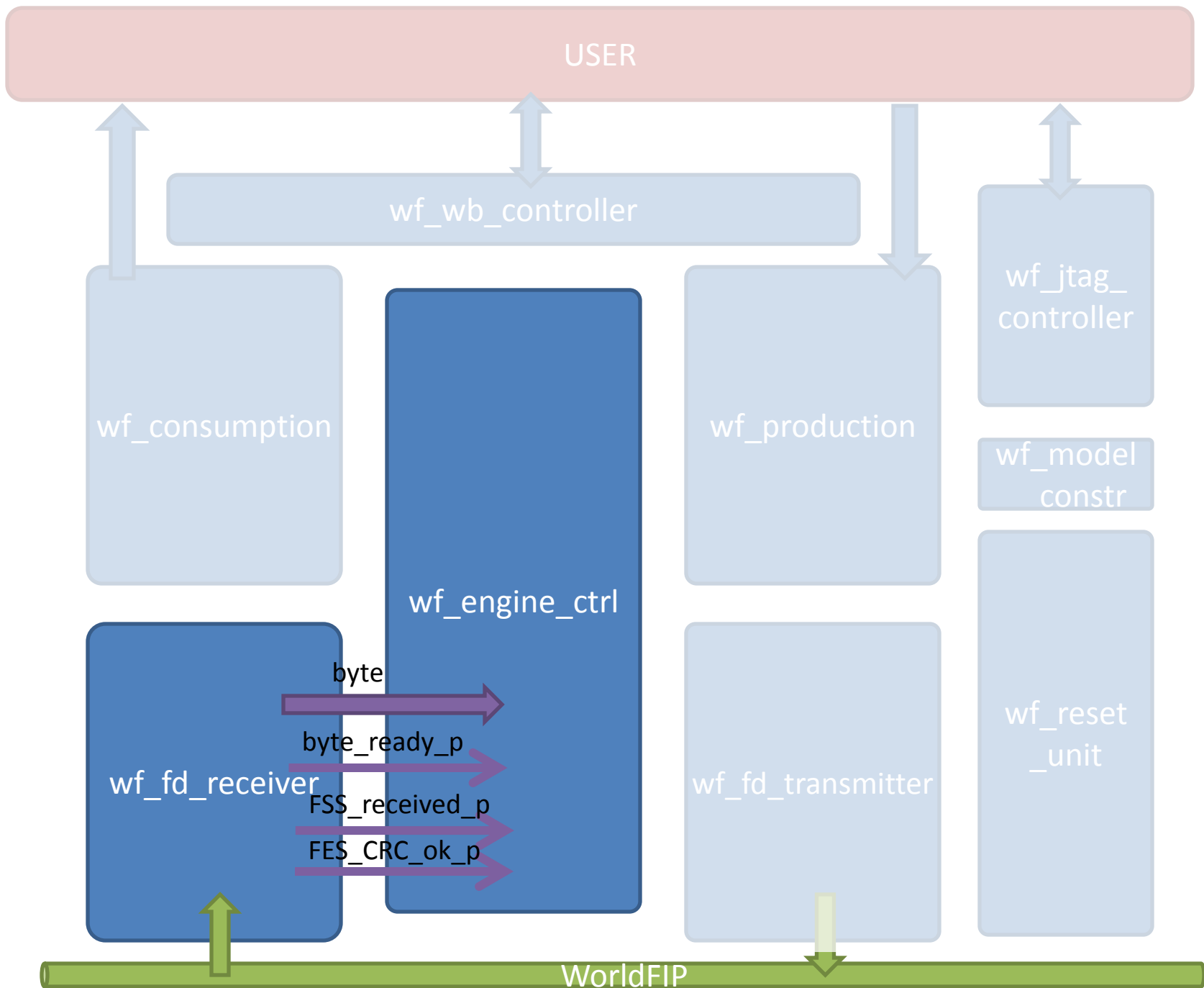
nanoFIP second code review
14 Nov 2011

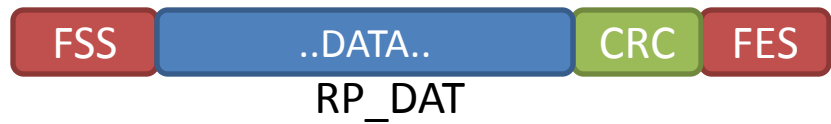
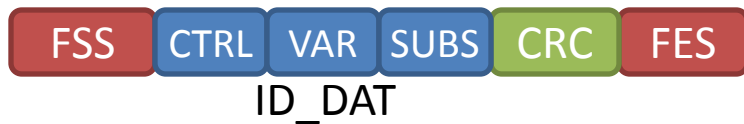
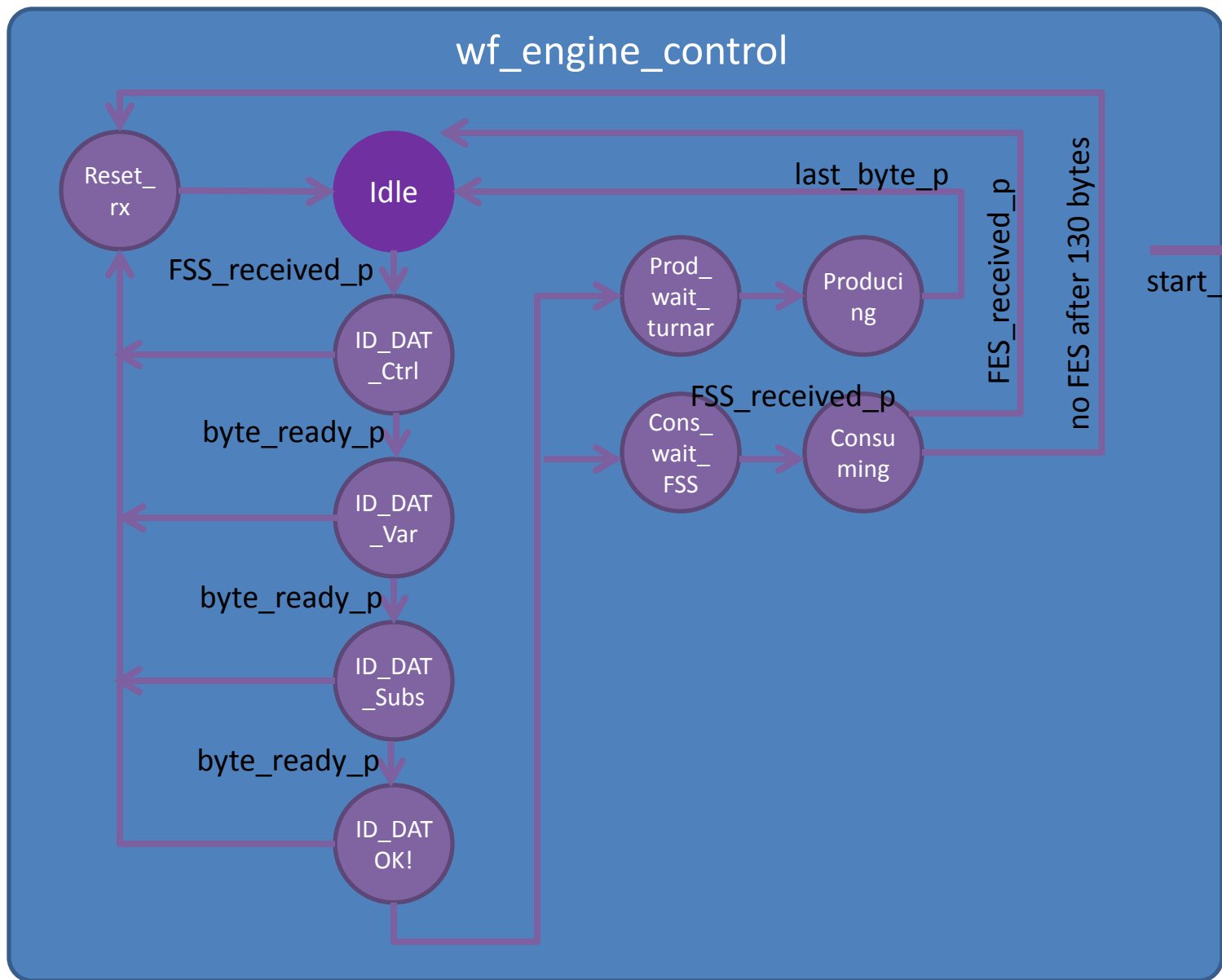


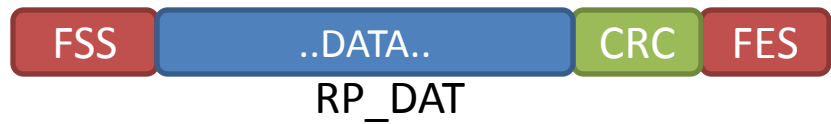
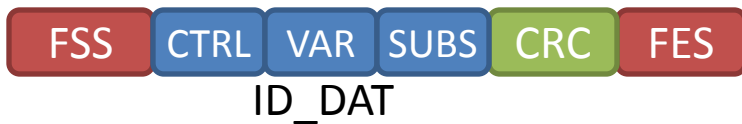
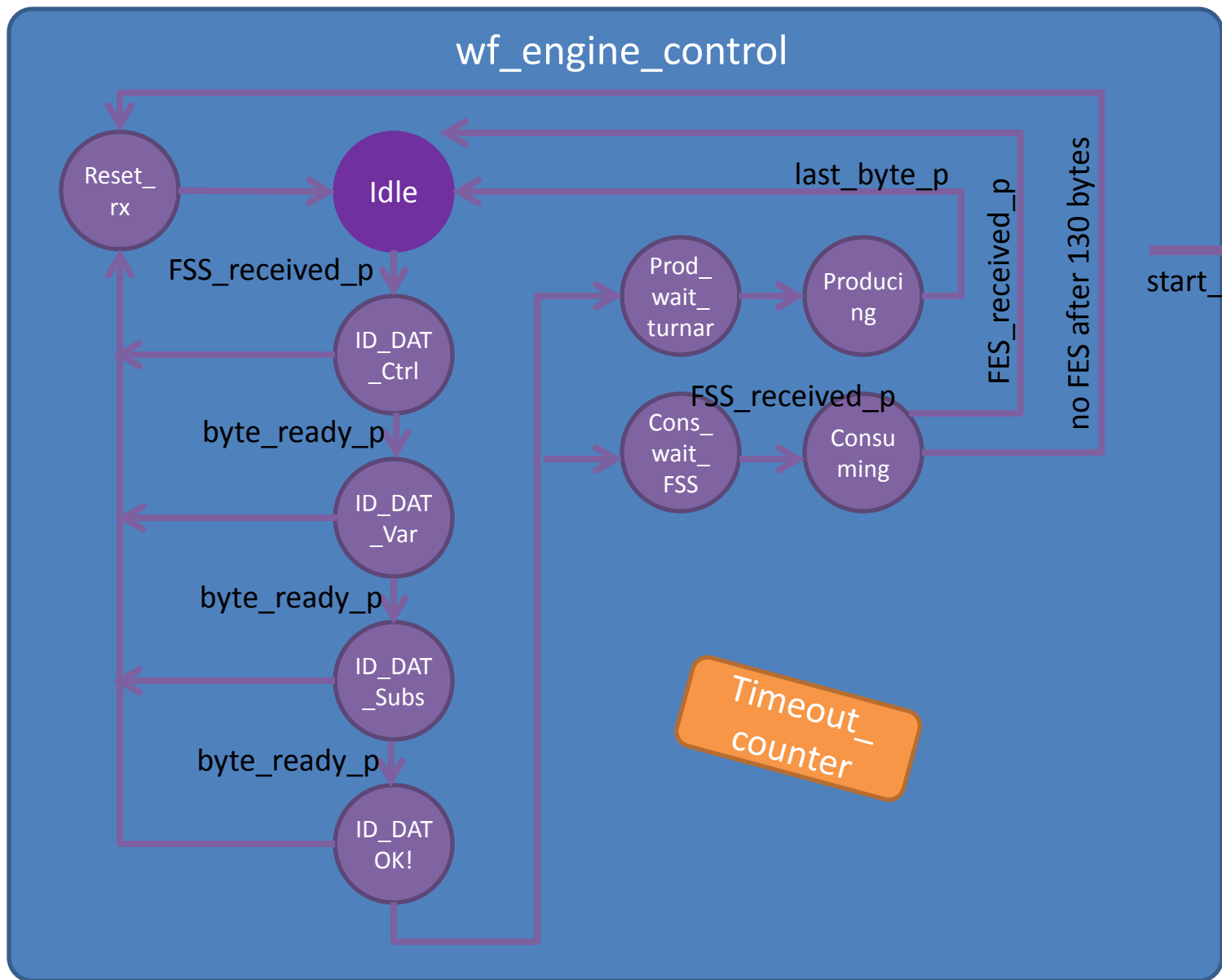


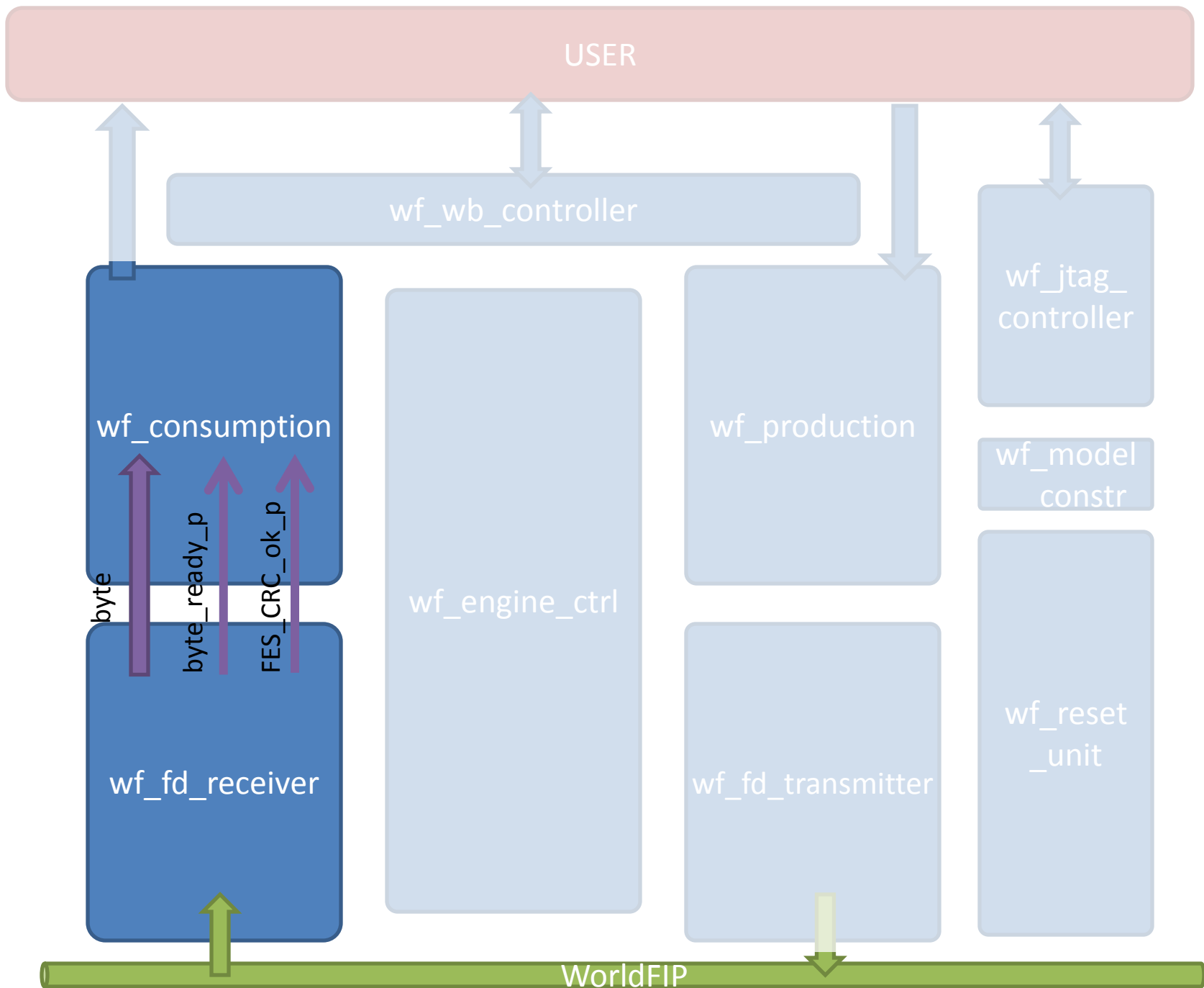


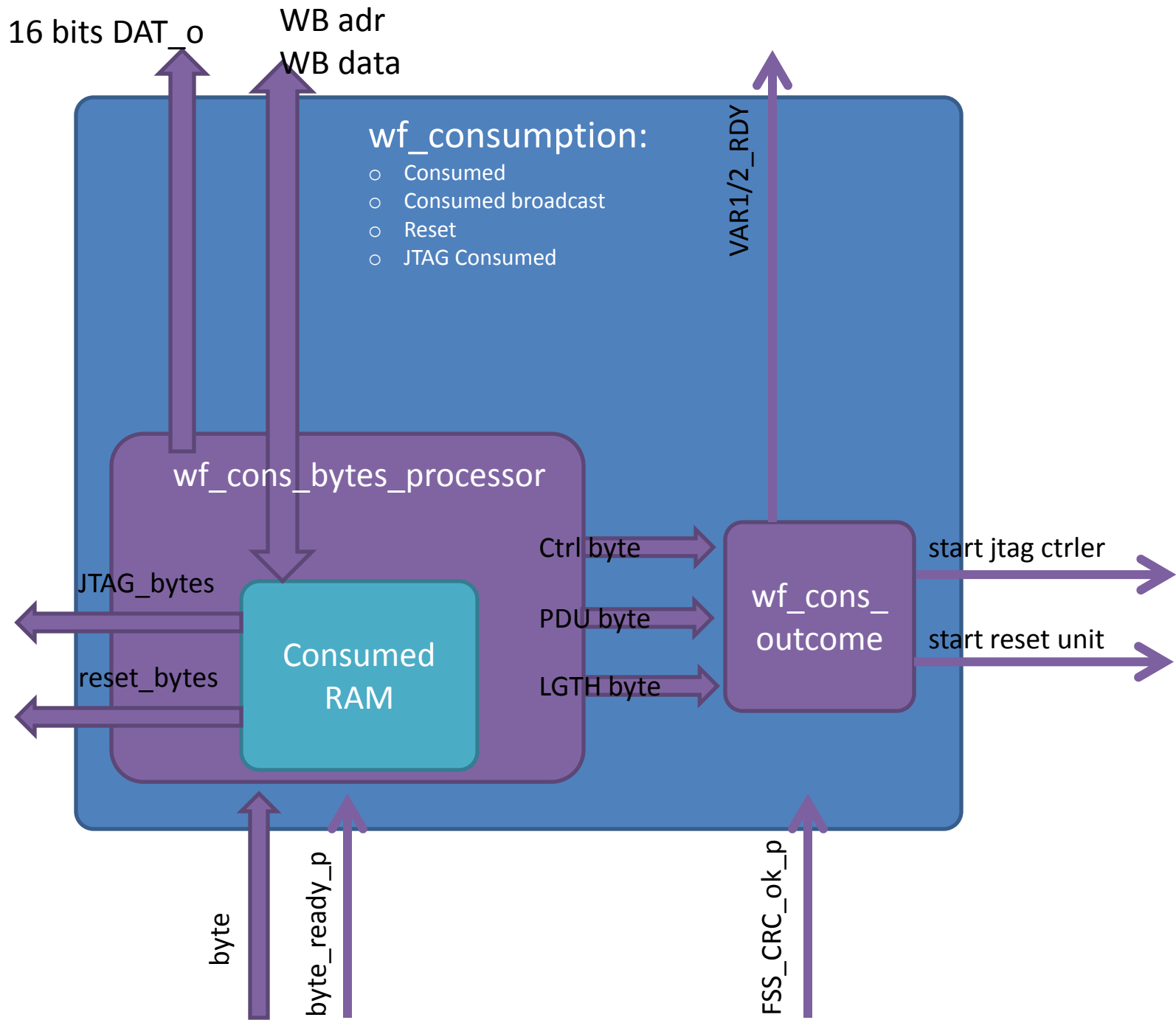


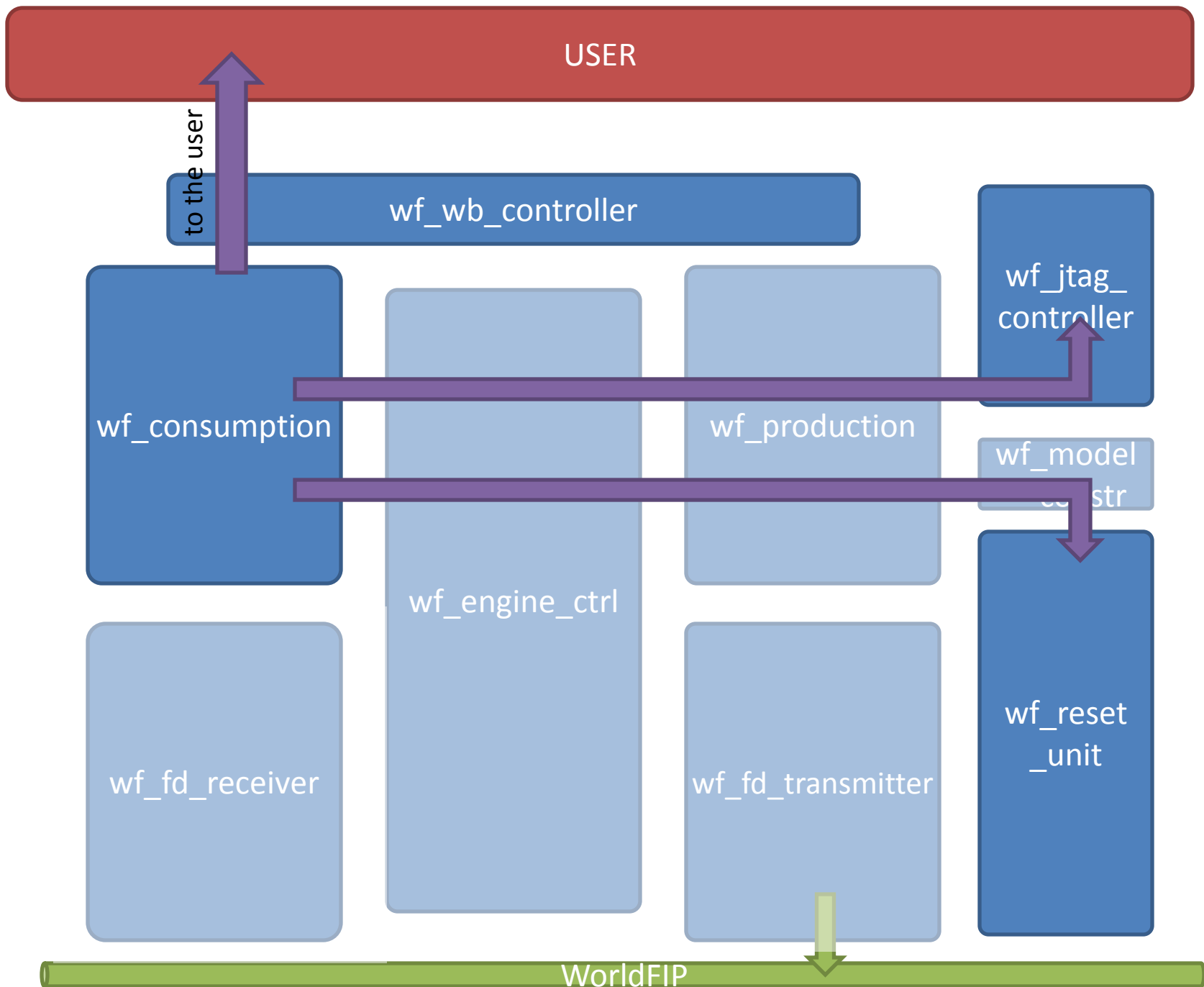


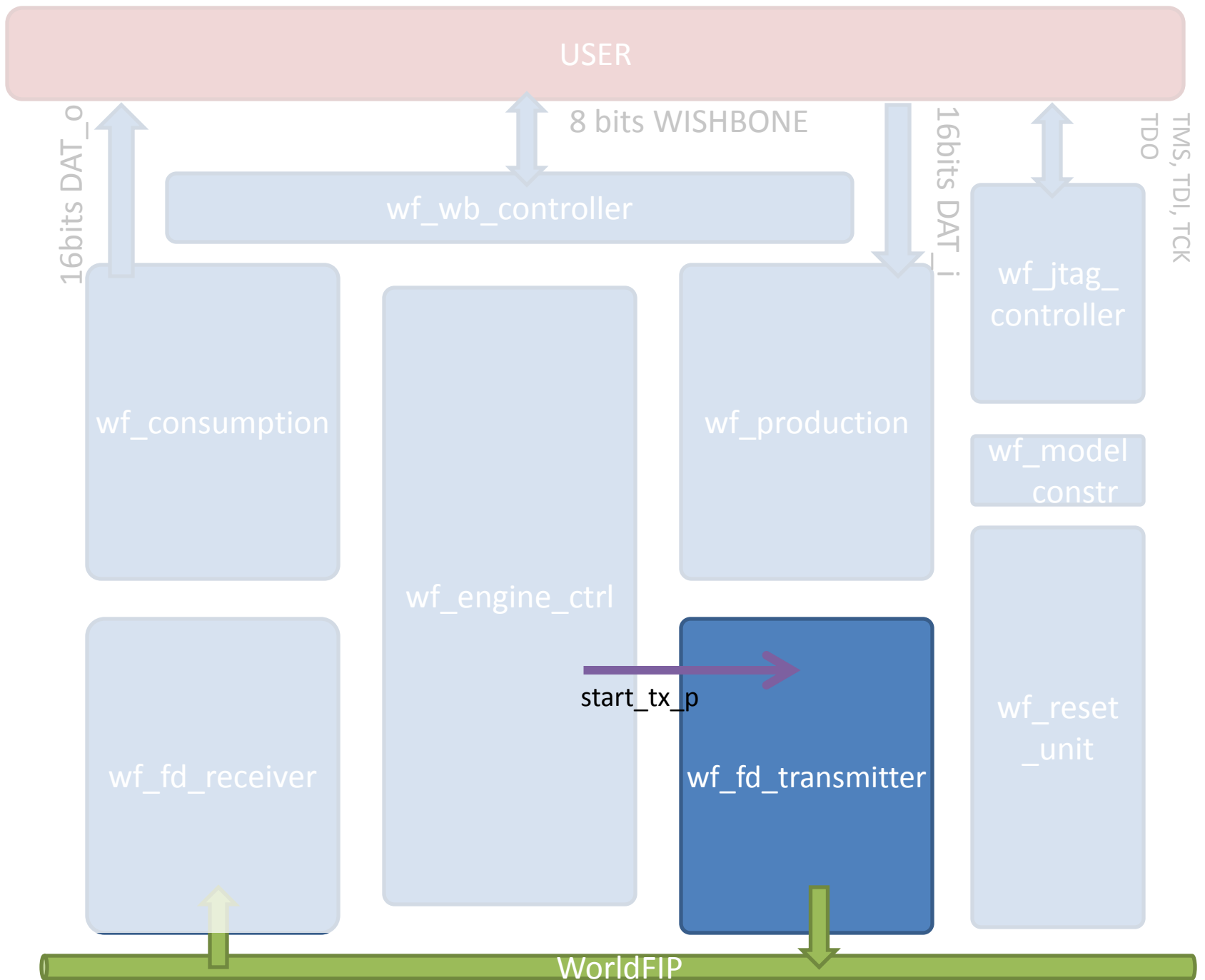


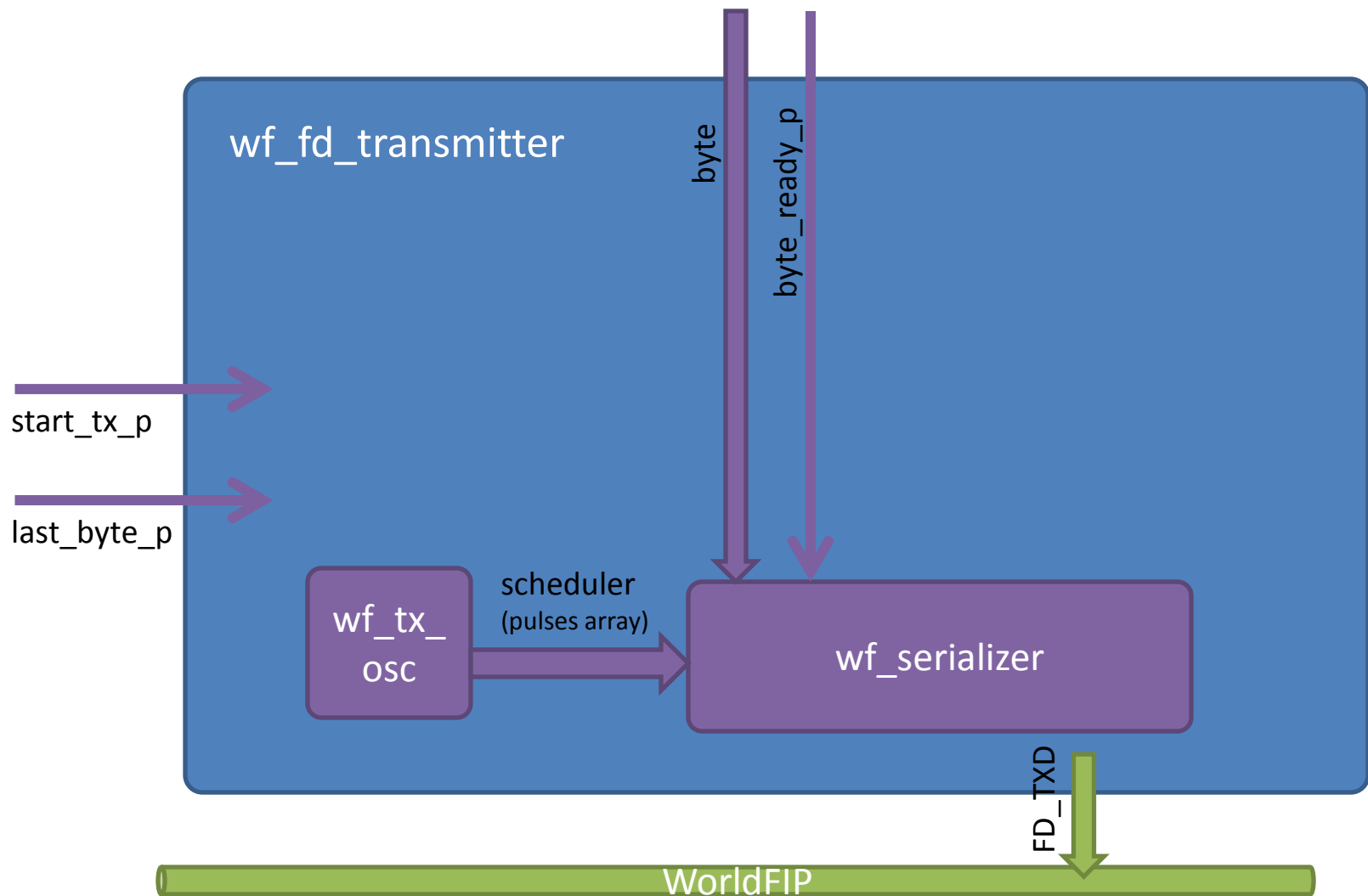


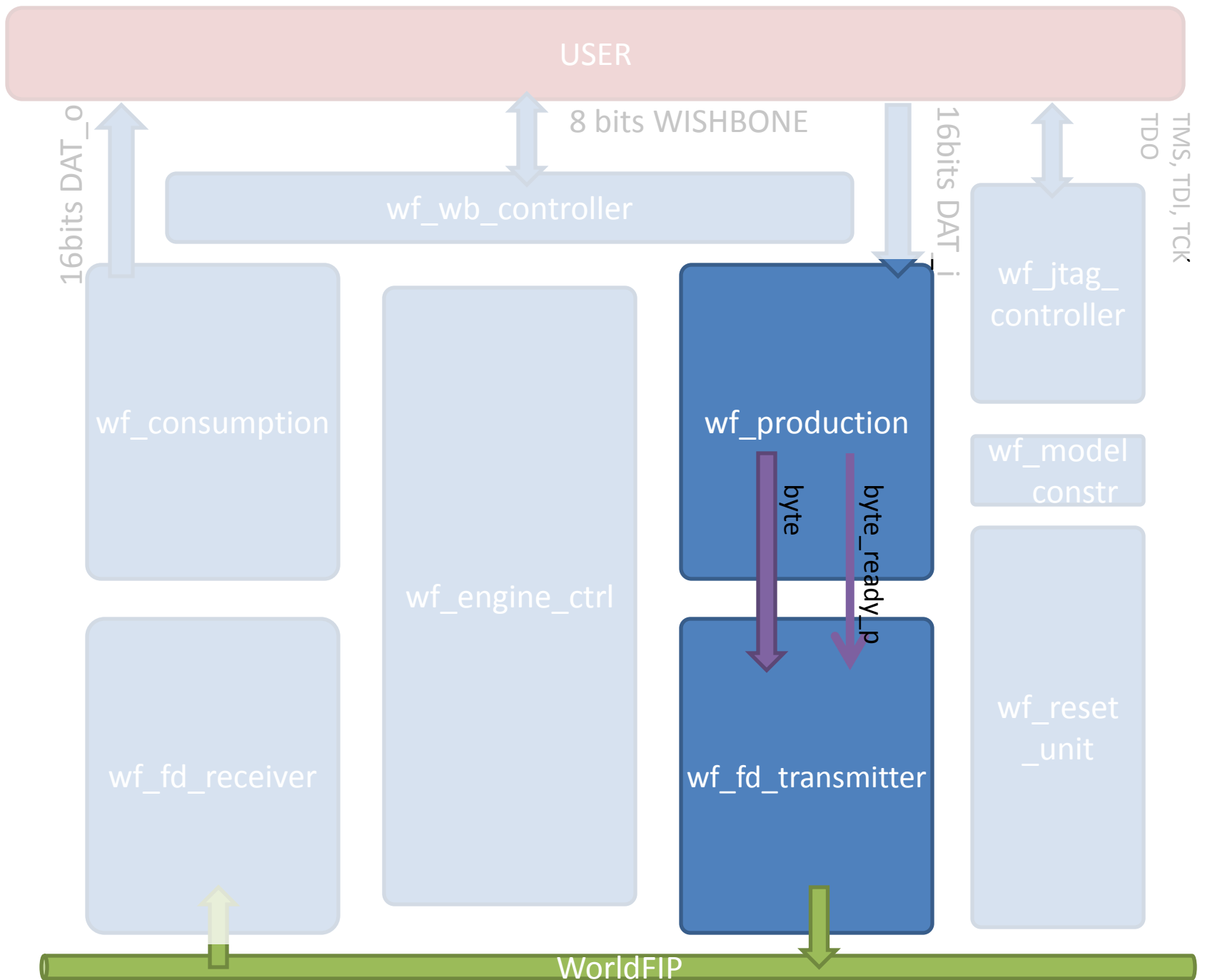












16 bits DAT_i

WB adr
WB data

wf_production

- Produced
- JTAG Produced

model_constr_byte

JTAG_byte

wf_prod_bytes_retriever

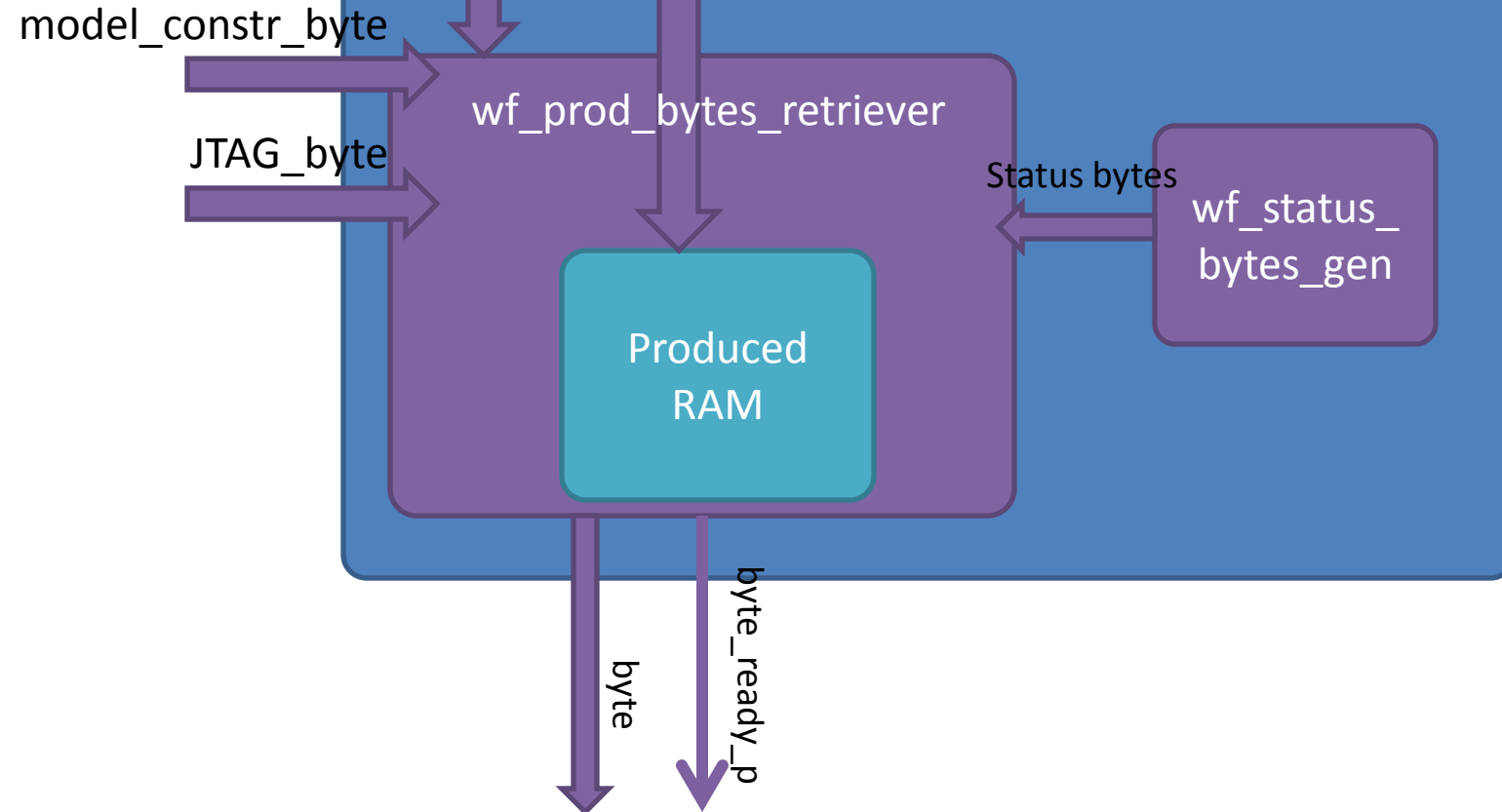
Produced
RAM

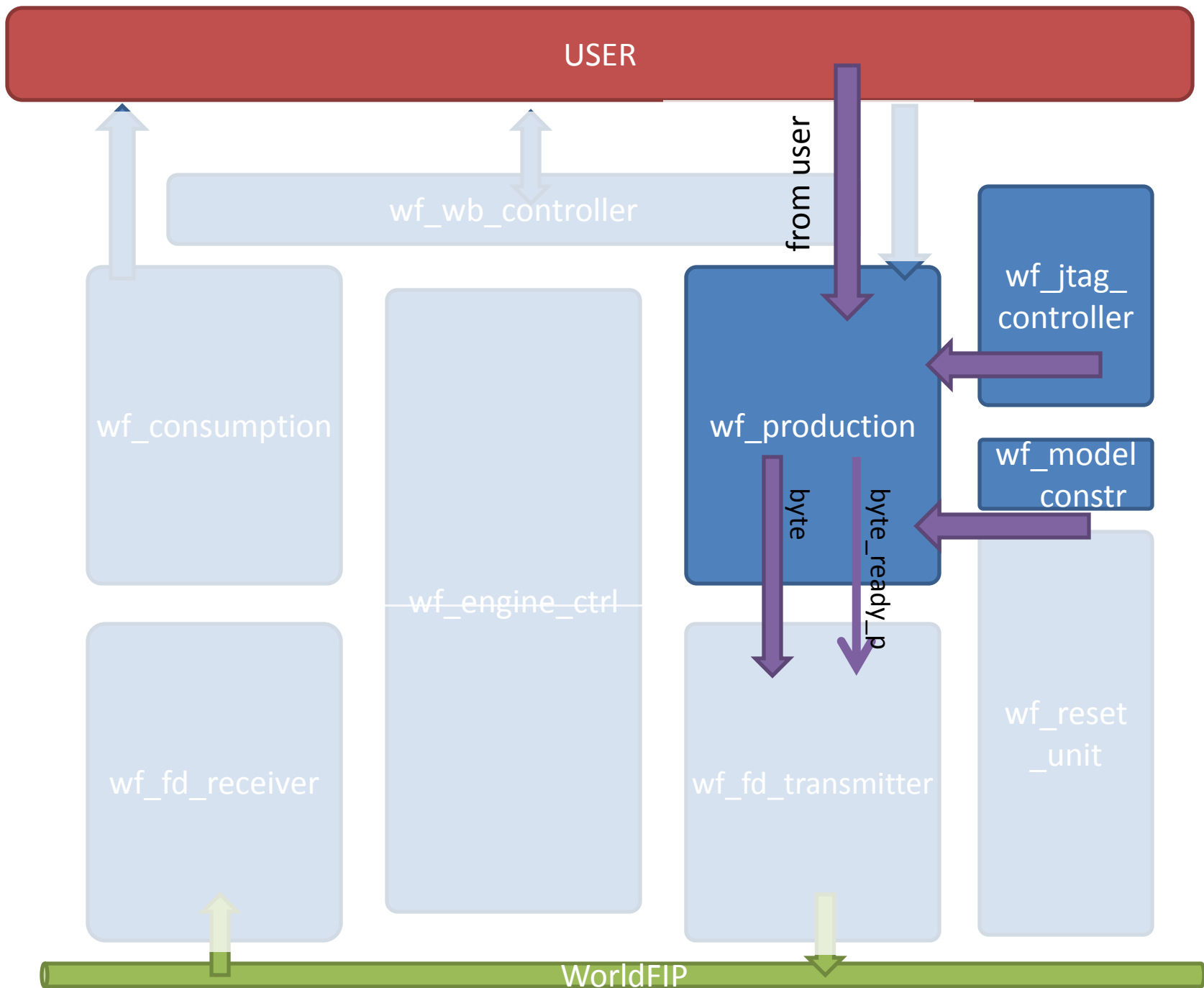
Status bytes

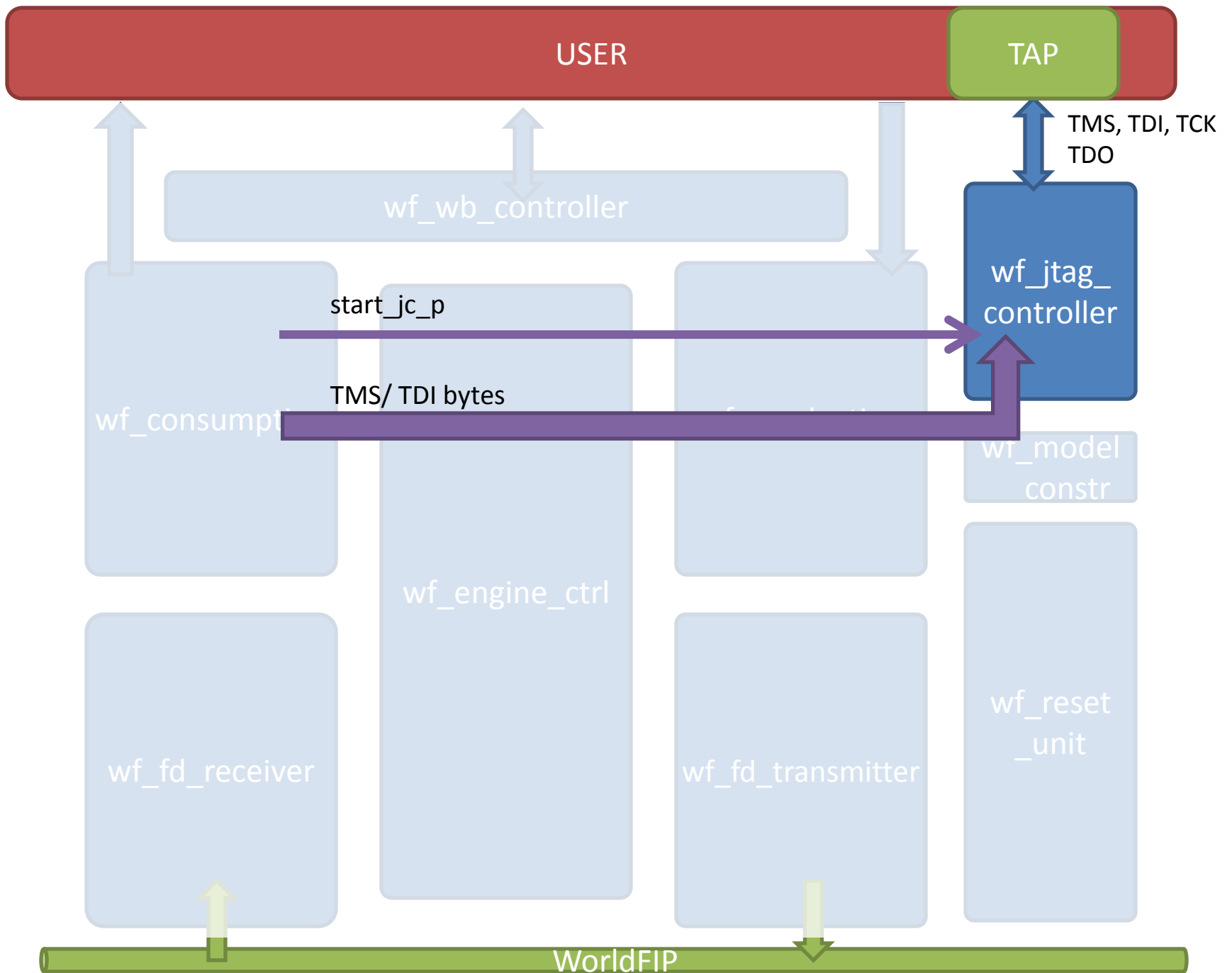
wf_status_
bytes_gen

byte

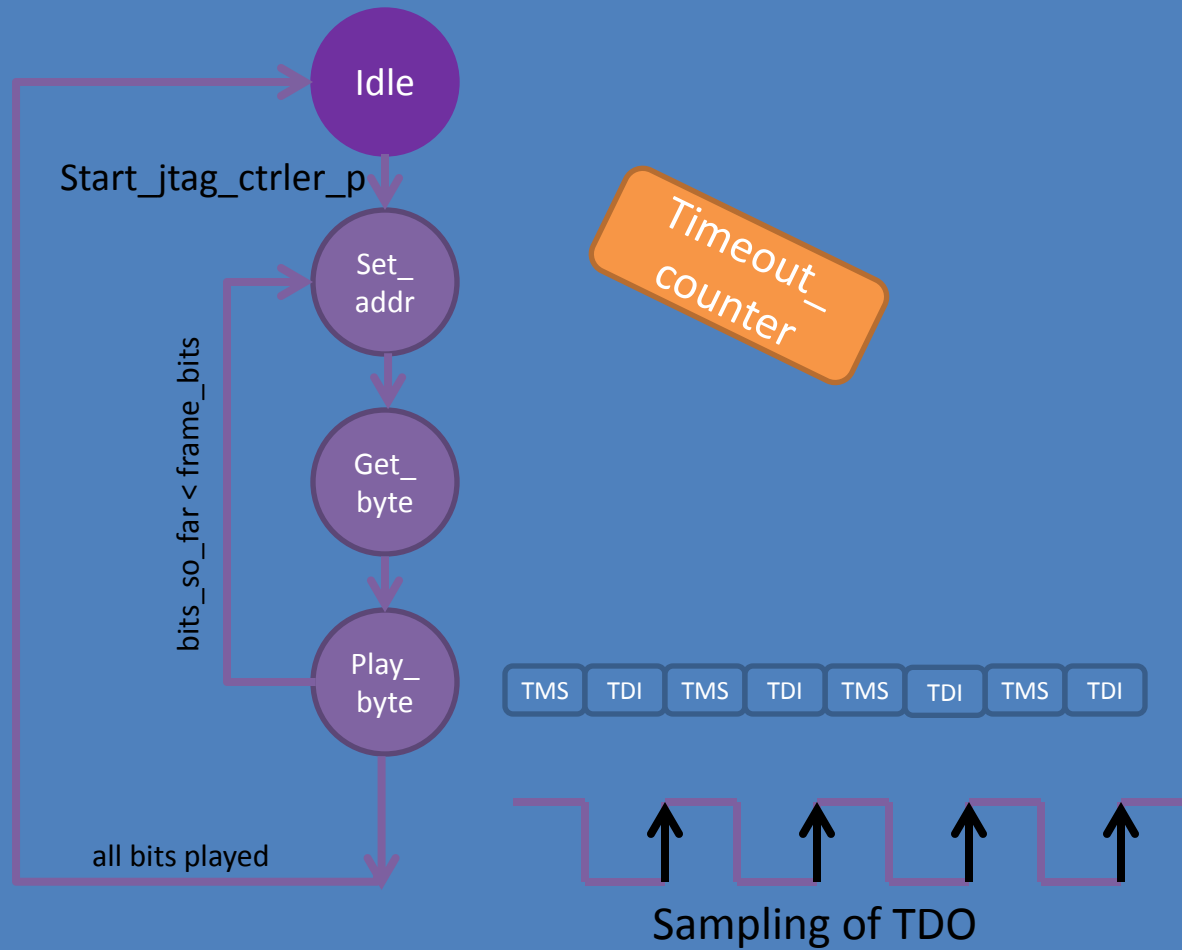
byte_ready_p

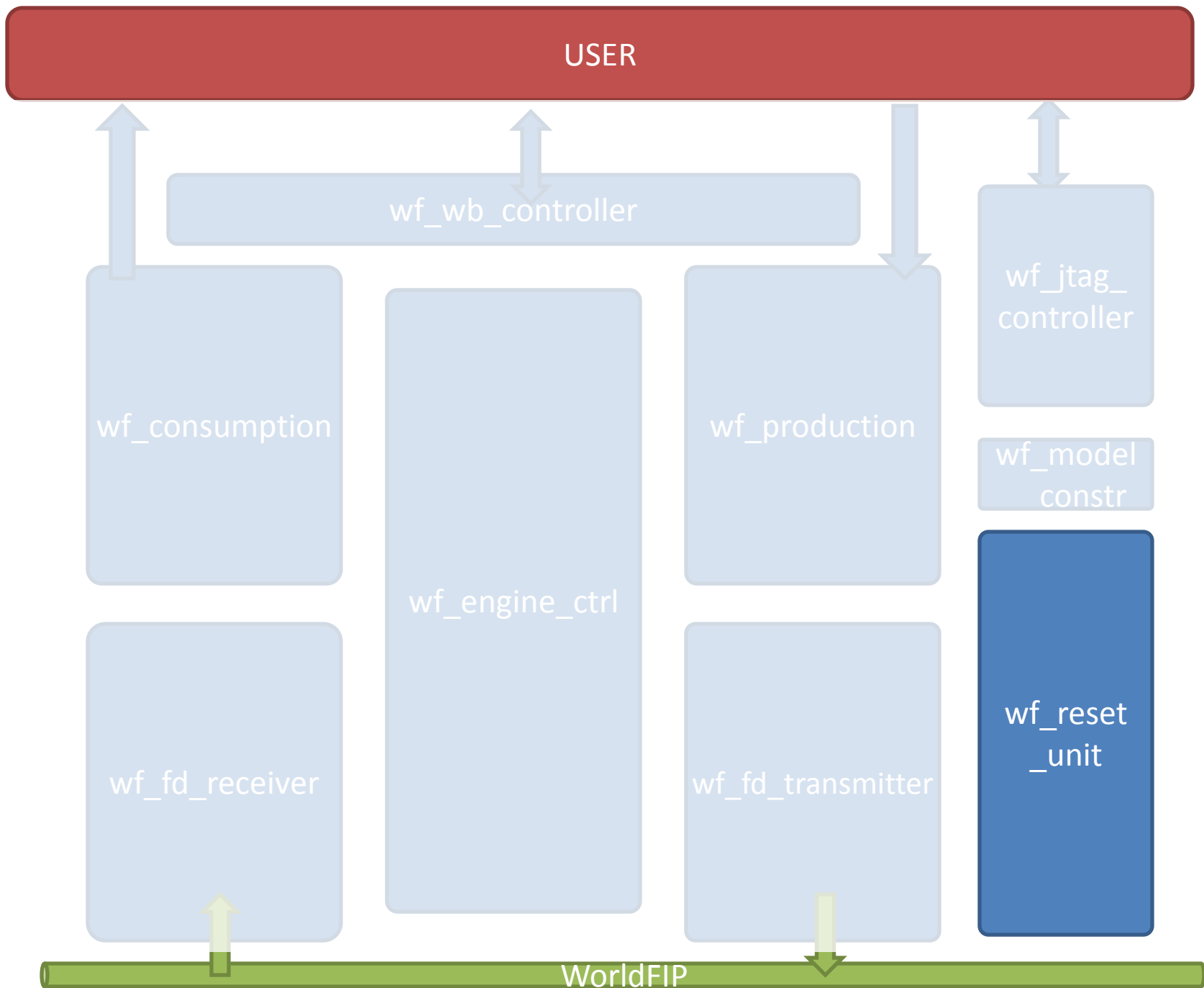


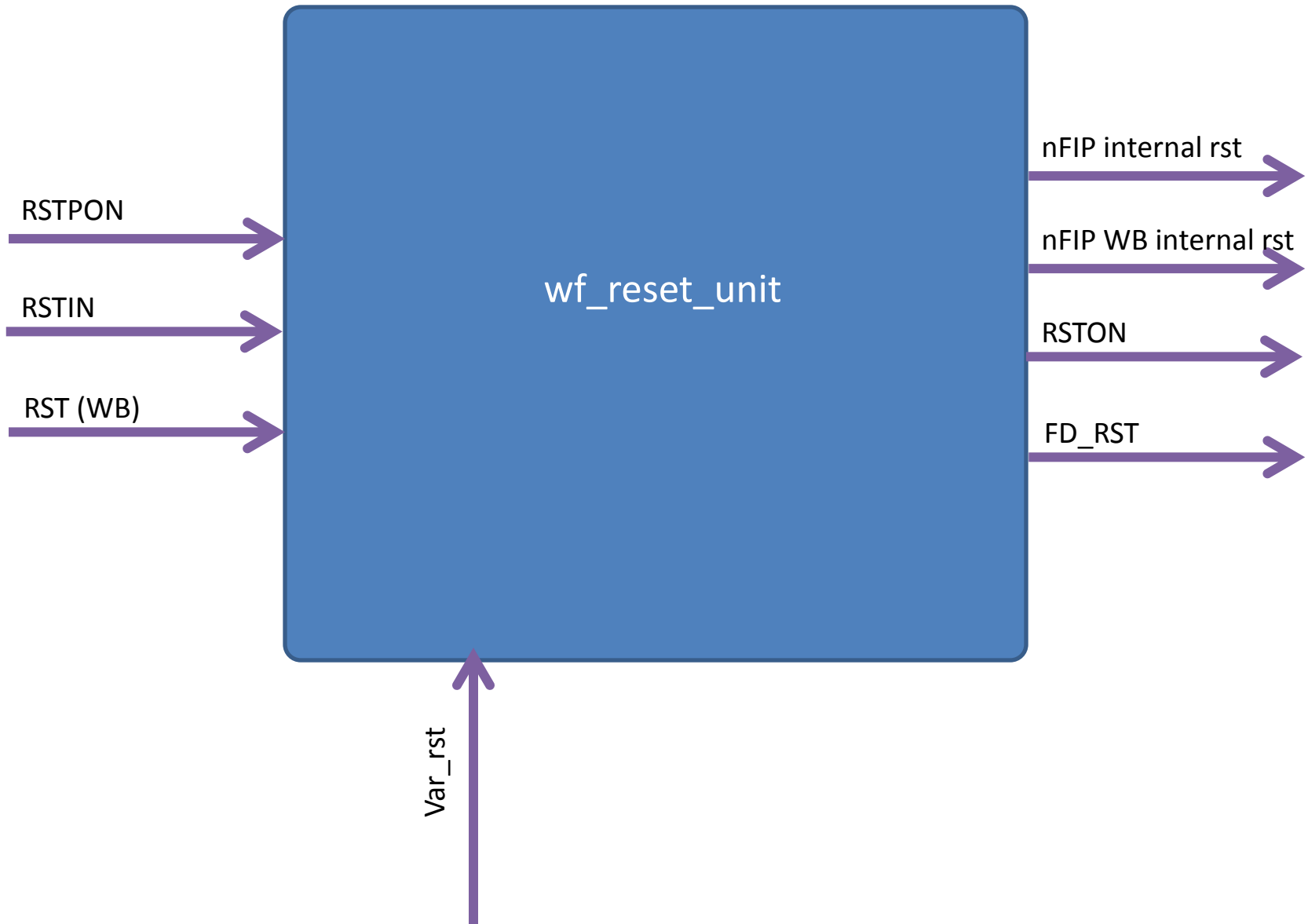




wf_JTAG_controller







Please focus on:

- wf_jtag_controller
- wf_engine_control
- wf_rx_deserializer
- wf_tx_transmitter
- wf_reset_unit