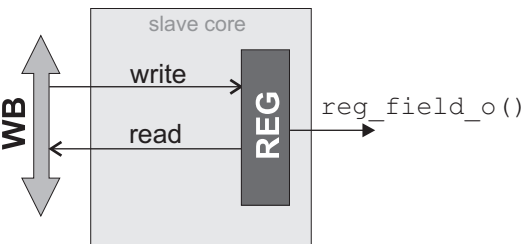


type: BIT/SLV/SIGNED/UNSIGNED

clocking: synchronous

access: RW/RO

cycle: 2 clocks

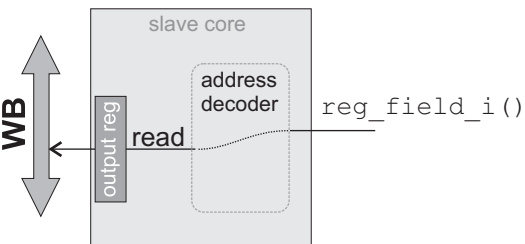


type: BIT/SLV/SIGNED/UNSIGNED

clocking: synchronous

access: RO/WO

cycle: 2 clocks

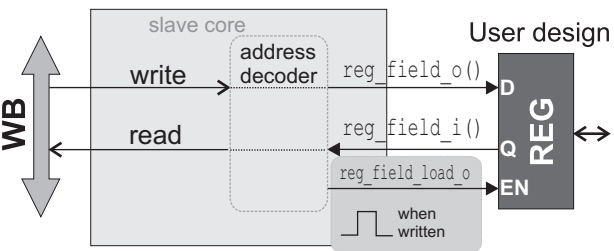


type: BIT/SLV/SIGNED/UNSIGNED

clocking: synchronous

access: RW/RW, LOAD_EXT

cycle: 2 clocks

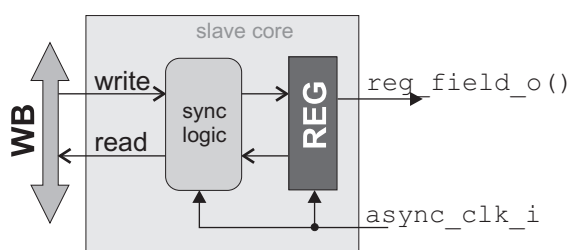


type: BIT/SLV/SIGNED/UNSIGNED

clocking: asynchronous

access: RW/RO

cycle: 3 clocks (BIT), 6 clocks (rest)

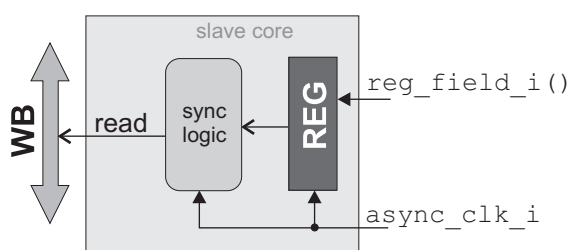


type: BIT/SLV/SIGNED/UNSIGNED

clocking: asynchronous

access: RO/WO

cycle: 3 clocks (BIT), 6 clocks (rest)



type: BIT/SLV/SIGNED/UNSIGNED

clocking: asynchronous

access: RW/RW, LOAD_EXT

cycle: 6 clocks

