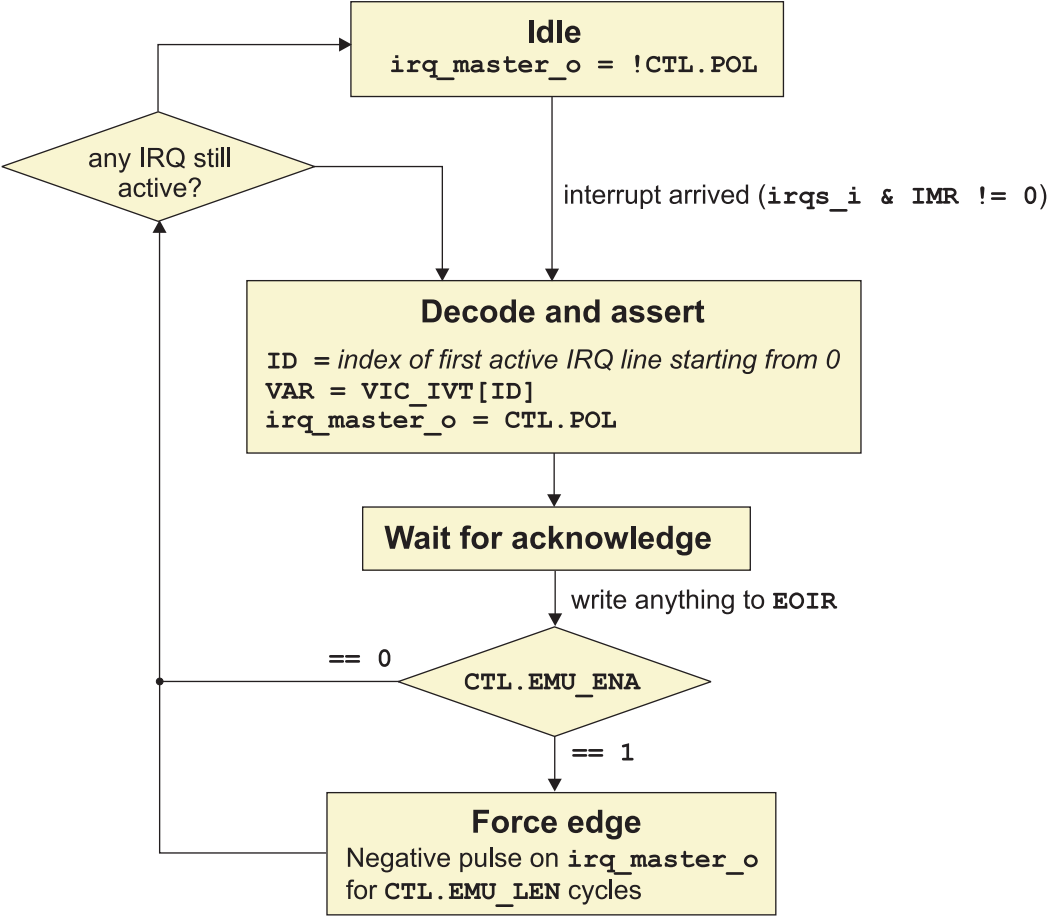
Idle
irq_master_o = !CTL.POL
any IRQ still active?
interrupt arrived (irqs_i & IMR != 0)
Decode and assert
ID = index of first active IRQ line starting from 0
VAR = VIC_IVT[ID]
irq_master_o = CTL.POL
Wait for acknowledge
write anything to EOIR
== 0
CTL.EMU_ENA
== 1
Force edge
Negative pulse on irq_master_o
for CTL.EMU_LEN cycles