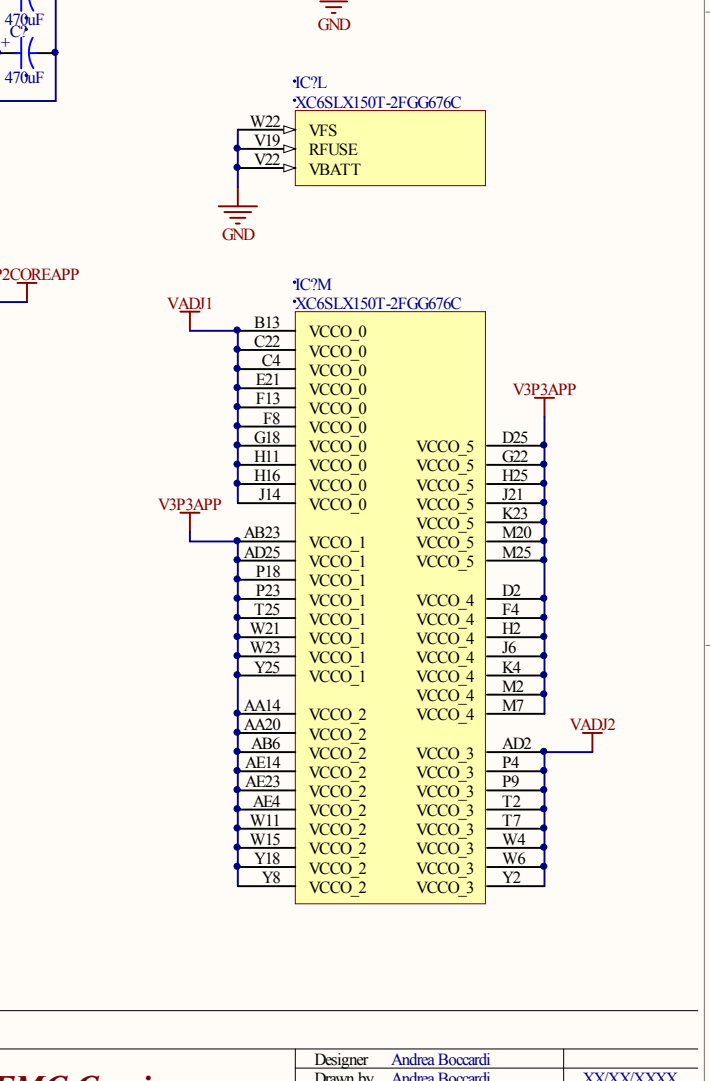

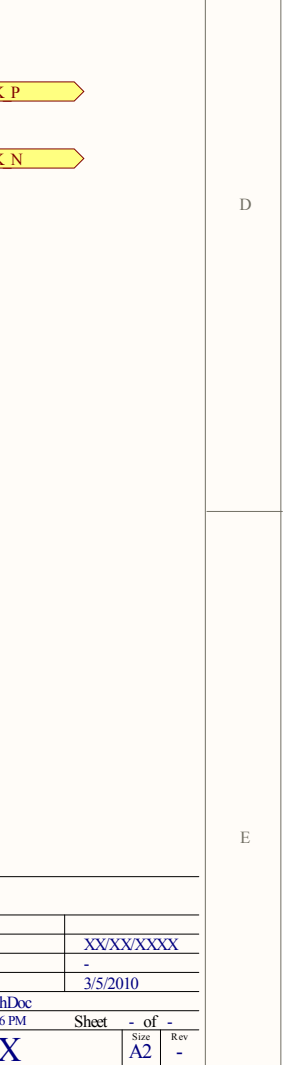
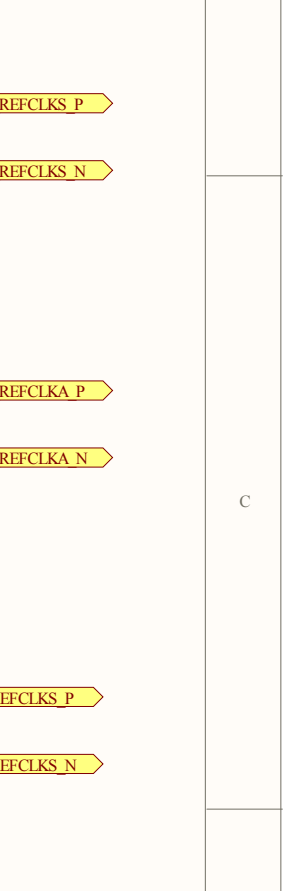
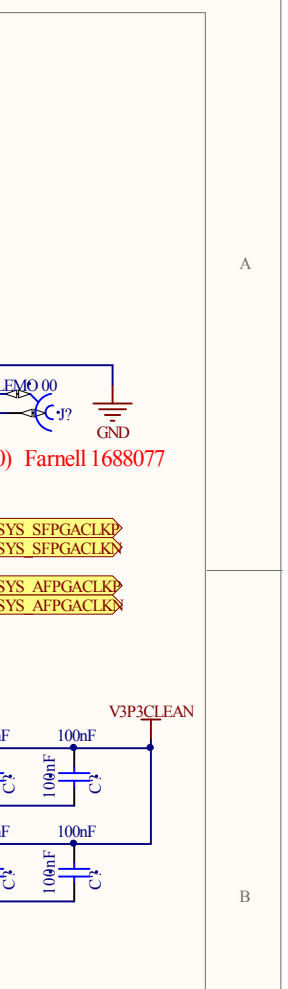
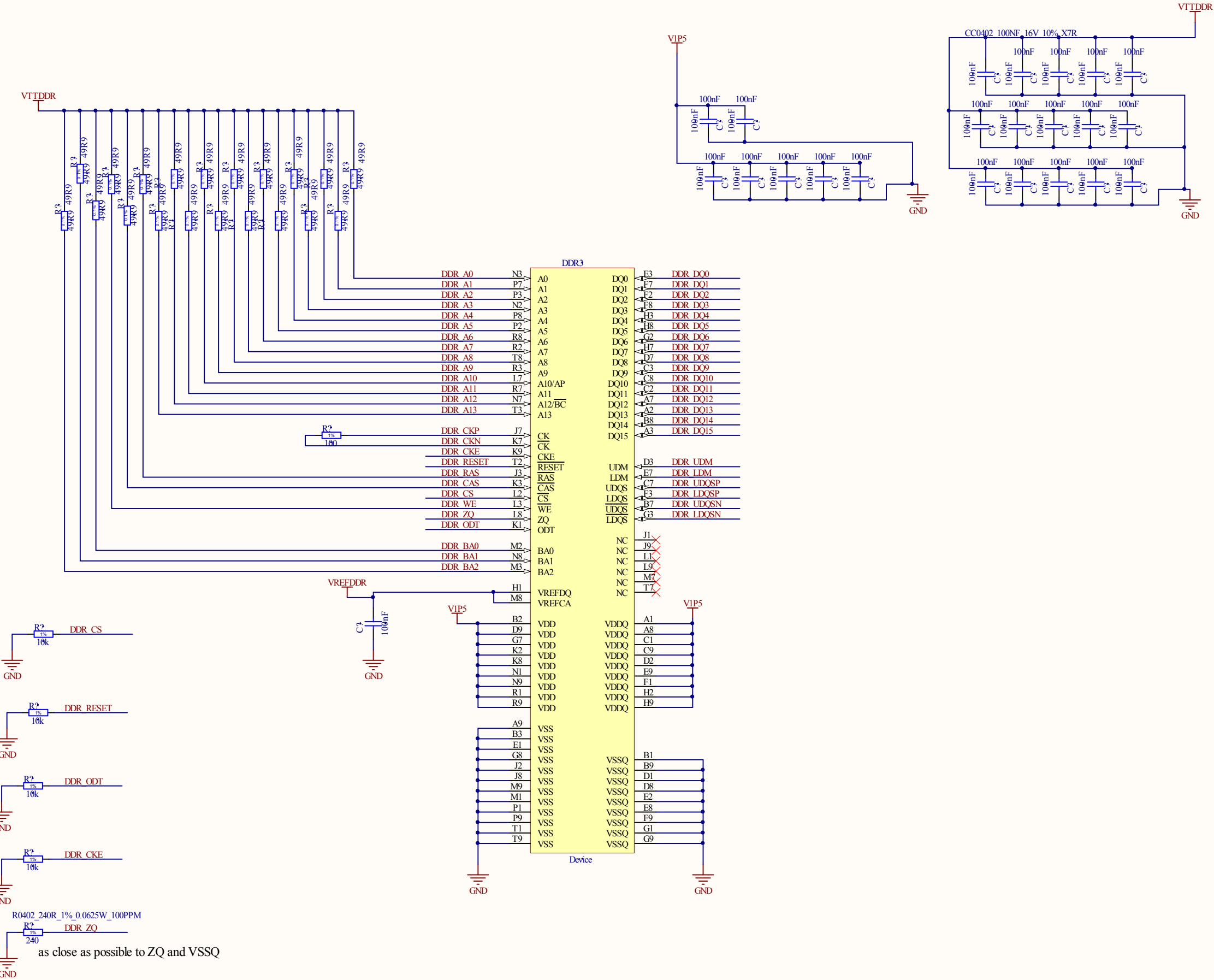
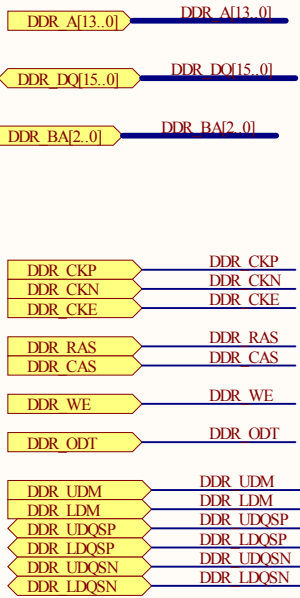


Figure 1: Block diagram of the test setup. The diagram shows a 4°C/K temperature source connected to a XC6SLX150T-2FGG676C device. The device has four input pins: C23, G21, A24, and F21, which are connected to AFPGA TDI, AFPGA TDO, AFPGA TCK, and AFPGA TMS respectively. The device also has two output pins, Y22 and Y23, which are connected to TMS and SUSPEND respectively.

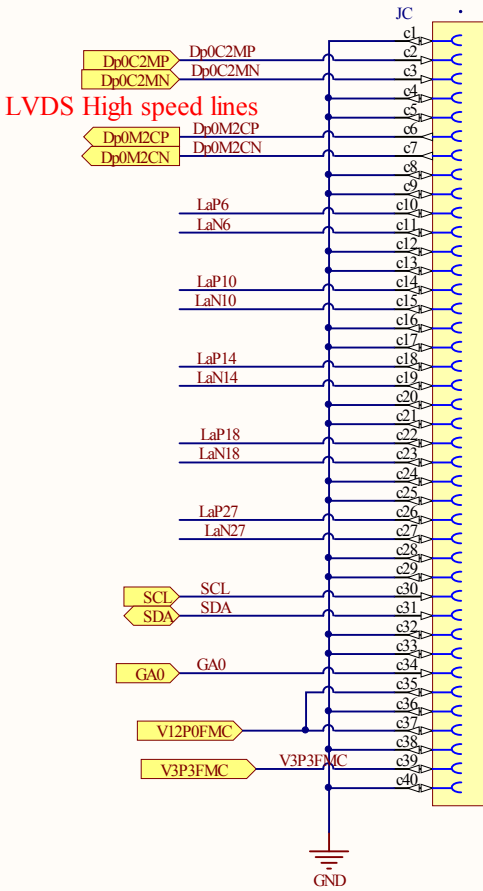
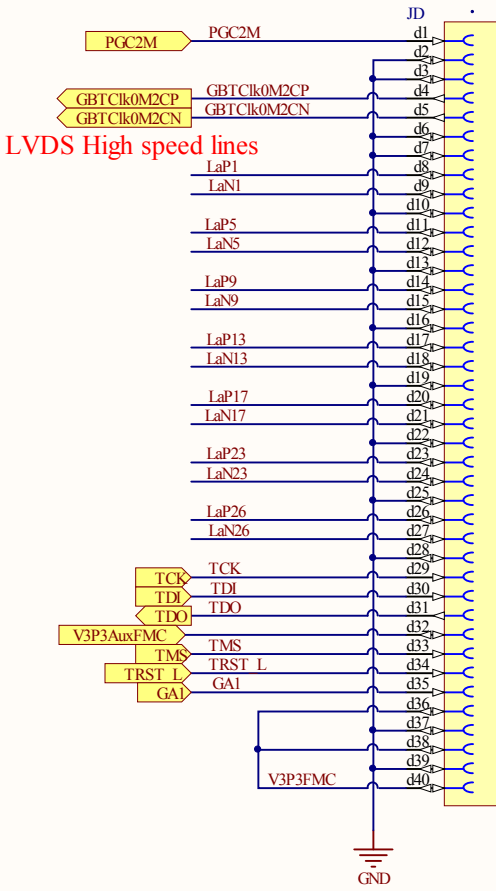
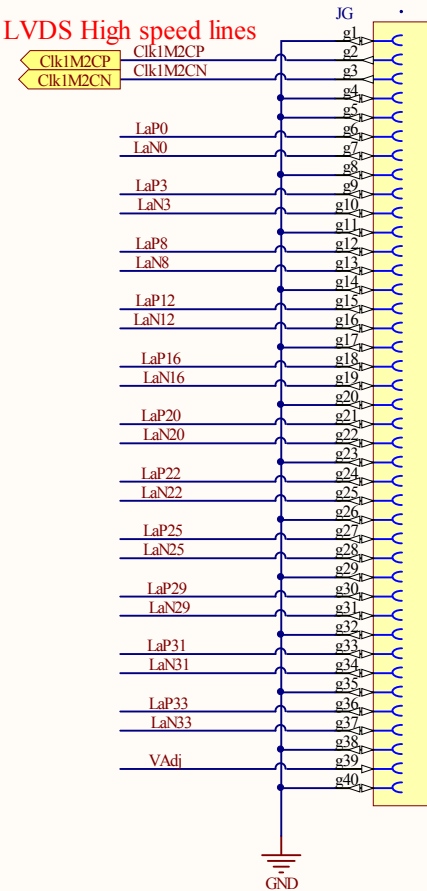
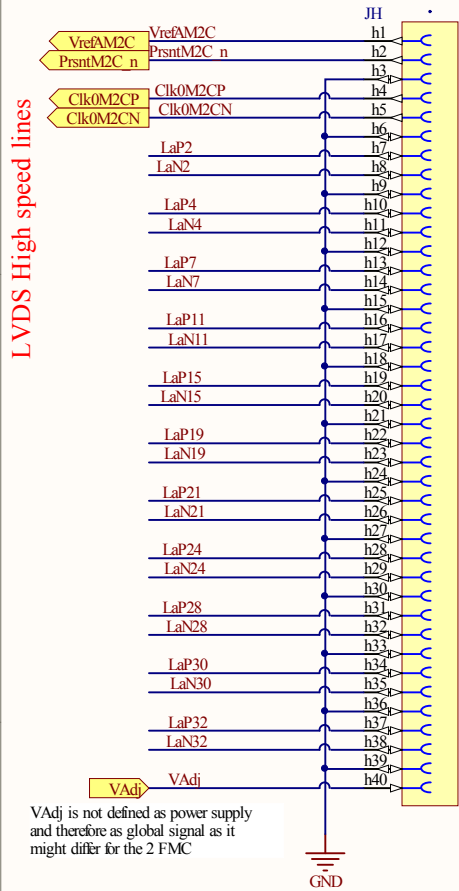


Project/Equipment		-	
Document		Designer: <u>Andrea Boccardi</u> Drawn by: <u> </u> Check by: <u> </u> Last Mod.: <u> </u> File: <u>AFPGA SchlDoc</u> Print Date: <u>3/5/2010 13:15:35 PM</u>	
		<i>VME FMC Carrier</i> <i>Application FPGA</i>	
European Organization for Nuclear Research CH-1211 Genève 23 - Switzerland		Sheet <u> 1 </u> of <u> 1 </u> Size A2 Rev <u> 0 </u>	
		EDA-XXXXX-VX-X	

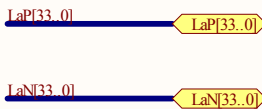




Low Pin Count Rows

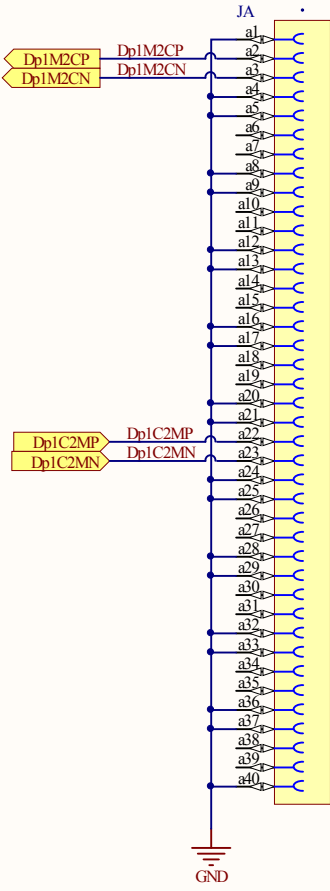
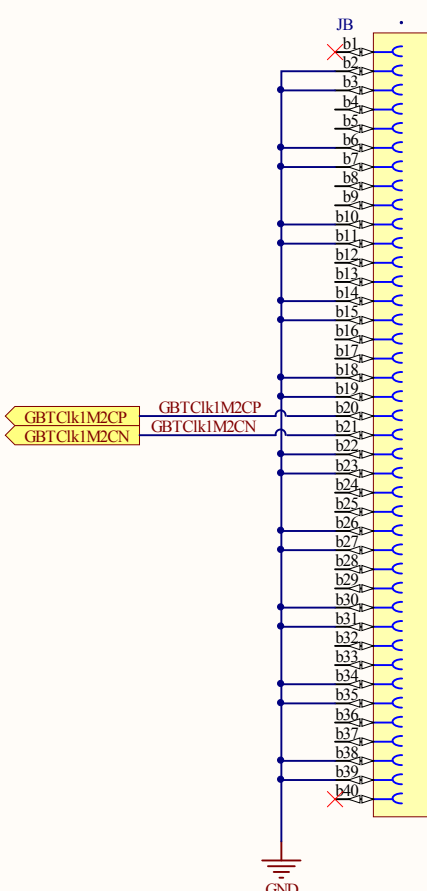
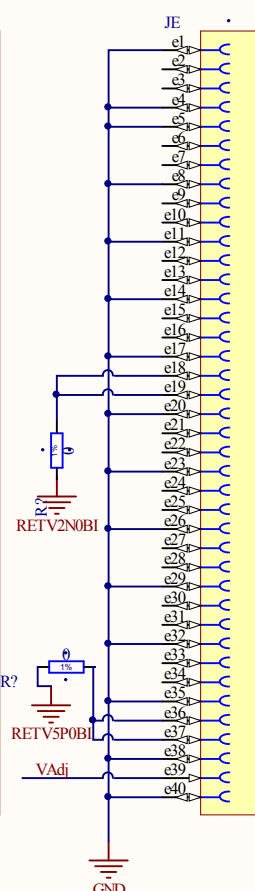
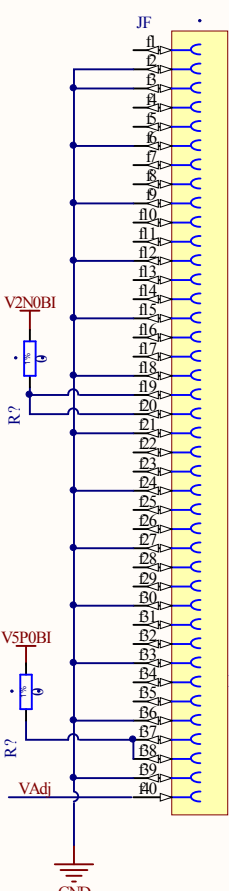
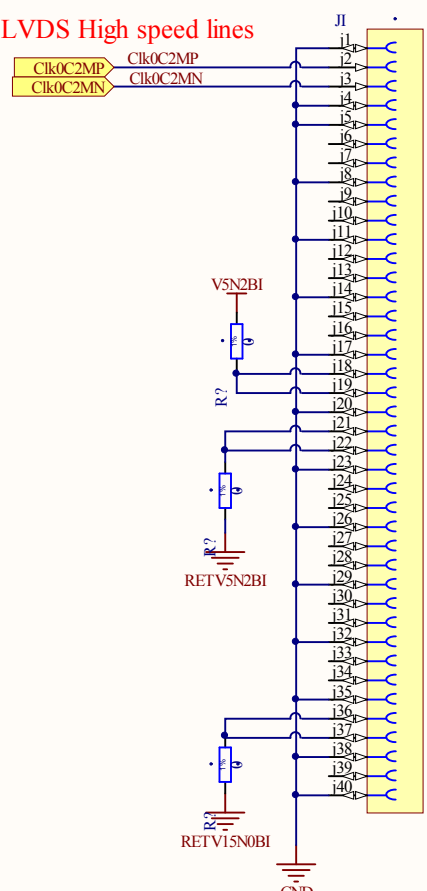
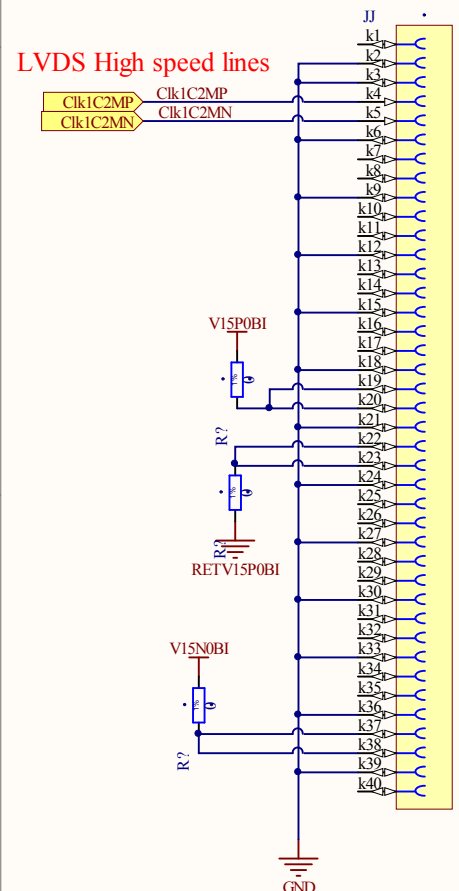



LaP and LaN are LVDS lines

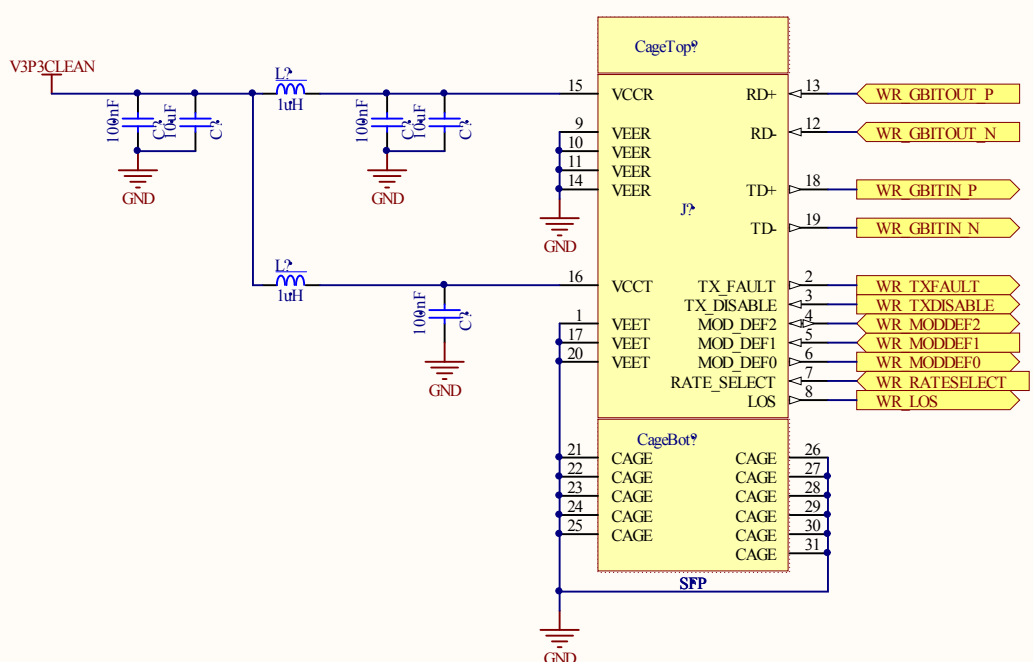
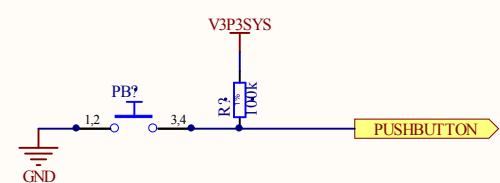
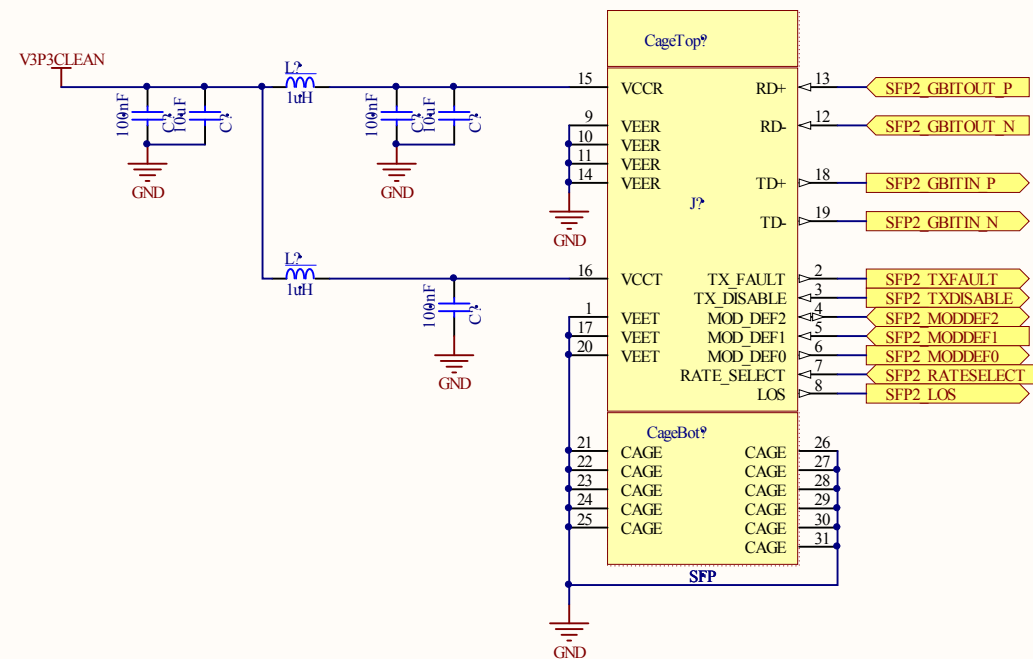
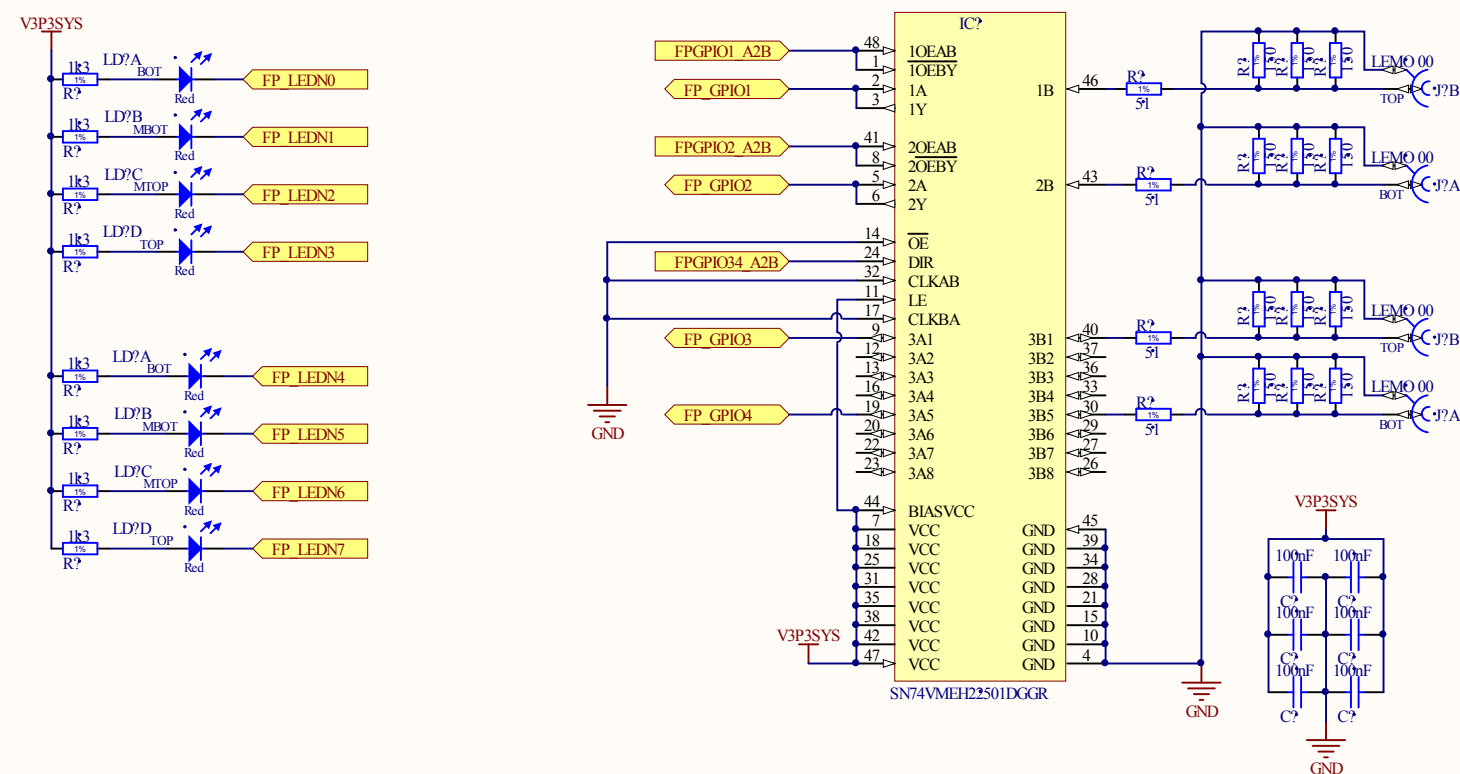


NB: the LVDS pairs must have a differential impedance of 100 ohm and be routed with no skew between the P and the N lines. The skew between the various La pairs should be kept as low as possible.

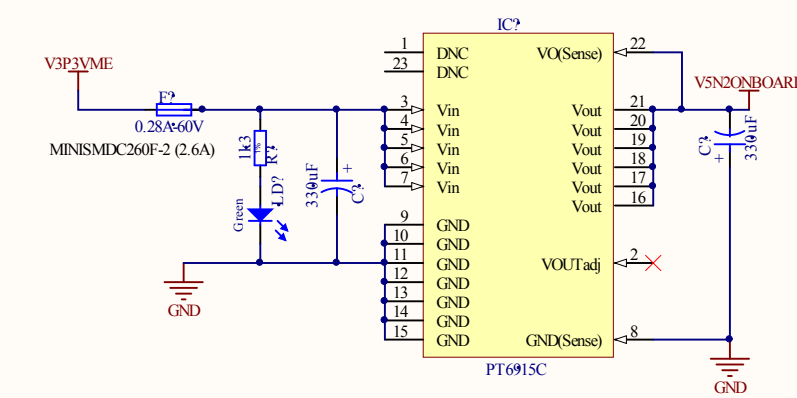
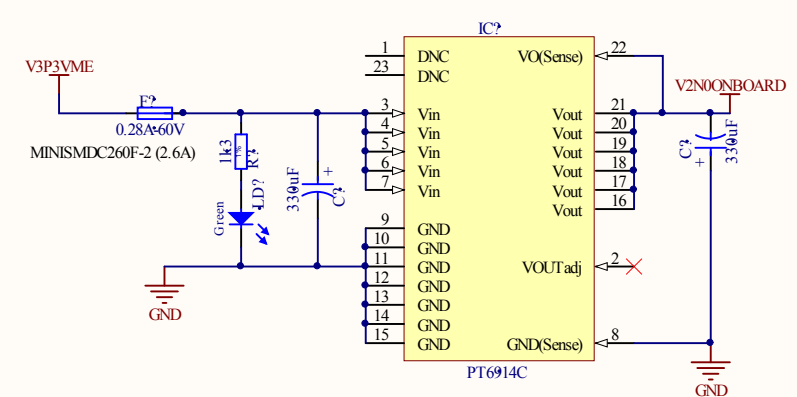
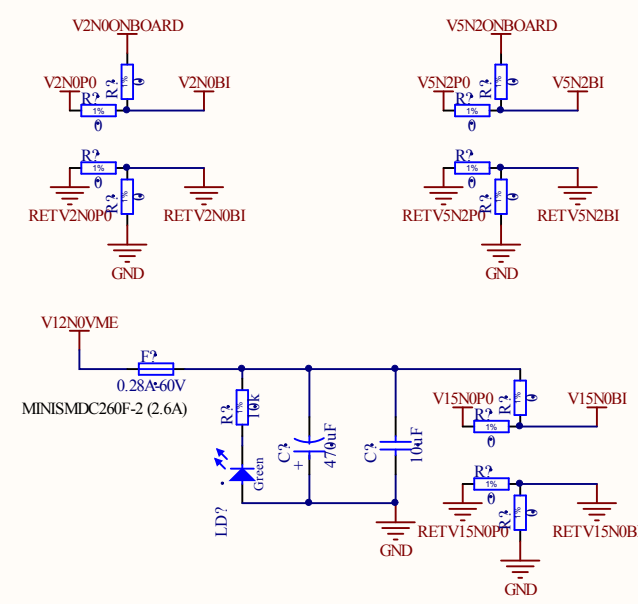
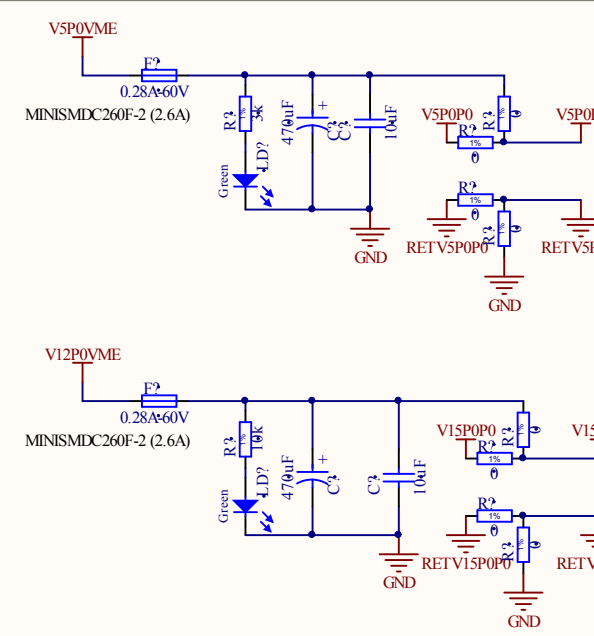
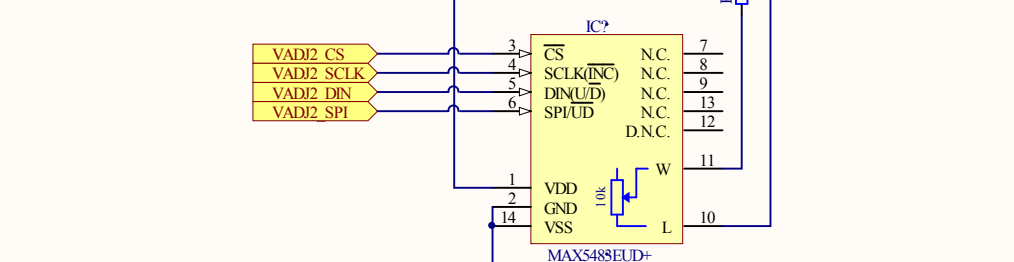
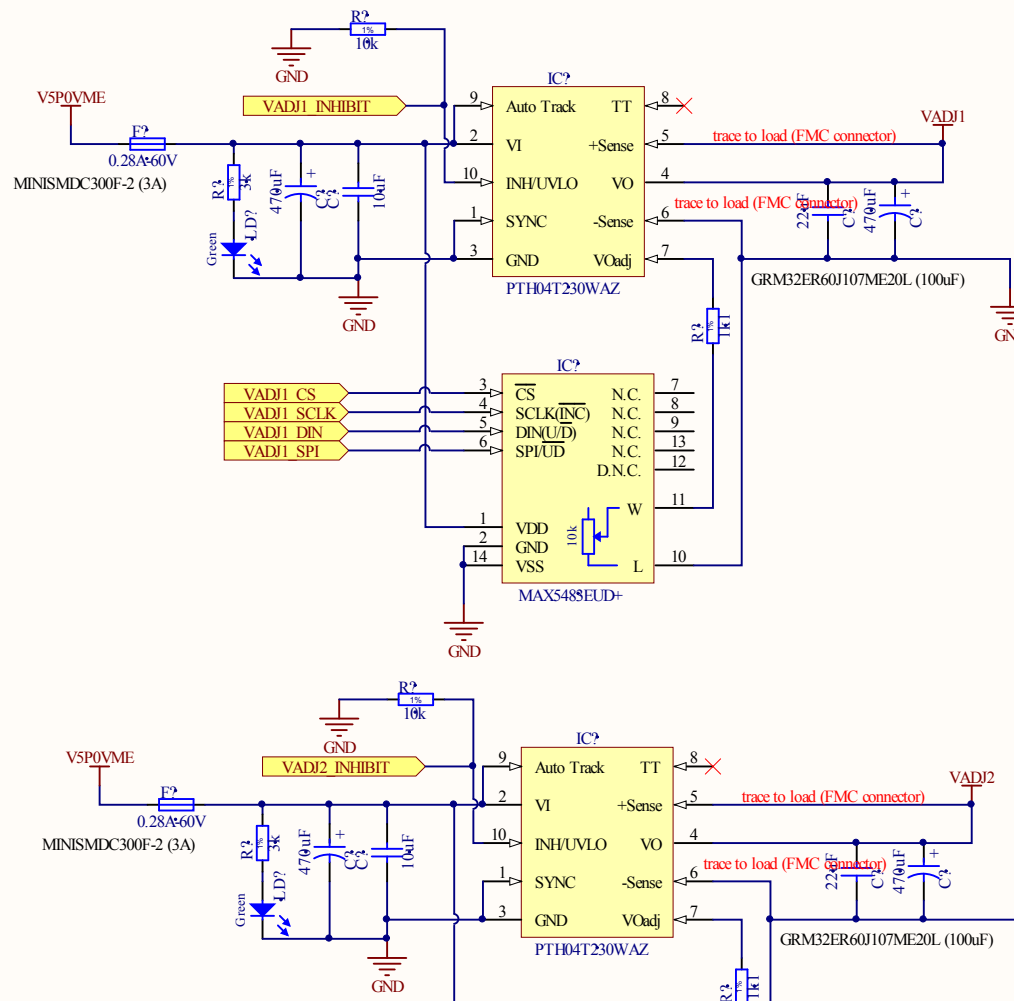
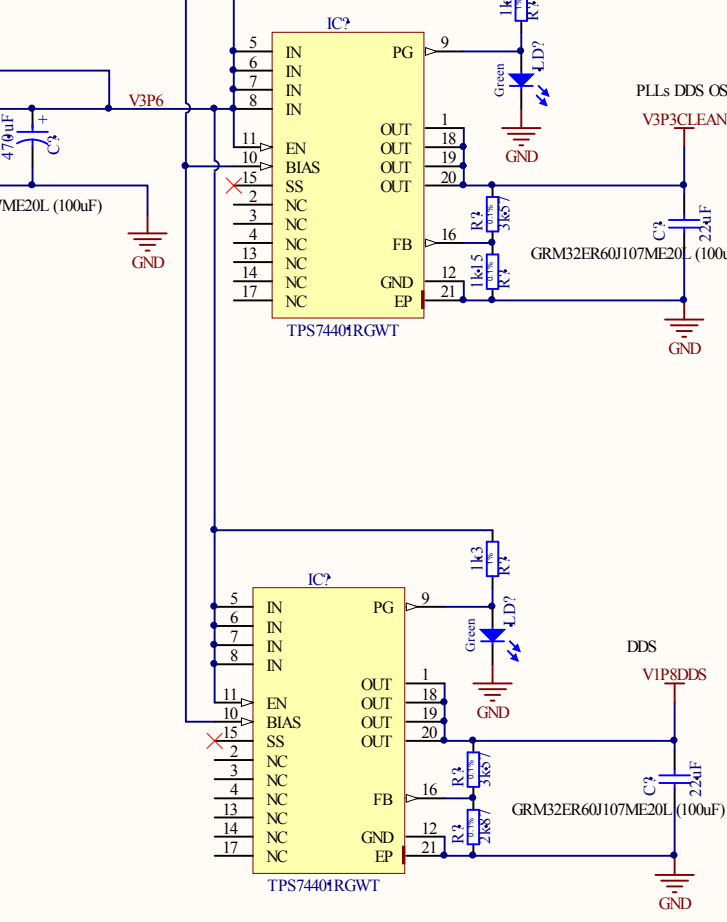
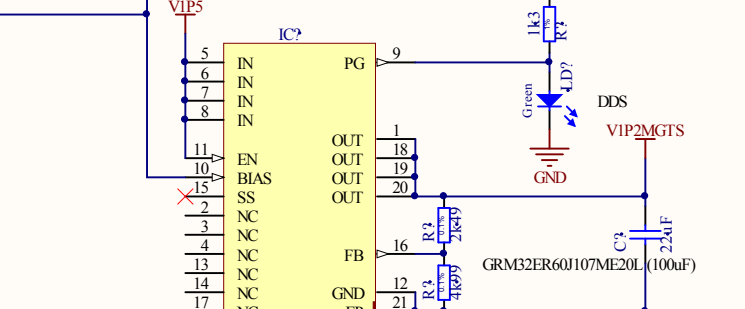
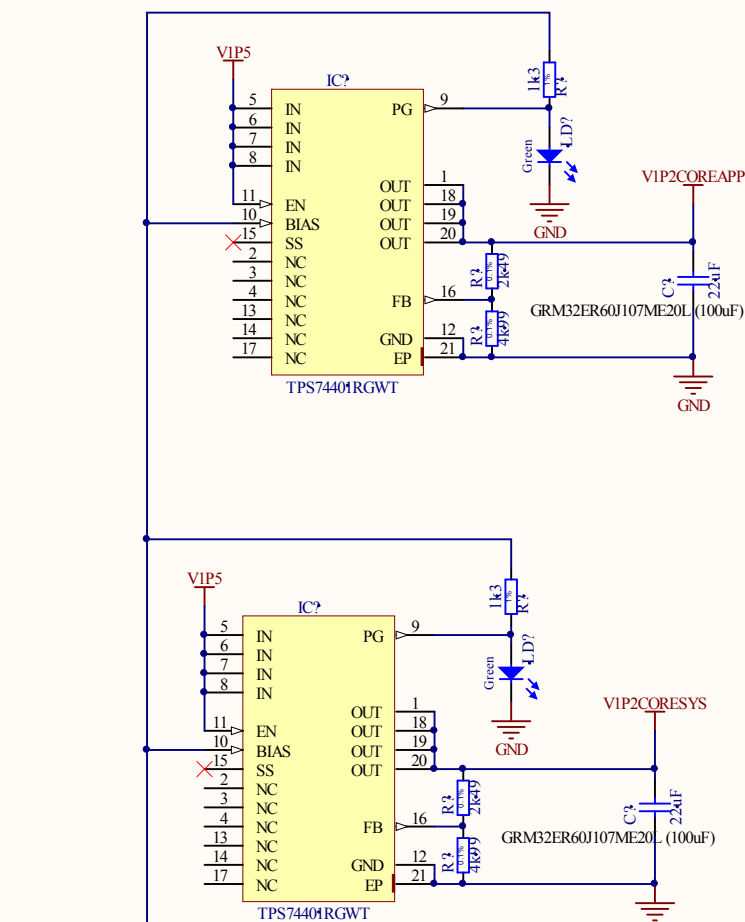
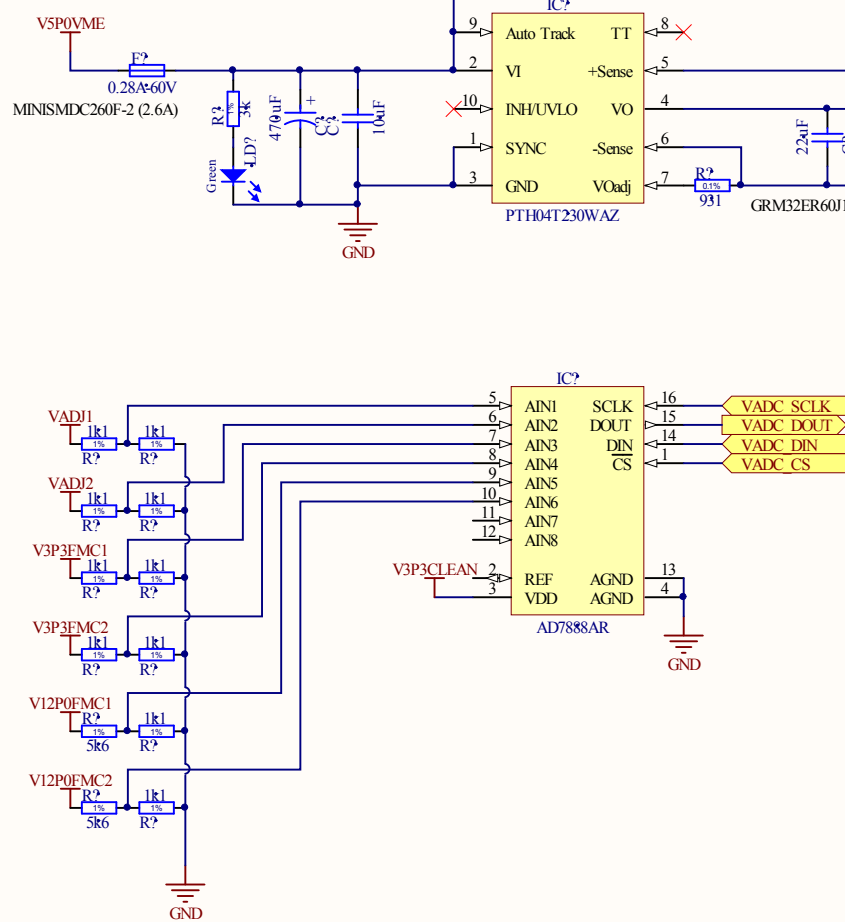
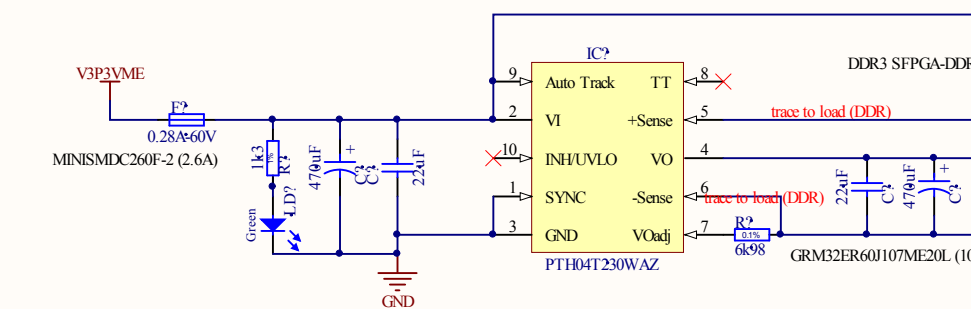
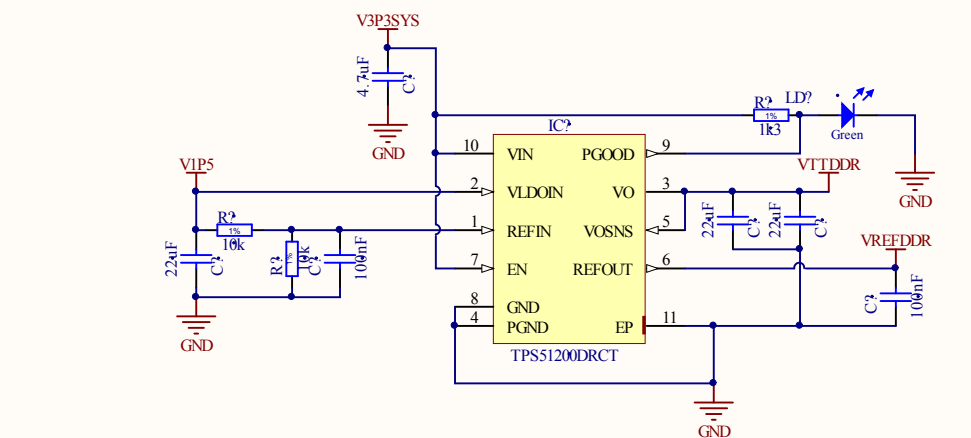
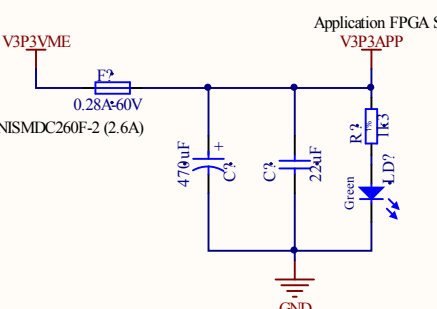
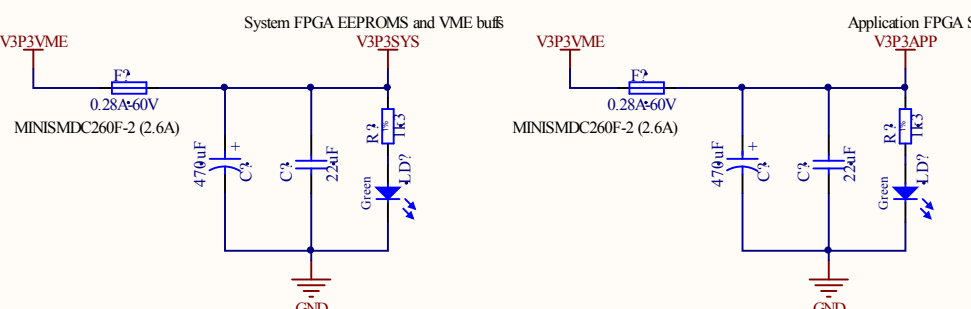
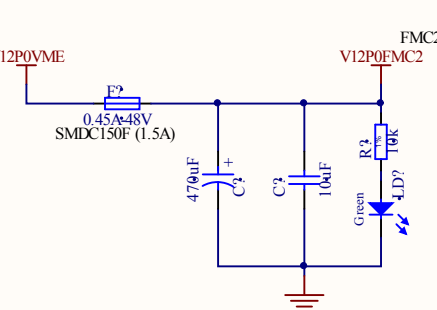
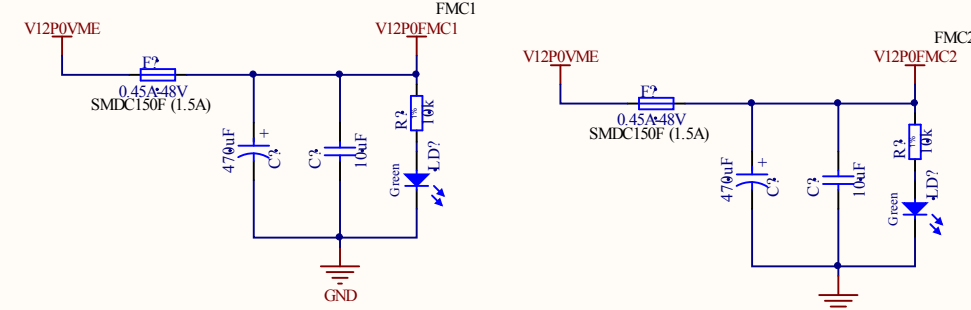
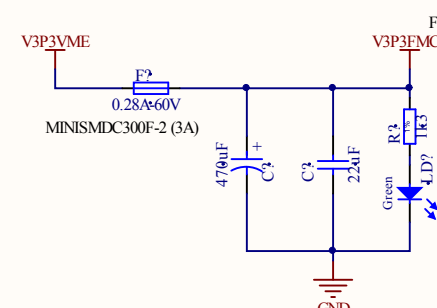
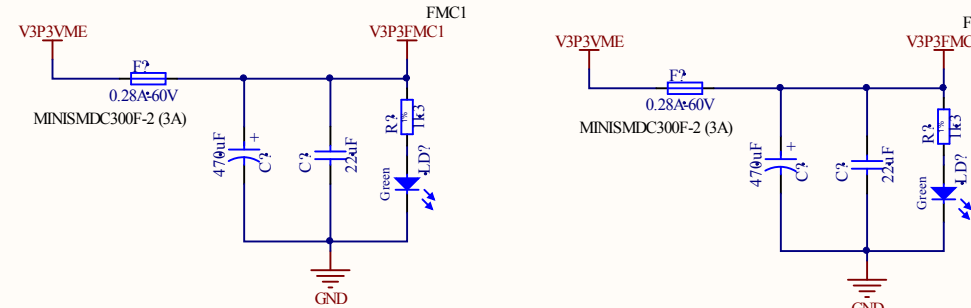
High Pin Count Rows



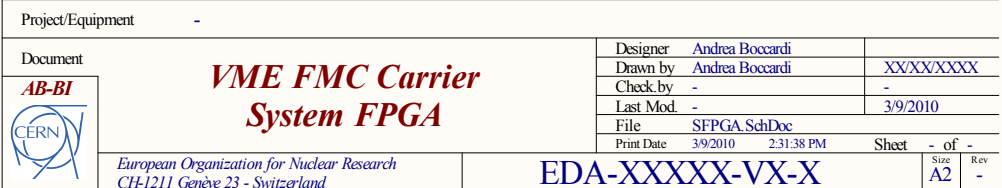
Project/Equipment		-		
Document		Designer	Andrea Boccardi	
<div>BE-BI-OP</div> <div></div>	<div>VME FMC Carrier</div> <div>FMC Connector</div>	Drawn by	Andrea Boccardi	XX/XX/XXXX
		Check by	-	-
		Last Mod.	-	3/5/2010
		File	FmcConnector.SchDoc	
European Organization for Nuclear Research CH-1211 Genève 23 - Switzerland		Print Date	3/9/2010 2:31:37 PM	Sheet - of -
		EDA-XXXXXX-VX-X		Size A3
				Rev -



replace ALL the leds for ROHM - SML-311DTT86K - LED 0603 FAIBLE COURANT ORANGE (farnell 1685067)



Project/Equipment	-	Designer	Andrea Boccardi	XX/XX/XXXX
Document	-	Drawn by	Andrea Boccardi	3/5/2010
Check by	-	Last Mod.	-	-
File	PowerSupplies_SchDoc	Print Date	3/9/2010 2:31:37 PM	Sheet - of -
EDA-XXXXX-VX-X		Rev	A2	-



Replace with the 3V3 version (same pinout)

