TLU event structure

BIT #	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
WORD 0	ev. type [4 bit]				trigger data [12 bit]												time stamp (25 ns) [upper 16 bit]															
WORD 1	time stamp (25 ns) [lower 32 bit]																															
WORD 2	Fine TS 0 [8 bit]								Fine TS 1 [8 bit]								Fine TS 2 [8 bit]								Fine TS 3 [8 bit]							
WORD 3	event number [32 bit]																															
WORD 4	Fine TS 4 [8 bit]								Fine TS 5 [8 bit]								Fine TS 6 [8 bit]								Fine TS 7 [8 bit]							
WORD 5	Fine TS 8 [8 bit]								Fine TS 9 [8 bit]								spare 32 bit															