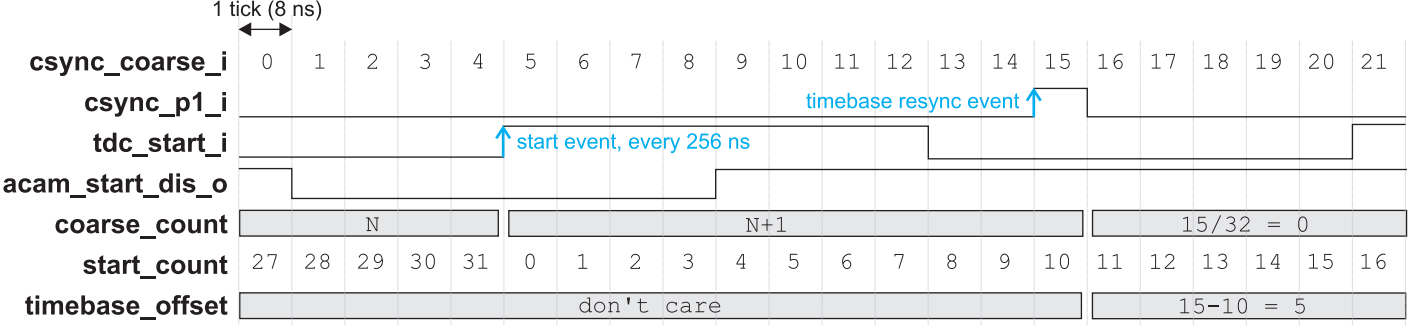
1 tick (8 ns)
csync_coarse_i
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21
csync_p1_i
timebase resync event
tdc_start_i
start event, every 256 ns
acam_start_dis_o
coarse_count
N
N+1
15/32 = 0
start_count
27 28 29 30 31 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16
timebase_offset
don't care
15-10 = 5