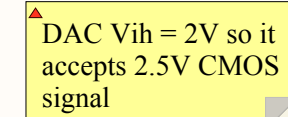


A



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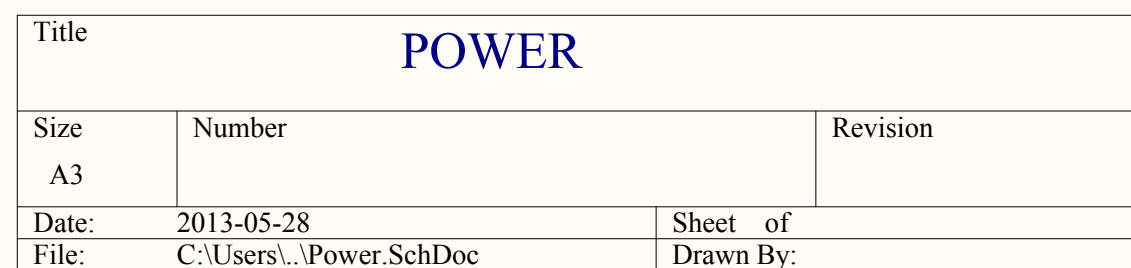
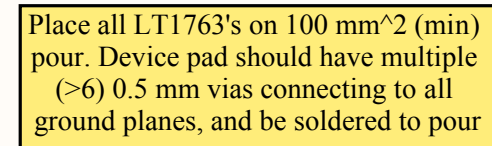


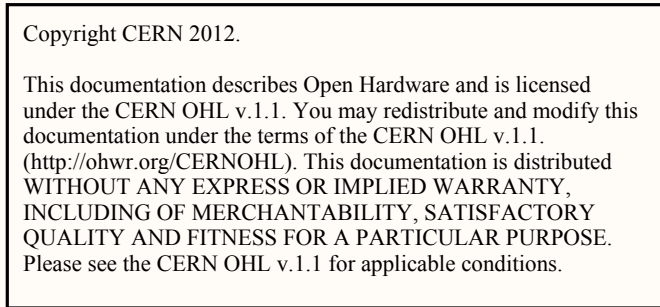
▲ DAC output range: 0V to 2.5V

ull voltage is $+1.5\text{V} \pm 1\text{V}$.
ull range is ± 10 ppm for $\pm 1\text{V}$.
e slope (Positive voltage for
e frequency shift).

Title		CLOCKING	
Size A3	Number	Revision	
Date:	2013-05-28	Sheet	of
File:	C:\Users\...\Clocking.SchDoc	Drawn By:	

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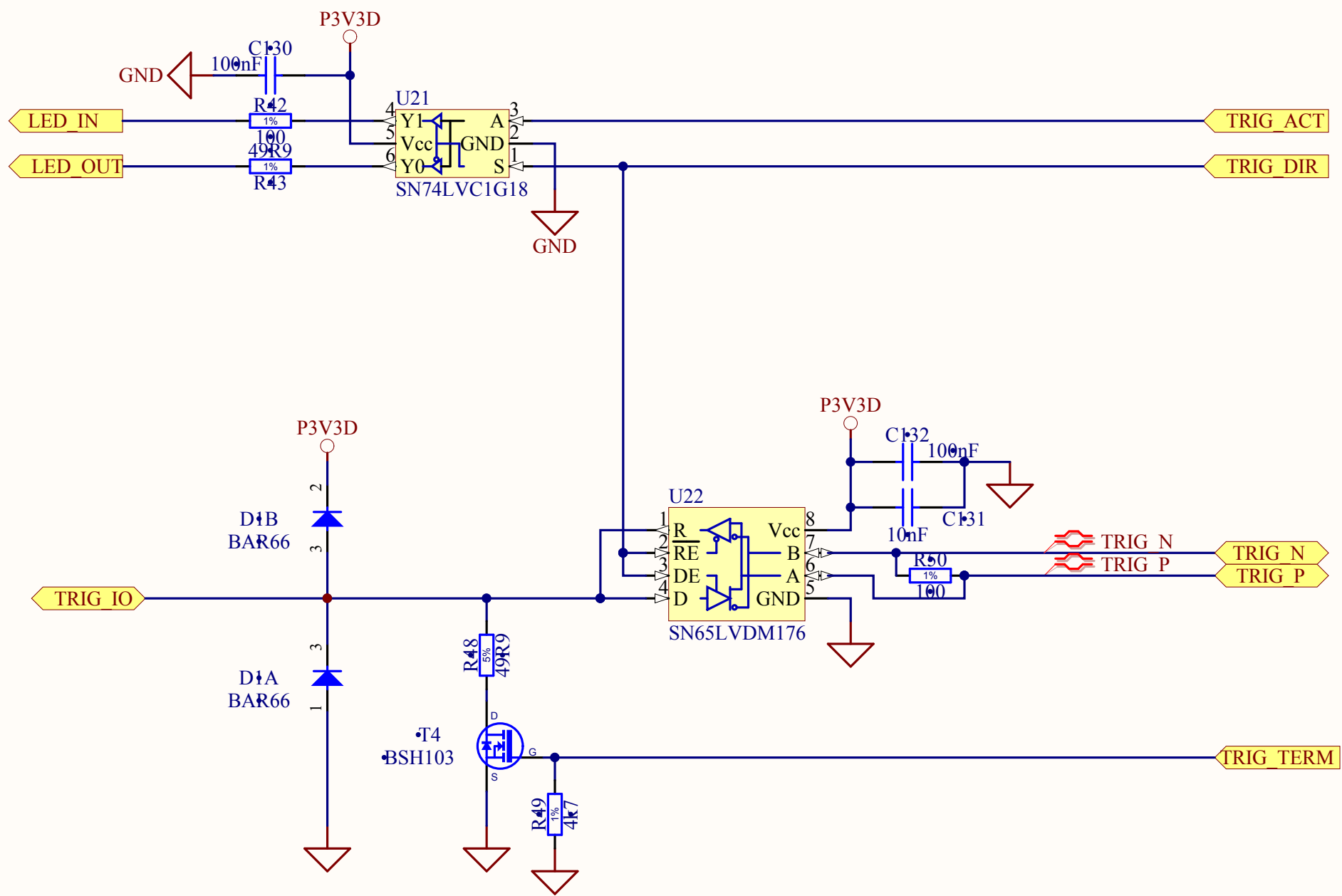




<div> <div>Title</div> <div>FMC</div> </div>		
Size A3	Number	Revision
Date:	2013-05-28	Sheet of
File:	C:\Users\...\FMC DDS.SchDoc	Drawn By:

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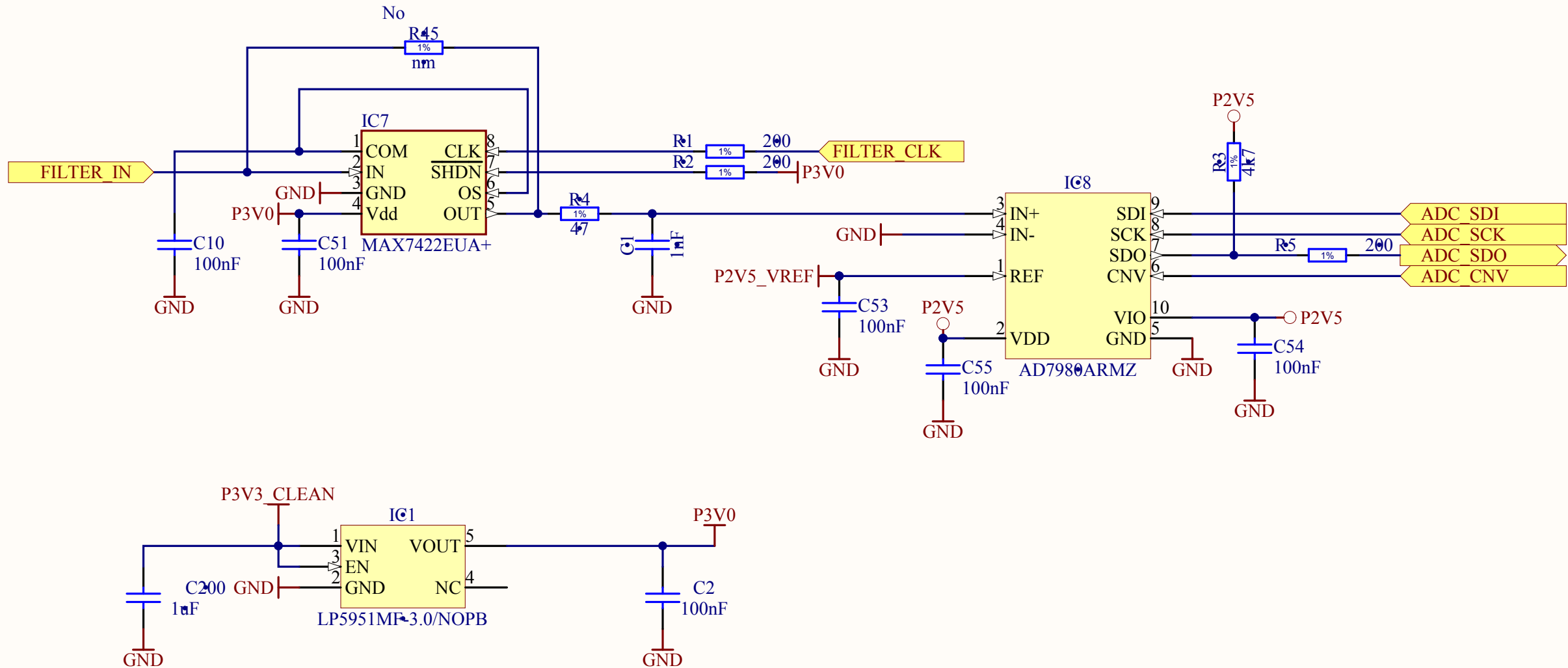


Title			TRIGGER	
Size	Number		Revision	
A3				
Date:	2013-05-28		Sheet of	
File:	C:\Users\...\Trigger.SchDoc		Drawn By:	Tony Rohlev

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▲ Output Voltage Range 0.25 VDD- 0.25



Title		
ADC FILTER		
Size	Number	Revision
A4		
Date:	2013-05-28	Sheet of
File:	C:\Users\...\ADC filter.SchDoc	Drawn By:

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