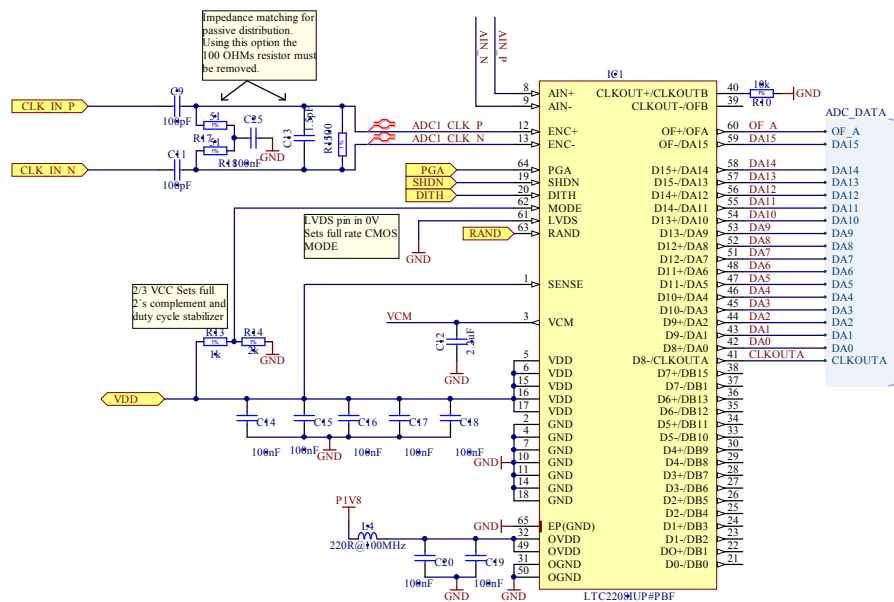
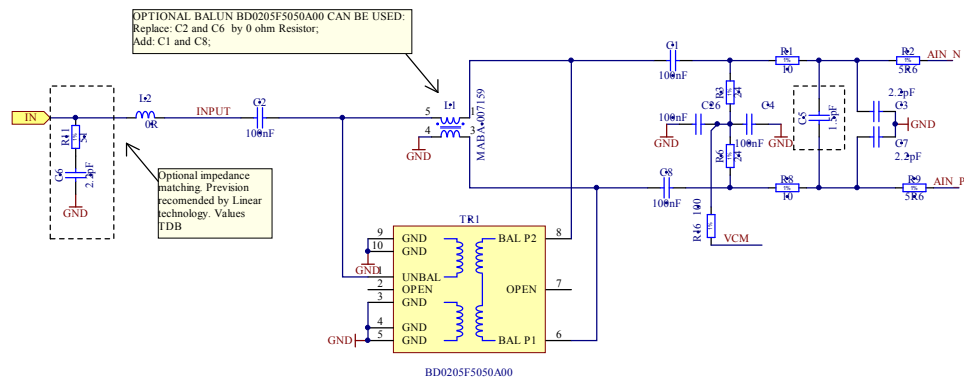


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Project/Equipment		FMC_130MSPS_4CH	
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DivGrp		CROSTEC	
Instruments S.A.		Crostec Instruments S.A.	
Designer			
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Last Mod		2014-08-20	
File		FMC 130MSPS 4CH.SchDoc	
Print Date		2014-08-20 14:09:04	
Sheet		1 of 8	
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CMOS OUTPUT MODE	MIN	TYP	MAX	UNITS
OVDD Output Supply Voltage (Note 8) 1	0.5	3.6	3.6	V
OVDD Analog Supply Current 1		380	450	mA
PDIS Power Dissipation 1		1250	1485	mW

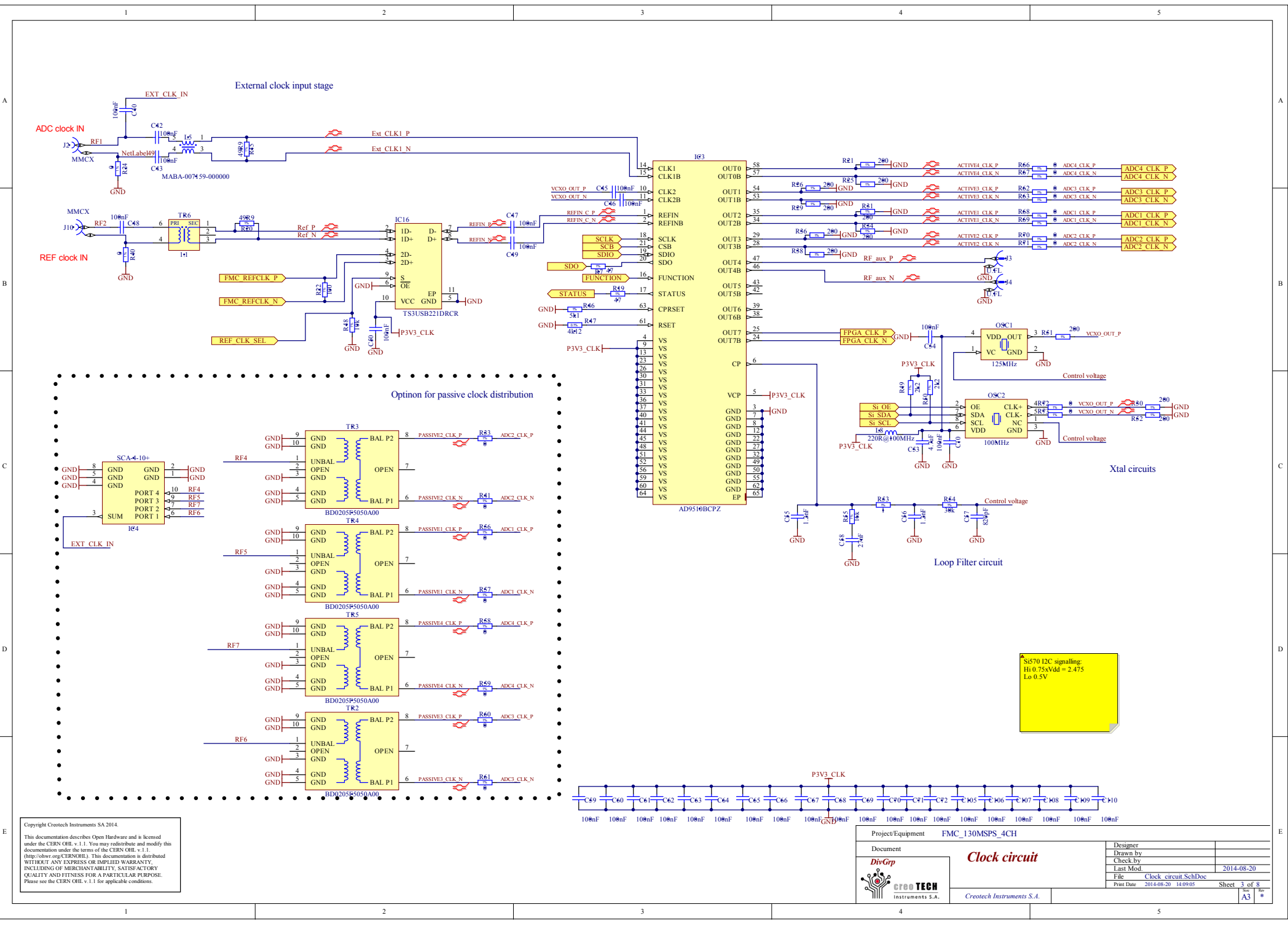
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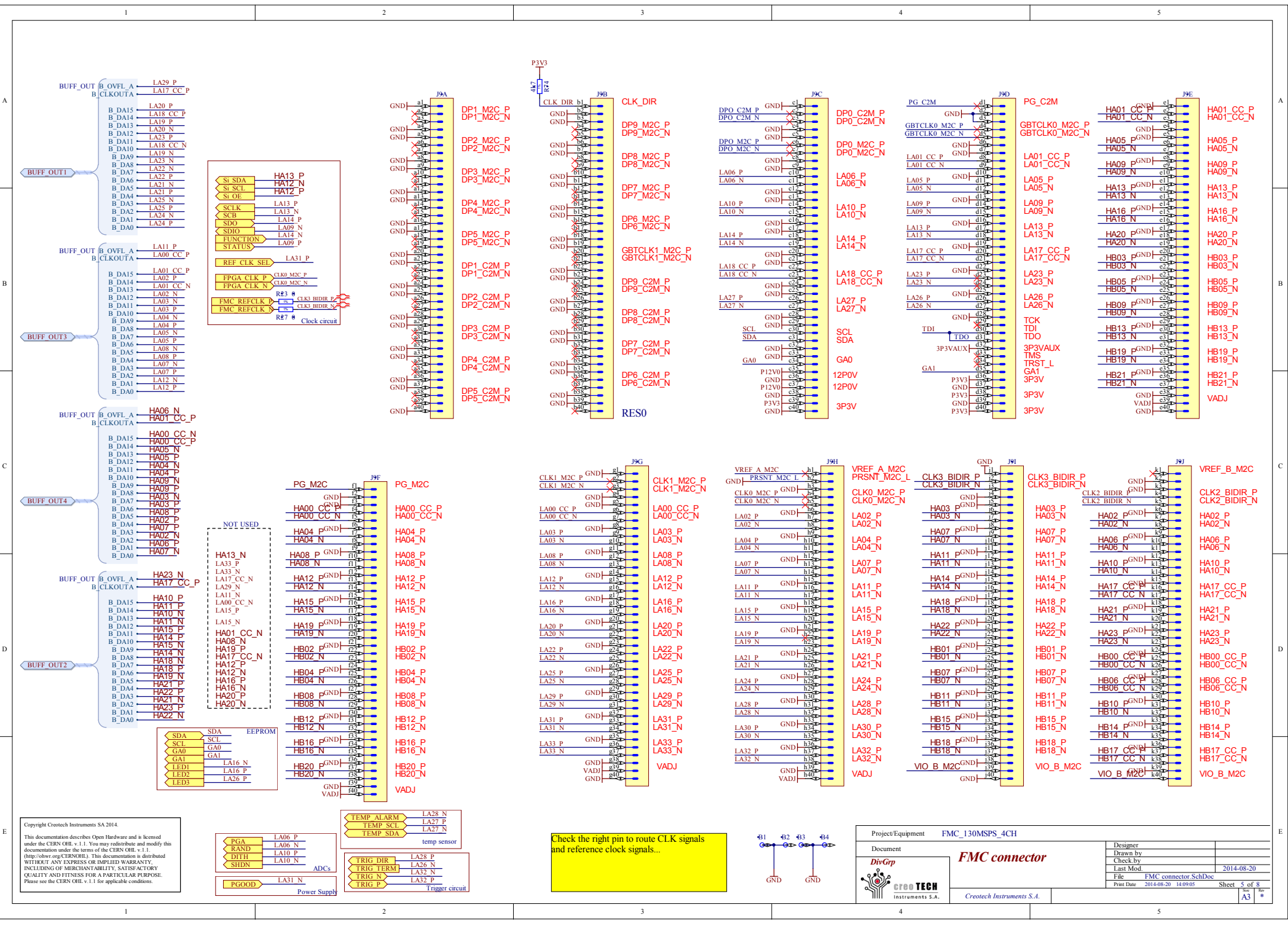
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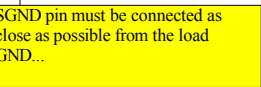







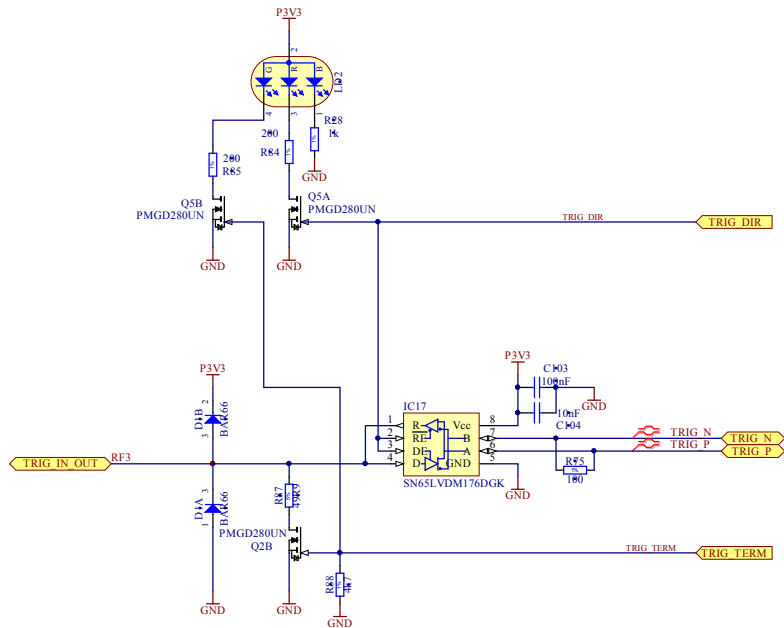
Power budget for 3.3V  
ADC #450mA = 1800mA  
clock distribution 500mA  
VCCXO 130mA  
sum: 2430mA

The TPS7A47 sense pin must be connected as close as possible from the load...





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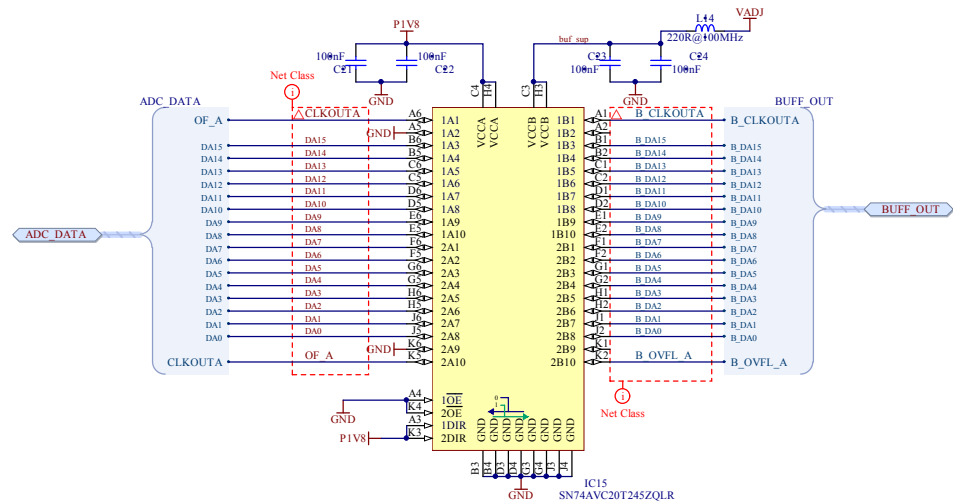
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Project/Equipment		FMC_130MSPS_4CH	
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Project/Equipment		FMC_130MSPS_4CH	
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		Last Mod	2014-08-20
		File	BUFFER_SchDec
Print Date		2014-08-20 14:09:06	Sheet 8 of 8
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