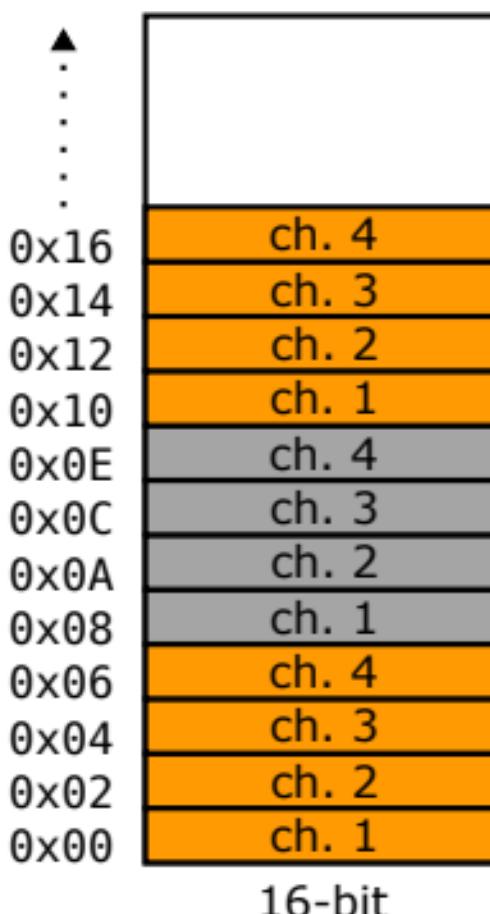


Samples write (from ADC core)



DDR memory



Samples read (from host)

