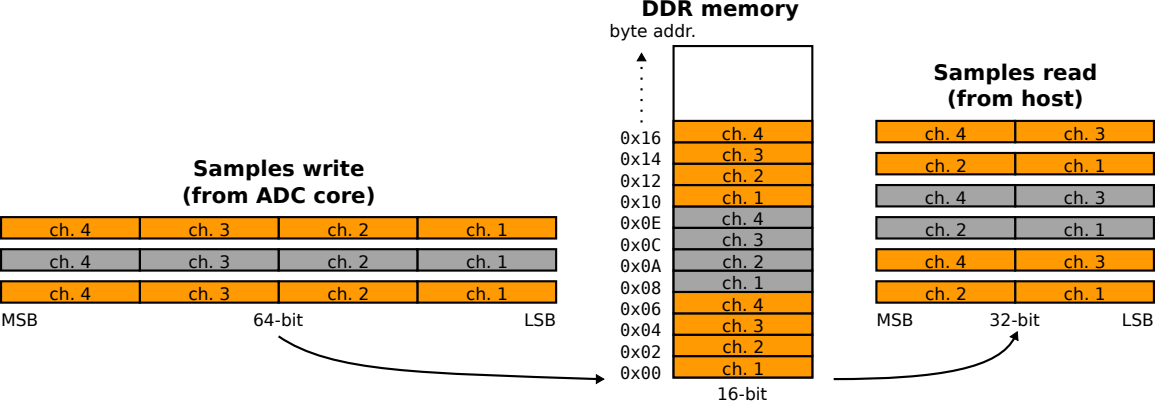
DDR memory
byte addr.
Samples write
(from ADC core)
ch. 4
ch. 3
ch. 2
ch. 1
MSB
64-bit
LSB
0x16
0x14
0x12
0x10
0x0E
0x0C
0x0A
0x08
0x06
0x04
0x02
0x00
16-bit
Samples read
(from host)
32-bit