

MC ADC mezzanine  
(hardware board)

- ADC datapath
- Analogue front-end

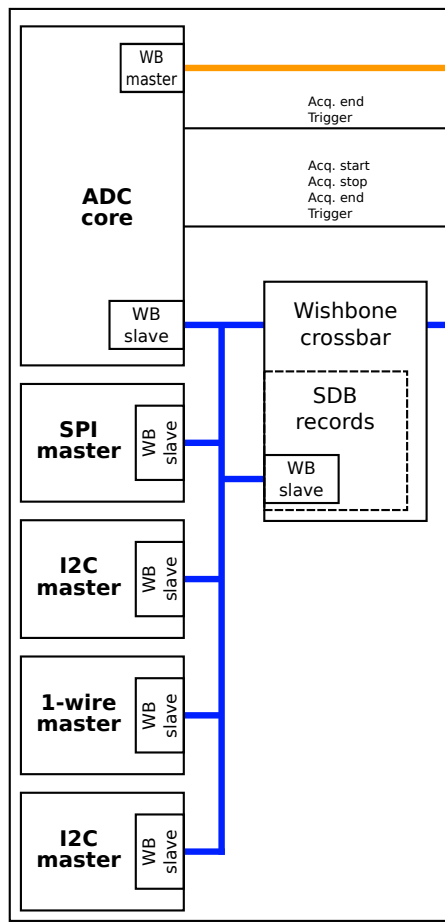
- DC offset DAC
- ADC control

- Clock control (Si570)

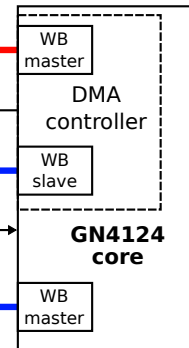
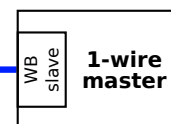
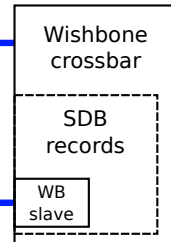
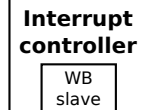
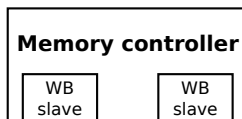
- Thermometer Unique ID (mezzanine)

- EEPROM

## FMC ADC mezzanine (hdl module)



256MB DDR3 memory



GN4124  
PCIe bridge

- LEDs (carrier)
- PLL, DDR, status

- Thermometer Unique ID (carrier)