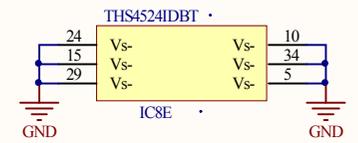
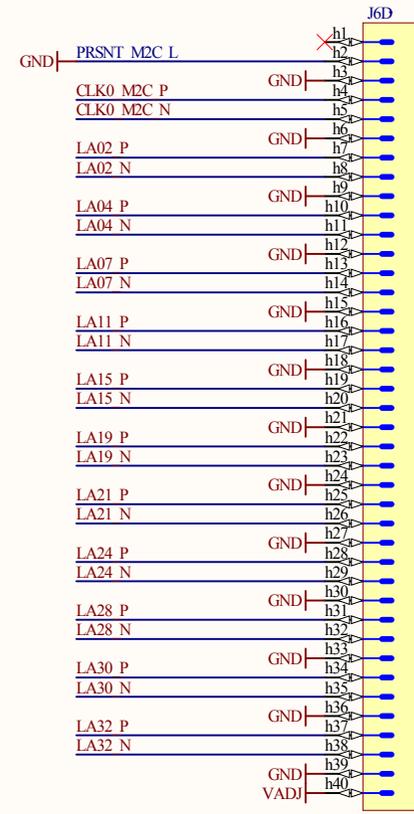
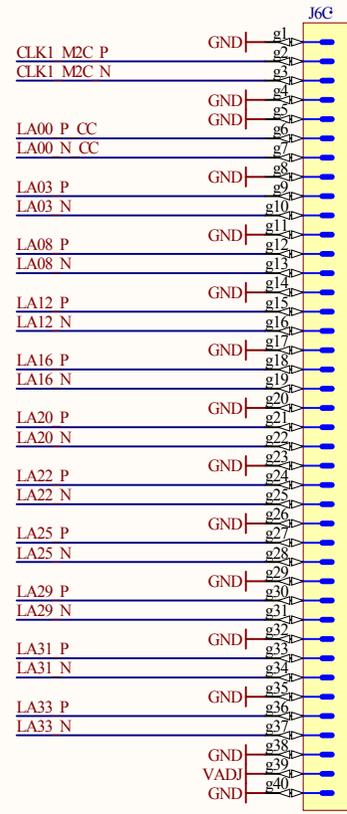
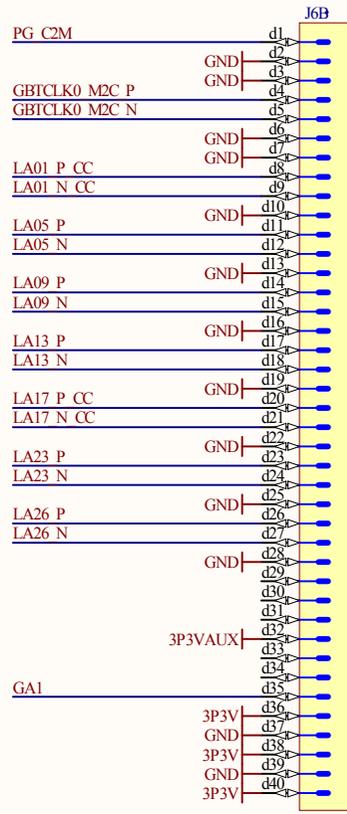
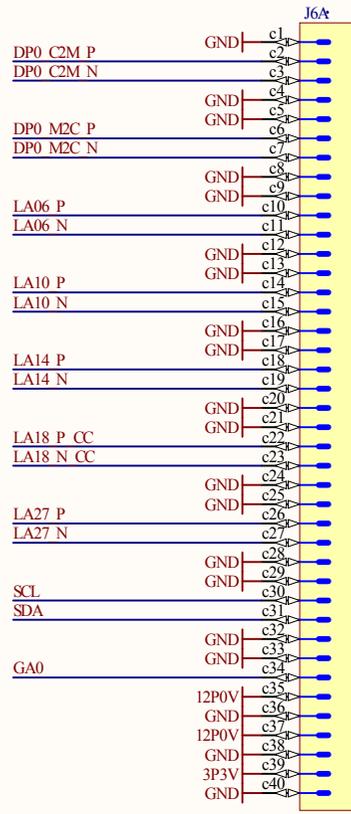


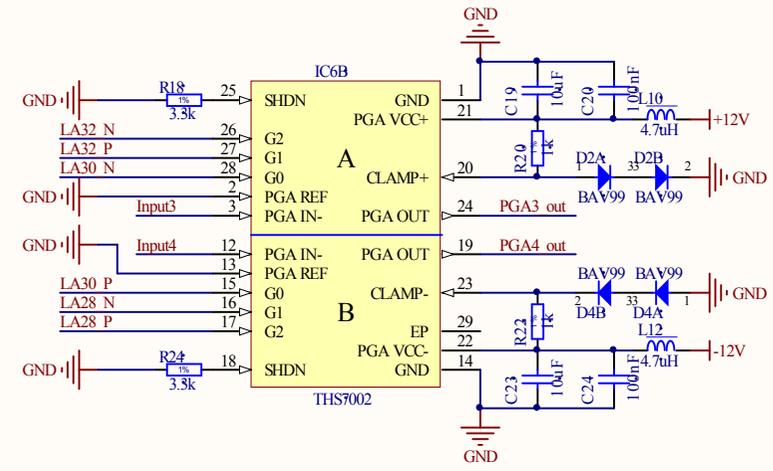
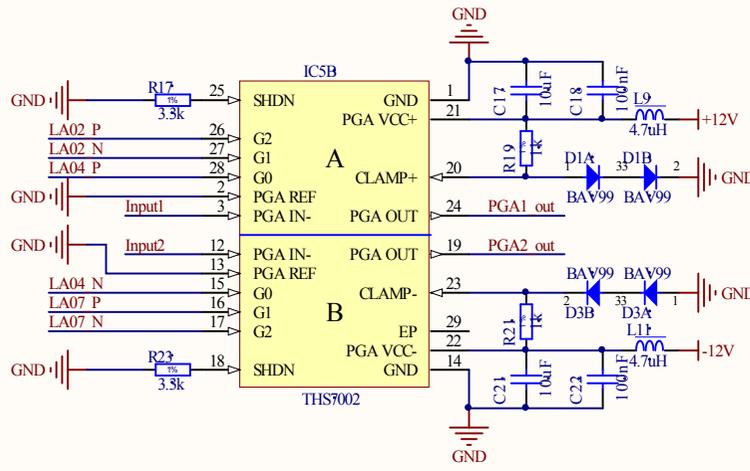
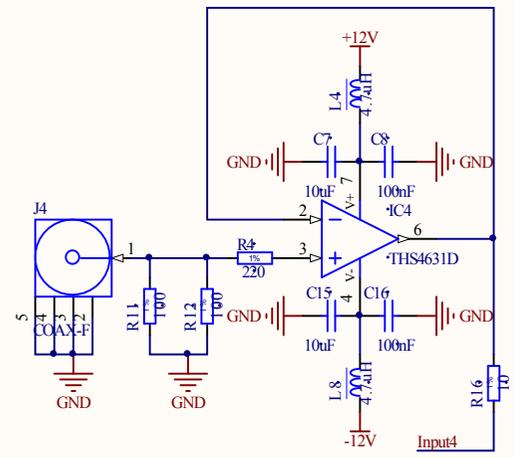
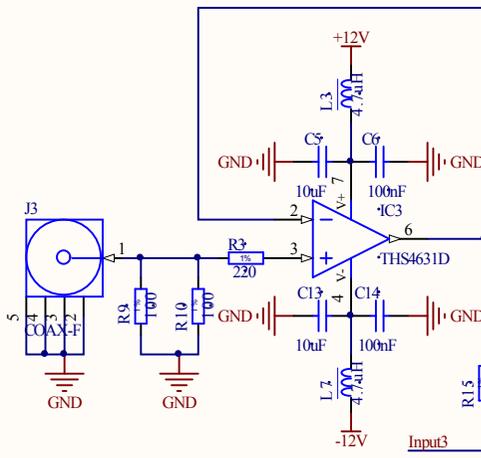
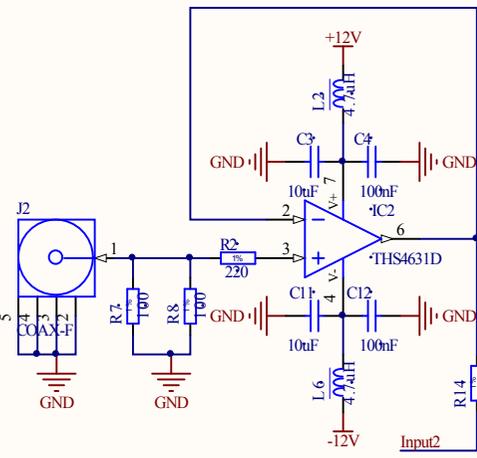
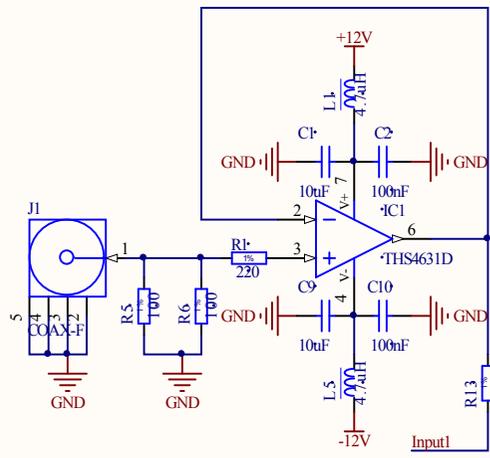
CC postfix lines are used as clocklines - in the spec, there is a note, that they can be used as a clock lines for synchronous applications.



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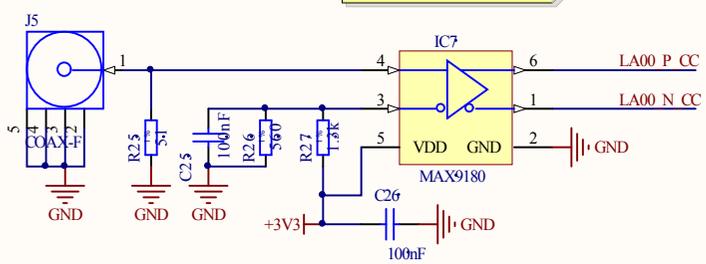


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▲ Trigger receiver. Trigger level can be adjusted by changing R26 and R27. Now: 1V (OK for 5V/3.3V TTL with 50Ohm termination)

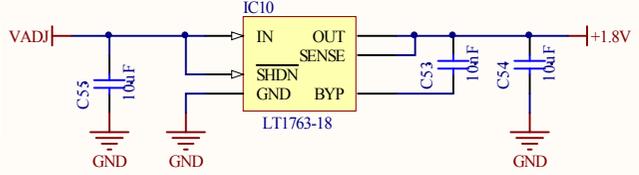
▲ BAV99 diodes clamp the output of the PGA and therefore don't allow the next stage to be deeply saturated



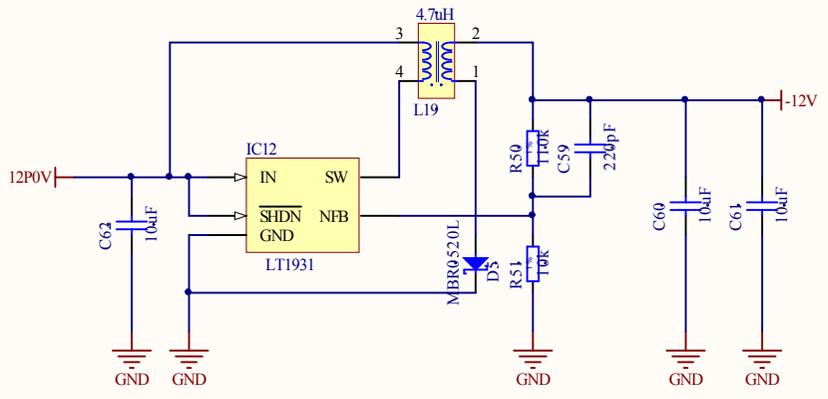
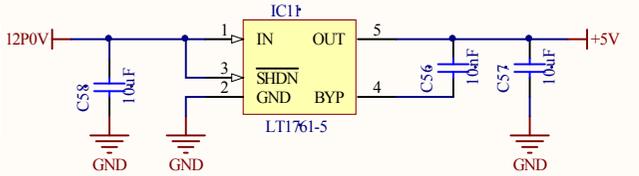
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VADJ has to be set to 2.5V
The linear regulator is added here for compatibility with XILINX kits



Linear regulator for creating +5V to power differential amplifier set



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