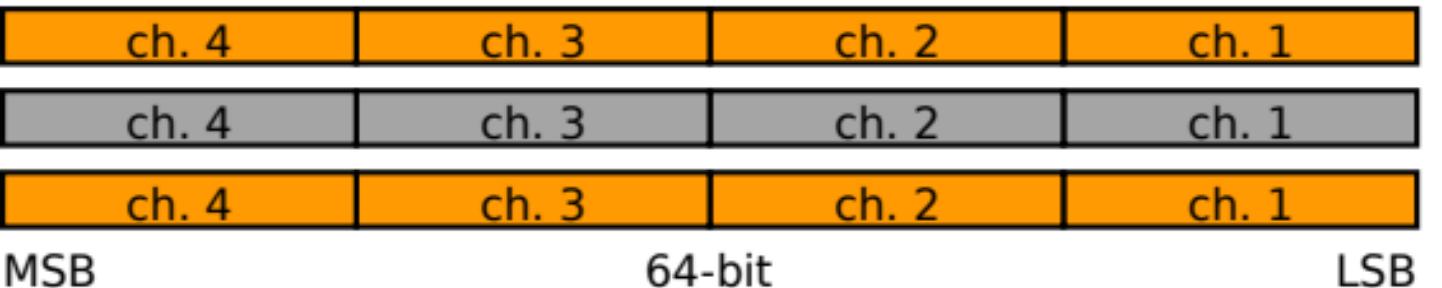


## Samples write (from ADC core)



## **DDR memory**

byte addr.

0x16	ch. 4
0x14	ch. 3
0x12	ch. 2
0x10	ch. 1
0x0E	ch. 4
0x0C	ch. 3
0x0A	ch. 2
0x08	ch. 1
0x06	ch. 4
0x04	ch. 3
0x02	ch. 2
0x00	ch. 1

## Samples read (from GN4124 core)

