

DUTx = CONV-TTL-BLO device-under-test in slot number x (first crate)

PR = Pulse Repater (8-chan)

CTBx = CONV-TTL-BLO in slot number x (second crate)

From	To	Remarks
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TTL to BLO system		
DUT1 CH1 TTL OUT	CTB1 CH1 TTL IN	Set DUT and CTB in TTL/TTL-BAR repetition mode
DUT1 CH2 BLO OUT	CTB1 CH2 BLO IN	
DUT1 CH3 TTL OUT	PR CH1 IN + 50ohm	Set PR CH1 in TTL/TTL-BAR mode
PR CH1 OUT	CTB1 CH3 BLO IN	
DUT1 CH4 BLO OUT	PR CH2 IN + 50ohm	Set PR CH1 in BLO mode
PR CH2 OUT	CTB1 CH4 BLO IN	
DUT1 CH5 TTL OUT	CTB1 CH5 TTL IN	
DUT1 CH6 BLO OUT	CTB1 CH6 BLO IN	

TTL to TTL system		
DUT2 CH1 TTL OUT	CTB2 CH1 TTL IN	Set DUT and CTB in TTL/TTL-BAR repetition mode
DUT2 CH2 TTL OUT	CTB2 CH2 TTL IN	
DUT2 CH3 TTL OUT	DUT2 CHA INV-TTL IN	Connect DUT CH6 OUT directly to CTB CH6 IN if glitch
DUT2 CHA INV-TTL OUT	DUT2 CHB INV-TTL IN	Connect DUT CH6 OUT directly to CTB CH6 IN if glitch
DUT2 CHB INV-TTL OUT	CTB2 CH3 TTL IN	
DUT2 CH4 TTL OUT	CTB2 CH4 TTL IN	
DUT2 CH5 TTL OUT	CTB2 CH5 TTL IN	
DUT2 CH6 TTL OUT	DUT2 CHC INV-TTL IN	Connect DUT CH6 OUT directly to CTB CH6 IN if glitch
DUT2 CHC INV-TTL OUT	DUT2 CHD INV-TTL IN	Connect DUT CH6 OUT directly to CTB CH6 IN if glitch
DUT2 CHD INV-TTL OUT	CTB2 CH6 TTL IN	