The pinout takes into account several facts:
1.- Return current in fast signals are more directive.
2.- Blocking output is considered preferential in terms of ease of layaout.
3.- Blocking input has less high spectral components than Blocking output.
4.- The leds signals are faster than the rest, so are the most directives.
5.- For improving coupled noise in the Blocking output, led trigger should be placed after the pulse has been already outputted.

This column is the reference for CONV-TTL-BLO modifications
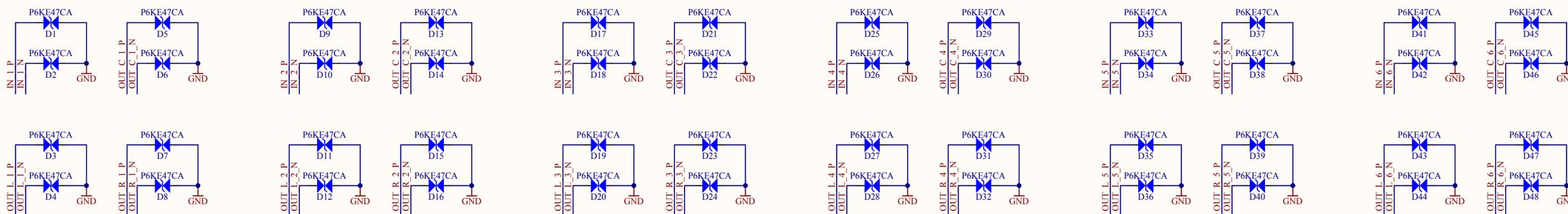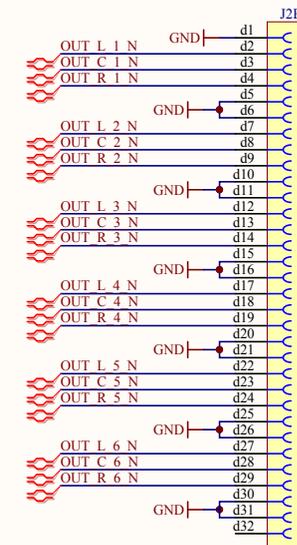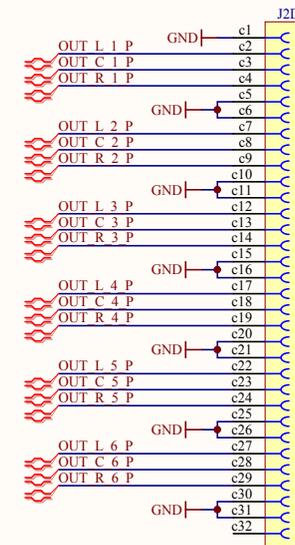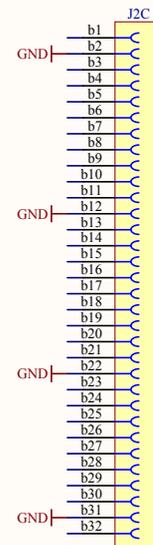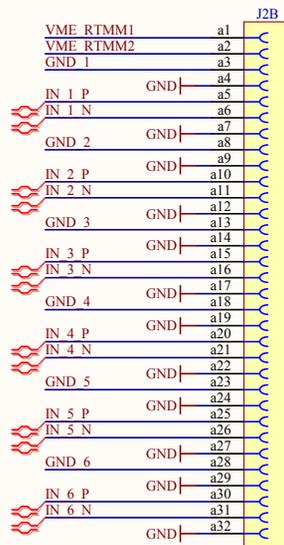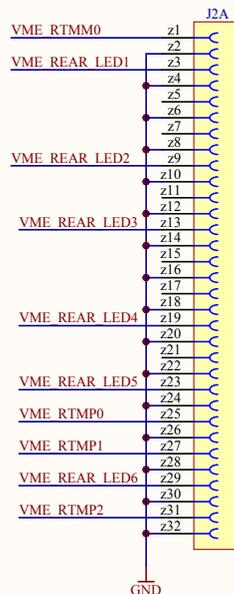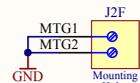
Rear Transition Module Motherboard

RTMM0   GND
RTMM1   Open
RTMM2   Open

VME_RTMM0
VME_RTMM1
VME_RTMM2

GND

J2A J2B J2C J2D J2E J2F

Mounting Holes

MTG1 MTG2

GND

Project/Equipment   -

Document

BE/CO

CERN

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CH-1211 Genève 23 - Switzerland

CONV-TTL-RTM
Motherboard
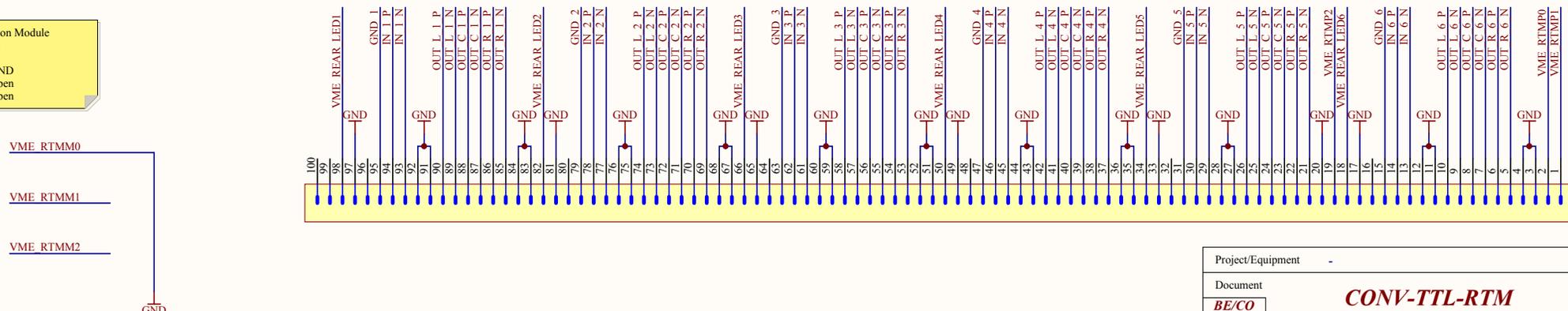
Designer	Carlos Gil Soriano	
Drawn by	Carlos Gil Soriano	05/12/2011
Check.by	C.Andouillet	12-02-2012
Last Mod.	Thedi Stana	23/04/2013
File	RTMM_top.SchDoc	
Print Date	23/04/2013 3:01:55 PM	Sheet 1 of 1

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