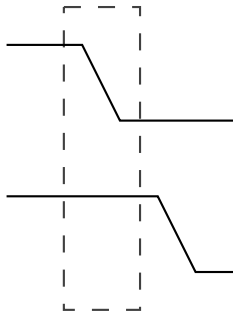
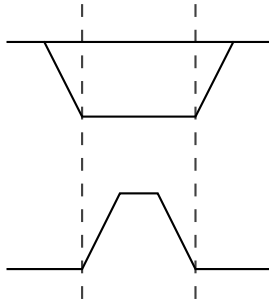


Start (S) or repeated start (Sr)
condition

SDA



Valid bit transfer



Stop condition (P)

