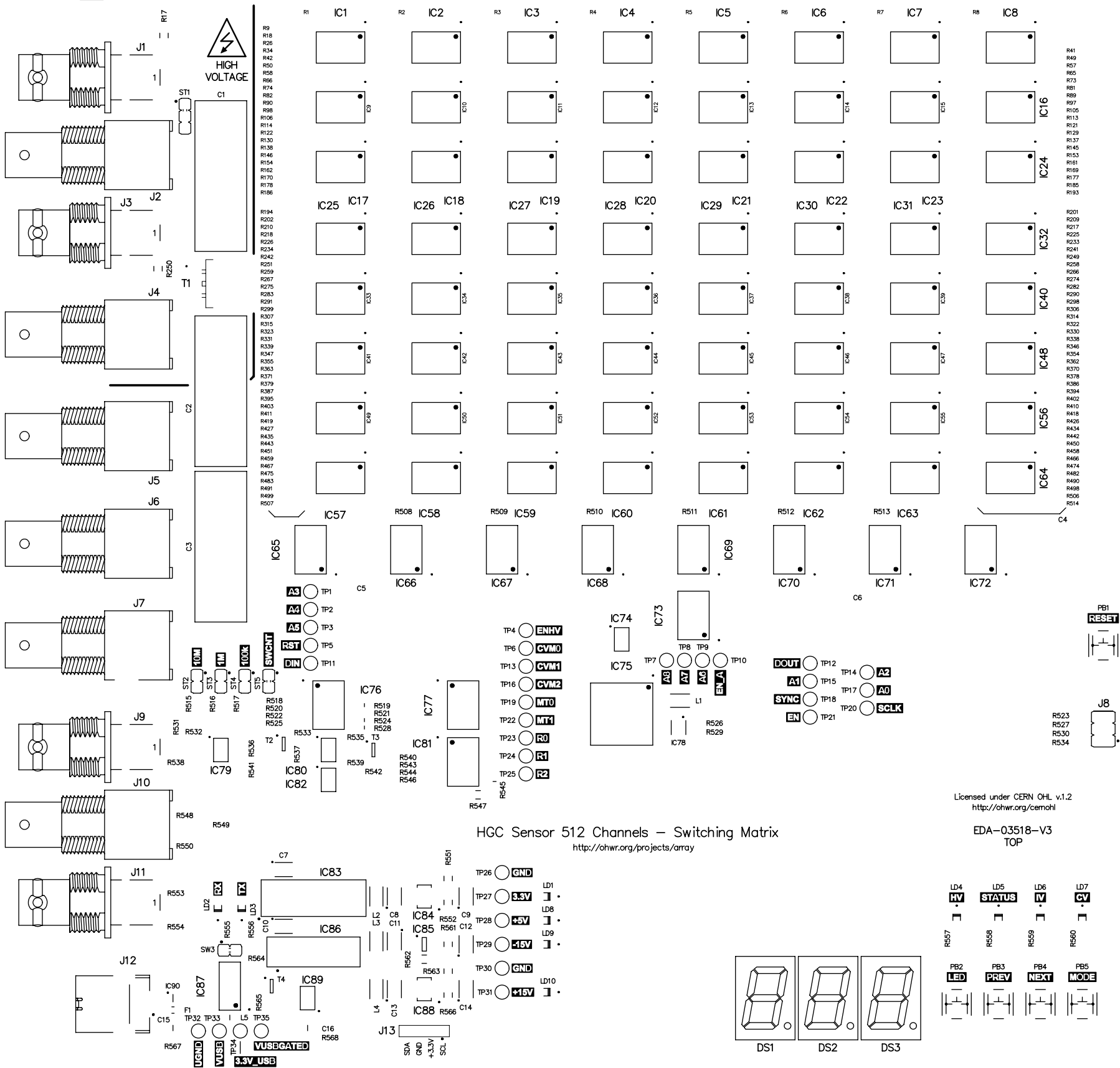


Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

Print Date 14/03/2019 15:36:37
SCALE: 1.00
Altium Designer Rev 17.1.9.592


Panelisation

ALL DIMENSIONS ARE IN MILLIMETERS
Dimensions are rounded to 0.1mm. Gerber data is the reference

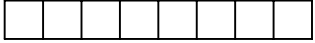


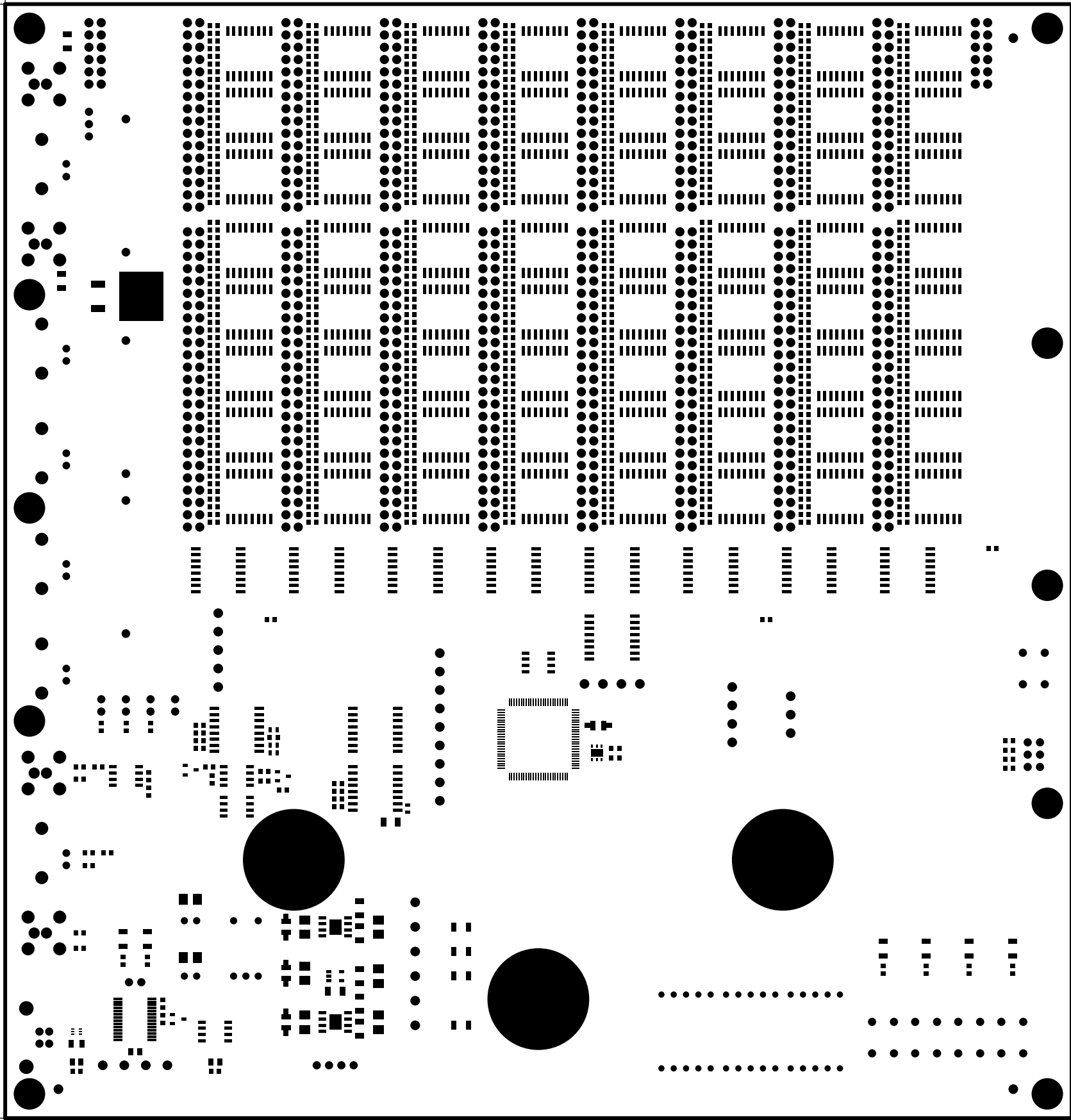
HGC Sensor 512 Channels – Switching Matrix
<http://ohwr.org/projects/array>

Licensed under CERN OHL v.1.2
<http://ohwr.org/cernohl>
EDA-03518-V3
TOP

	Number	EDA-03518-V3
	Drawn By	JMW
	DATE	28-10-2016
	MOD.V2	JMW 12-02-2019
Print Date		14/03/2019 15:36:17
SCALE:		1:00
Altium Designer Rev		17.1.9.592

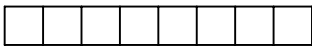
Top Overlay



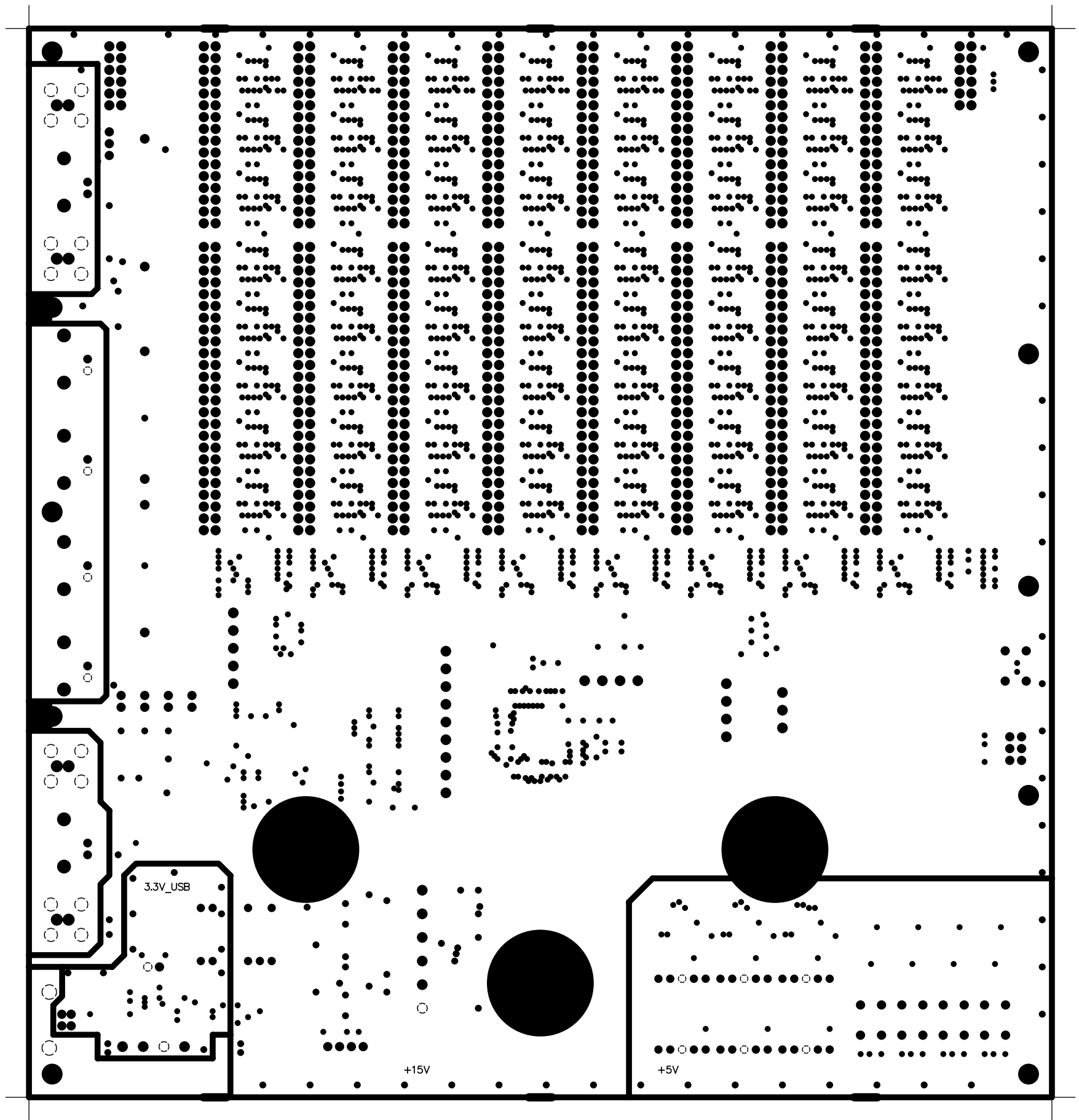


Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

Print Date 14/03/2019 15:36:20
SCALE: 1.00
Altium Designer Rev 17.1.9.592



Top Solder

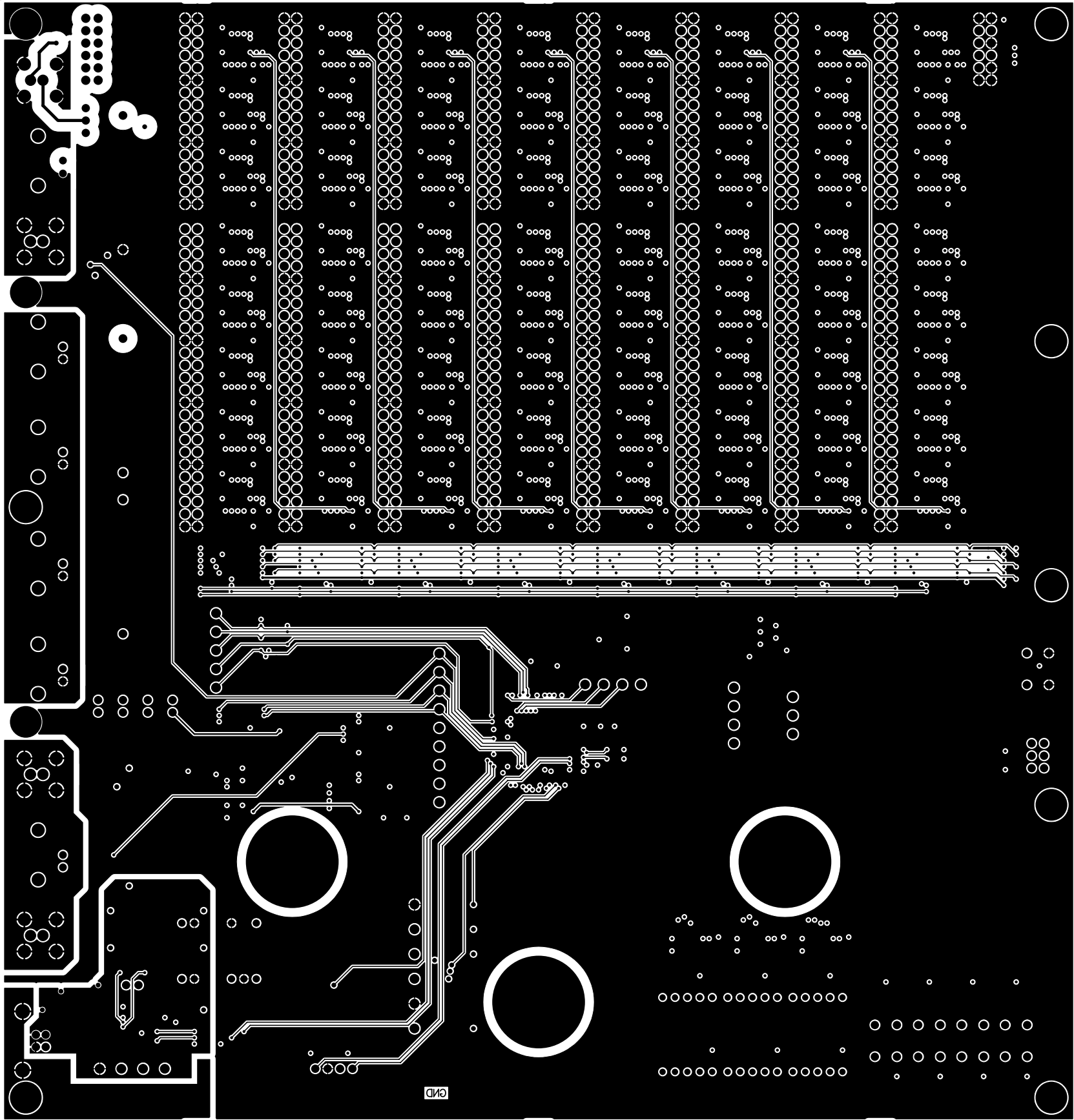


Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

Print Date 14/03/2019 15:36:03
SCALE: 1.00
Altium Designer Rev 17.1.9.592

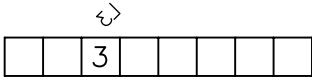
L2P

2

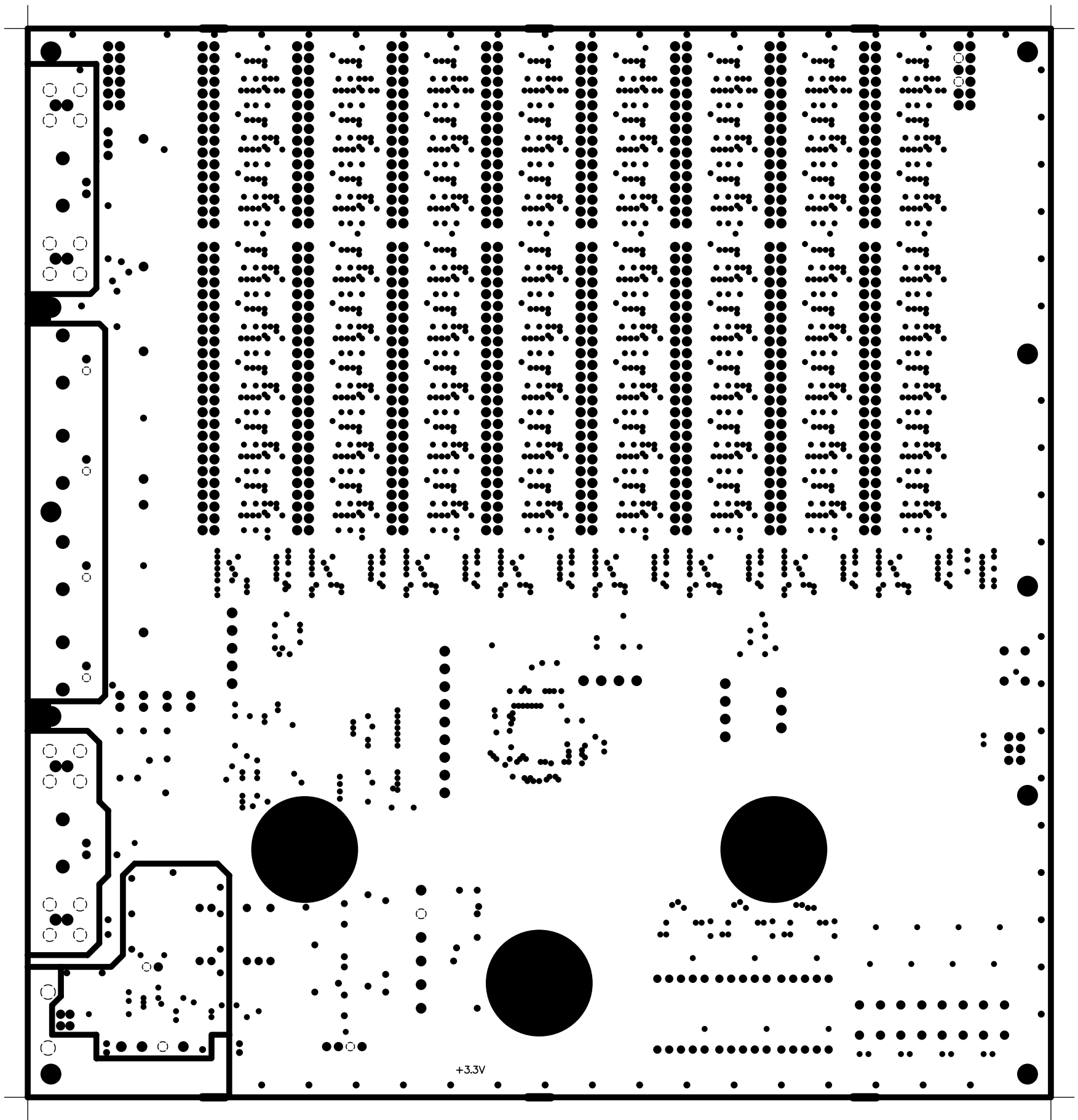


Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

Print Date 14/03/2019 15:36:04
SCALE: 1.00
Altium Designer Rev 17.1.9.592

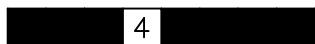


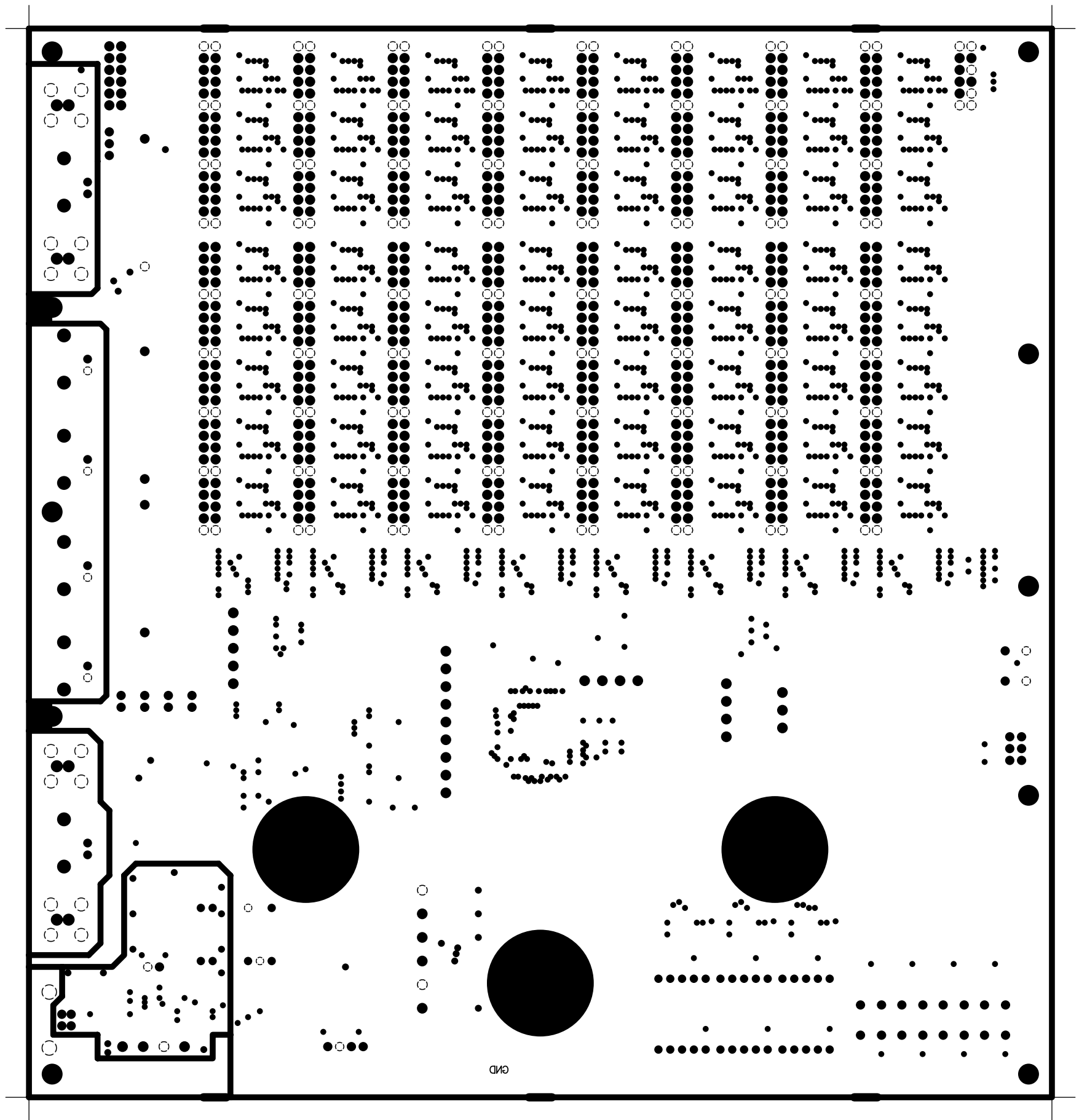
3



Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

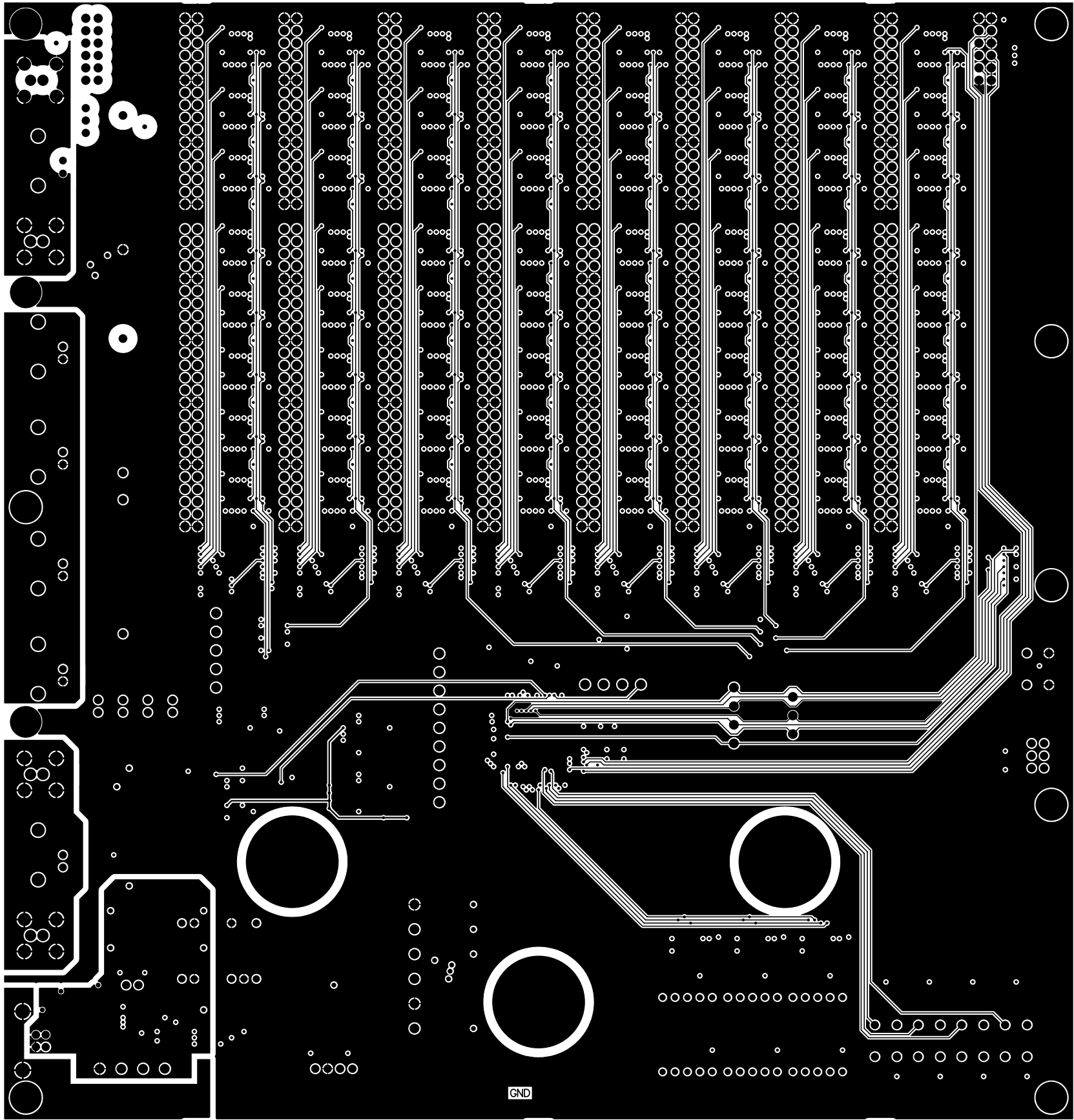
Print Date 14/03/2019 15:36:05
SCALE: 1.00
Altium Designer Rev 17.1.9.592





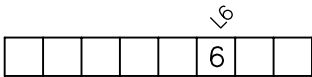
Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

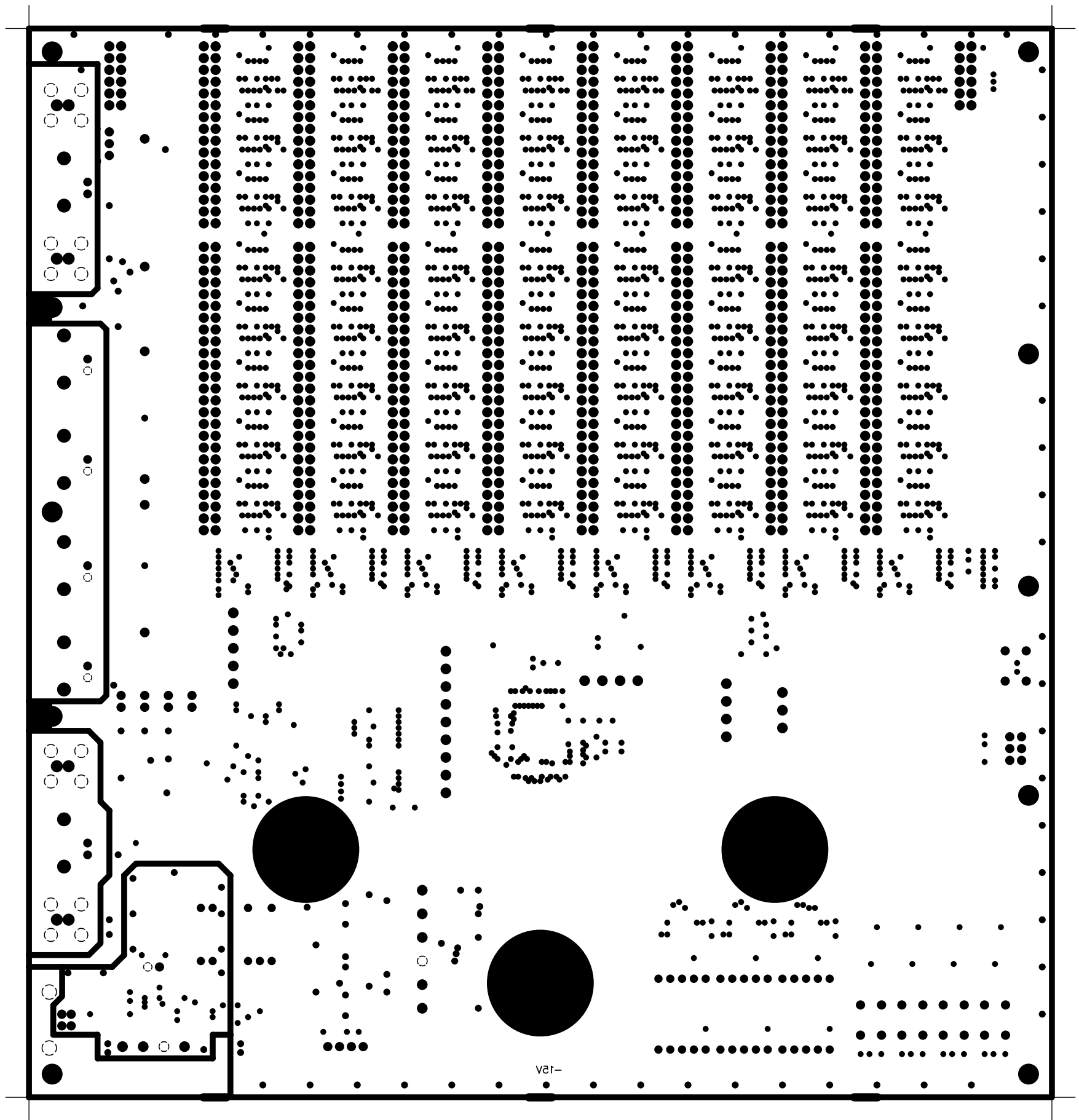
Print Date 14/03/2019 15:36:06
SCALE: 1.00
Altium Designer Rev 17.1.9.592



Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

Print Date 14/03/2019 15:36:09
SCALE: 1.00
Altium Designer Rev 17.1.9.592





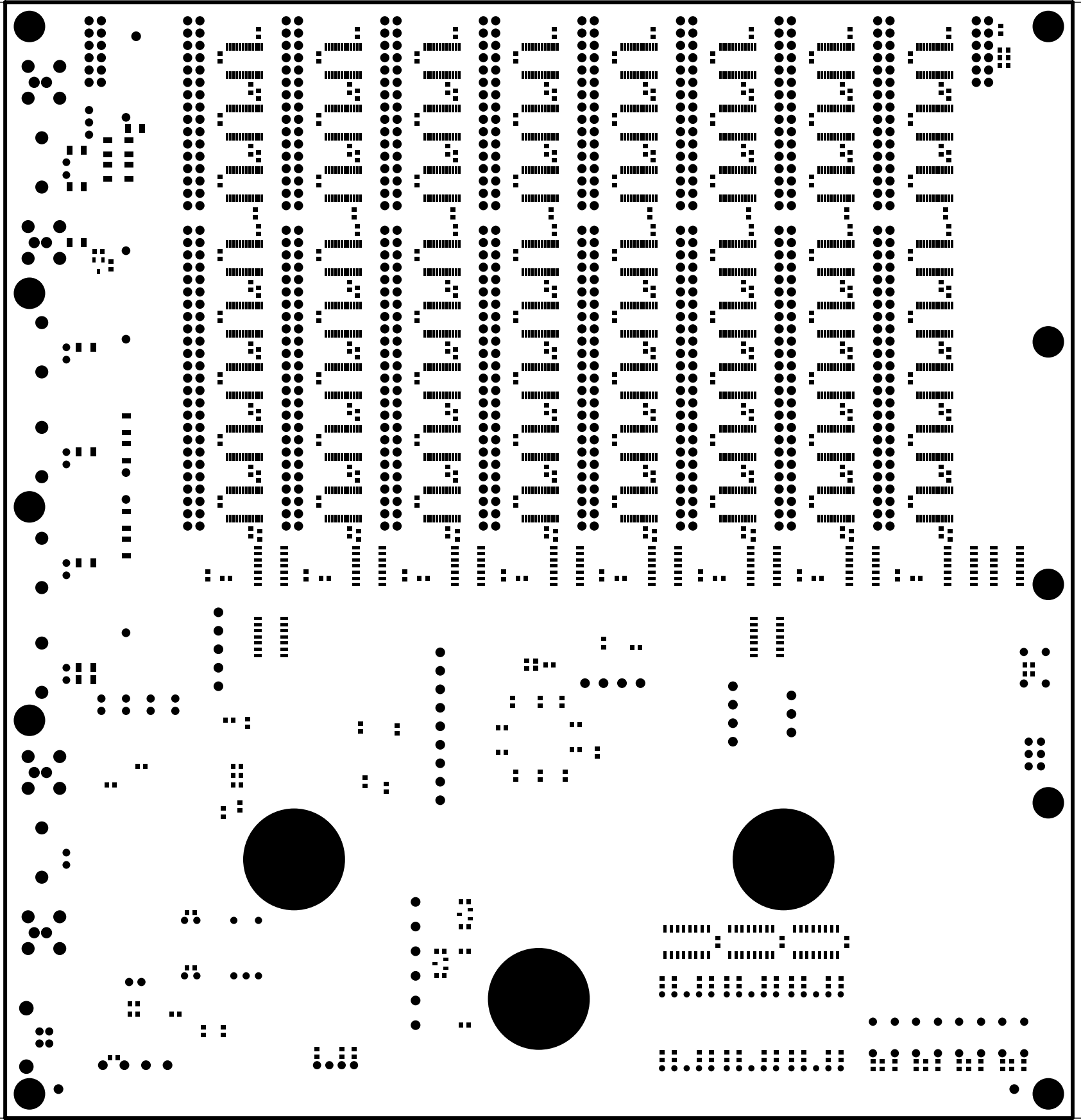
Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

Print Date 14/03/2019 15:36:10
 SCALE: 1.00
 Altium Designer Rev 17.1.9.592



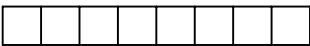
7

7/



Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

Print Date 14/03/2019 15:36:21
SCALE: 1.00
Altium Designer Rev 17.1.9.592



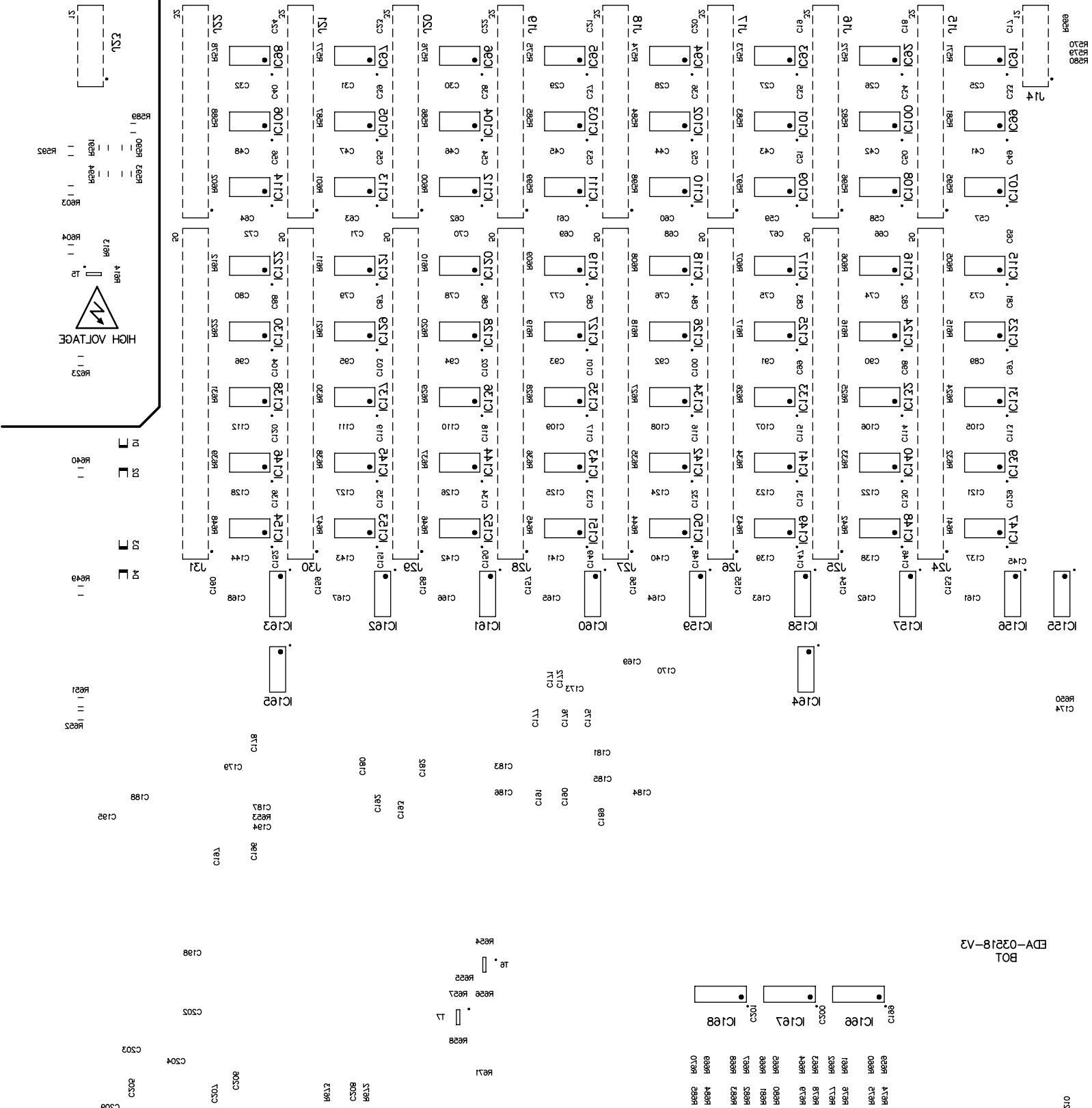
Bottom Solder

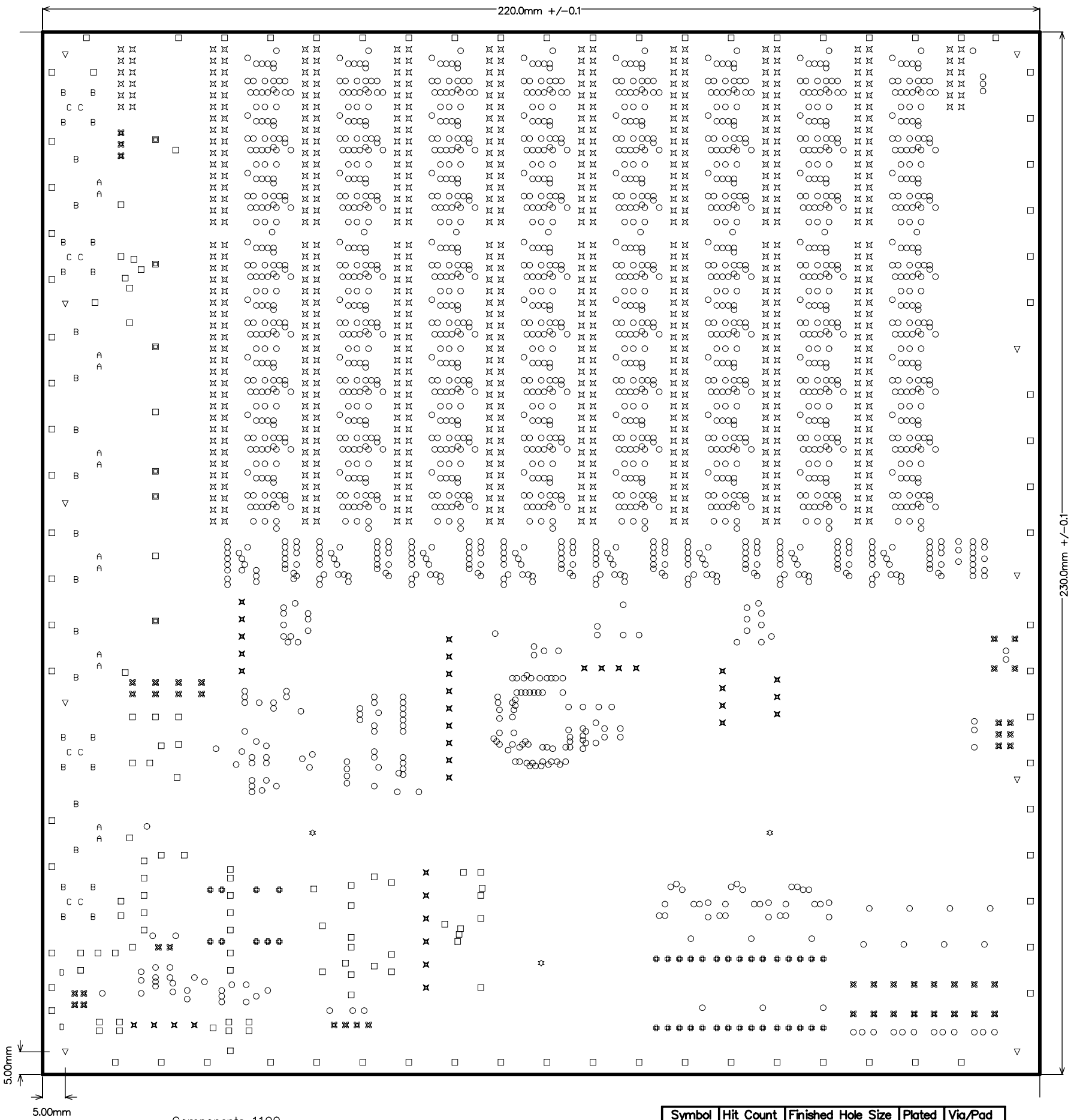


Number	EDA-03518-V3		
Drawn By	JMW		
DATE	28-10-2016		
MOD.V2	JMW 12-02-2019		
Print Date	14/03/2019	15:36:19	
SCALE:	1:00		
Altium Designer Rev	17.1.9.592		

--	--	--	--	--	--	--

Bottom Overlay



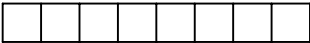


Components 1190
Pins 5955
Vias 2149
Holes 3019
Nets 1445



Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

Print Date 14/03/2019 15:36:32
SCALE: 1.00
Altium Designer Rev 17.1.9.592



Symbol	Hit Count	Finished Hole Size	Plated	Via/Pad
○	1995	0.30mm (11.81mil)	PTH	Via
□	154	0.50mm (19.69mil)	PTH	Via
⊕	39	0.80mm (31.50mil)	PTH	Pad
A	12	0.90mm (35.43mil)	PTH	Pad
⊞	47	1.00mm (39.37mil)	PTH	Pad
⊠	6	1.10mm (43.31mil)	PTH	Pad
⊞	680	1.20mm (47.24mil)	PTH	Pad
⊞	35	1.30mm (51.18mil)	PTH	Pad
C	8	1.60mm (62.99mil)	PTH	Pad
B	28	2.00mm (78.74mil)	PTH	Pad
D	2	2.30mm (90.55mil)	PTH	Pad
▽	10	3.50mm (137.80mil)	PTH	Pad
☆	3	20.00mm (787.40mil)	NPTH	Pad
3019 Total				

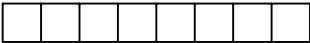
General tolerances +/-0.05mm unless otherwise stated





Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

Print Date 14/03/2019 15:36:17
SCALE: 1.00
Altium Designer Rev 17.1.9.592

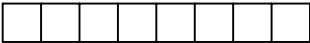


Top Paste



Number	EDA-03518-V3
Drawn By	JMW
DATE	28-10-2016
MOD.V2	JMW 12-02-2019

Print Date 14/03/2019 15:36:18
SCALE: 1.00
Altium Designer Rev 17.1.9.592



Bottom Paste